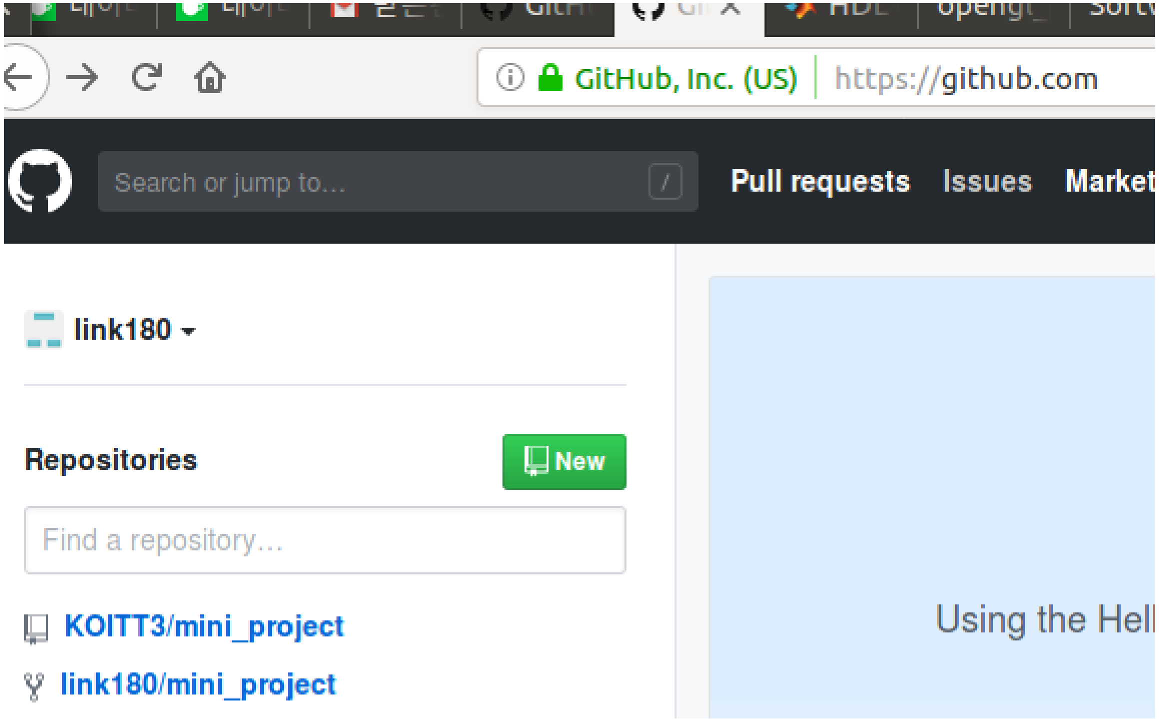
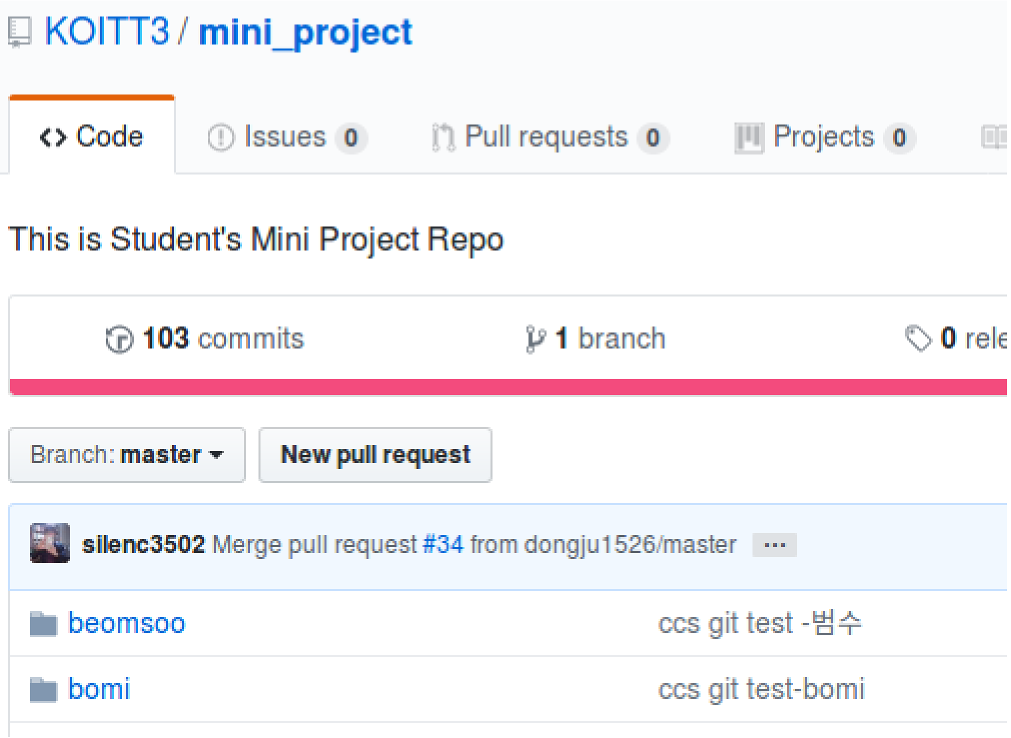
How to update git repo normally

Innova Lee(이상훈)
gcccompil3r@gmail.com

우선 현재는 아래와 같이 원본과 사본이 모두 있을 것이다.
그러므로 conflict(충돌) 가 발생했던 사람들은 먼저 conflict 에 대한 것을 해결하고 해당 문서를 추후에 열람하도록 한다.
conflict(충돌) 가 발생하지 않는다면 본 문서를 먼저 진행해도 무방하다.


GitHub, Inc. (US)
https://github.com
Search or jump to…
Pull requests
Issues
link180
Repositories
New
Find a repository…
KOITT3/mini_project
link180/mini_project

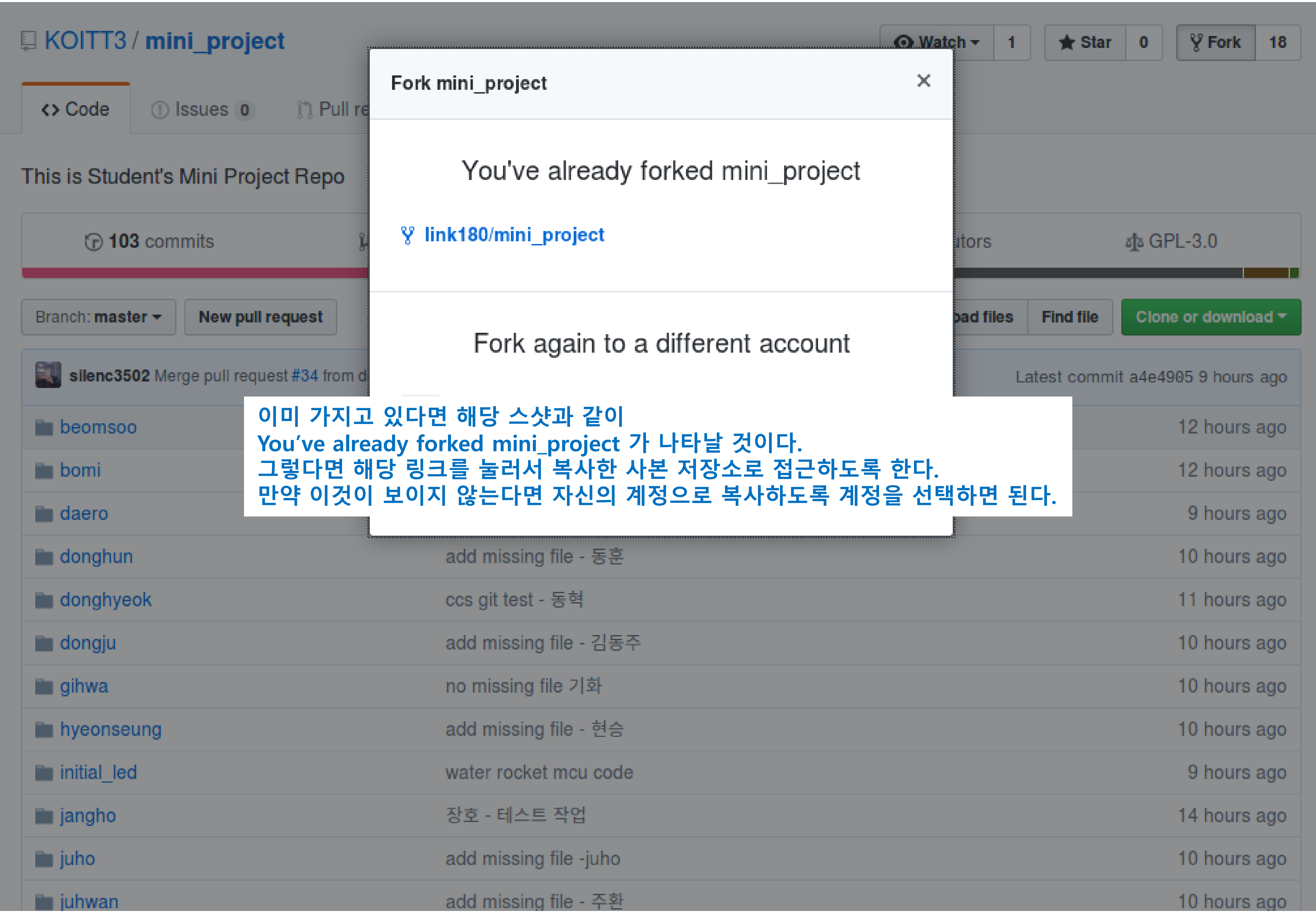
자신의 저장소를 찾지 못하겠다면 우선 원본을 찾아서 들어온다.
우측 상단에 fork 가 보일텐데 누른다.


KOITT3 / mini_project
Code
Issues 0
Pull requests 0
Projects 0
103 commits
1 branch
0 rele
Branch: master
New pull request

This is Student's Mini Project Repo

silenc3502 Merge pull request #34 from dongju1526/master

beomsoo	ccs git test -범수
bomi	ccs git test-bomi


KOITT3 / mini_project
Watch 1
Star 0
Fork 18
Code
Issues 0
Pull re
This is Student's Mini Project Repo
103 commits
tors
GPL-3.0
Branch: master
New pull request
ad files
Find file
Clone or download
silenc3502 Merge pull request #34 from d
Latest commit a4e4905 9 hours ago

Fork mini_project

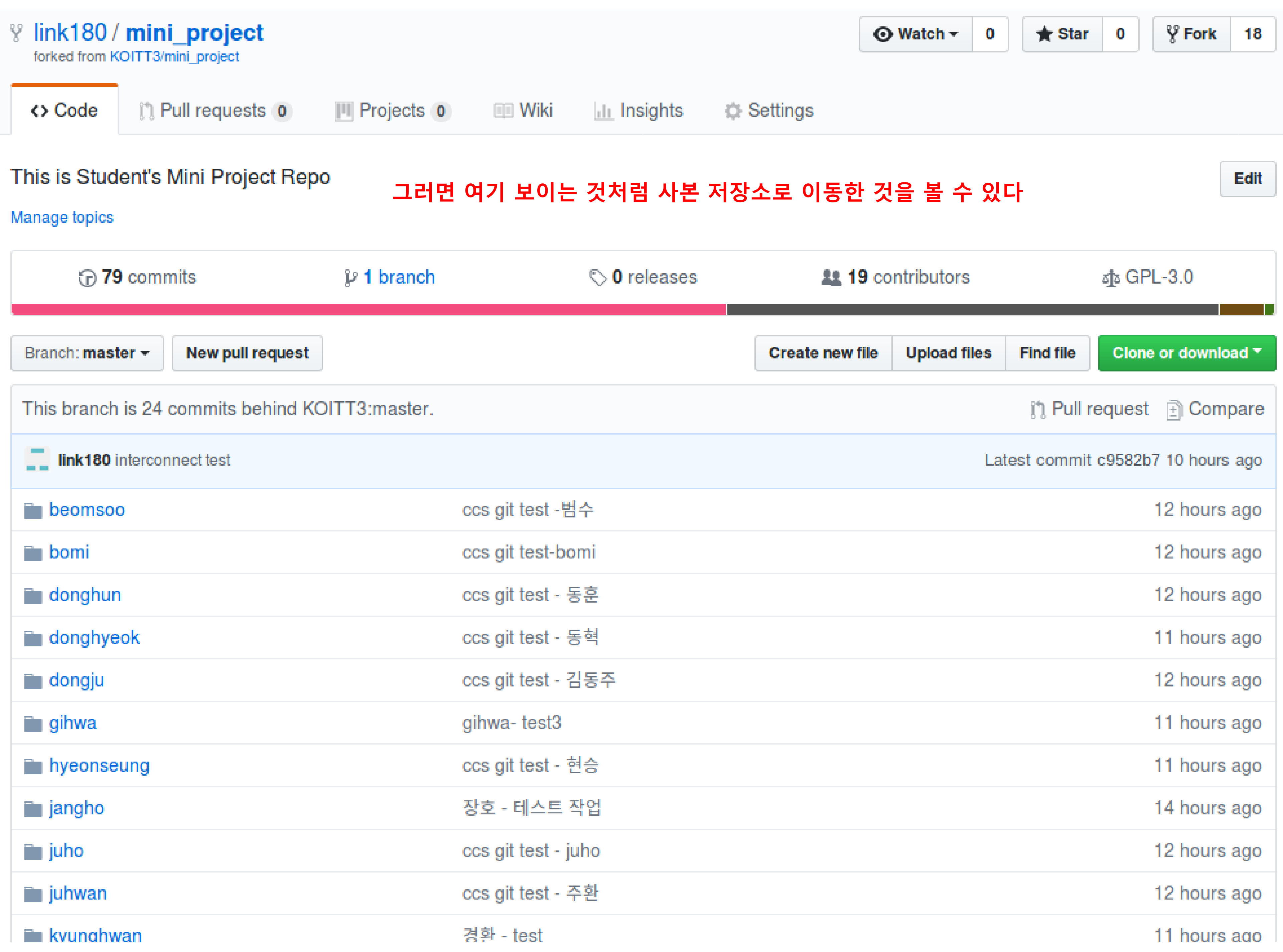
You've already forked mini_project

link180/mini_project

Fork again to a different account

beomsoo		12 hours ago
bomi		12 hours ago
daero		9 hours ago
donghun	add missing file - 동훈	10 hours ago
donghyeok	ccs git test - 동혁	11 hours ago
dongju	add missing file - 김동주	10 hours ago
gihwa	no missing file 기화	10 hours ago
hyeonseung	add missing file - 현승	10 hours ago
initial_led	water rocket mcu code	9 hours ago
jangho	장호 - 테스트 작업	14 hours ago
juho	add missing file -juho	10 hours ago
juhwan	add missing file - 주환	10 hours ago

이미 가지고 있다면 해당 스샷과 같이
You've already forked mini_project 가 나타날 것이다.
그렇다면 해당 링크를 눌러서 복사한 사본 저장소로 접근하도록 한다.
만약 이것이 보이지 않는다면 자신의 계정으로 복사하도록 계정을 선택하면 된다.


link180 / mini_project
forked from KOITT3/mini_project
Watch 0
Star 0
Fork 18
Code
Pull requests 0
Projects 0
Wiki
Insights
Settings
Edit
Branch: master
New pull request
Create new file
Upload files
Find file
Clone or download
Pull request
Compare

This is Student's Mini Project Repo

그러면 여기 보이는 것처럼 사본 저장소로 이동한 것을 볼 수 있다

Manage topics

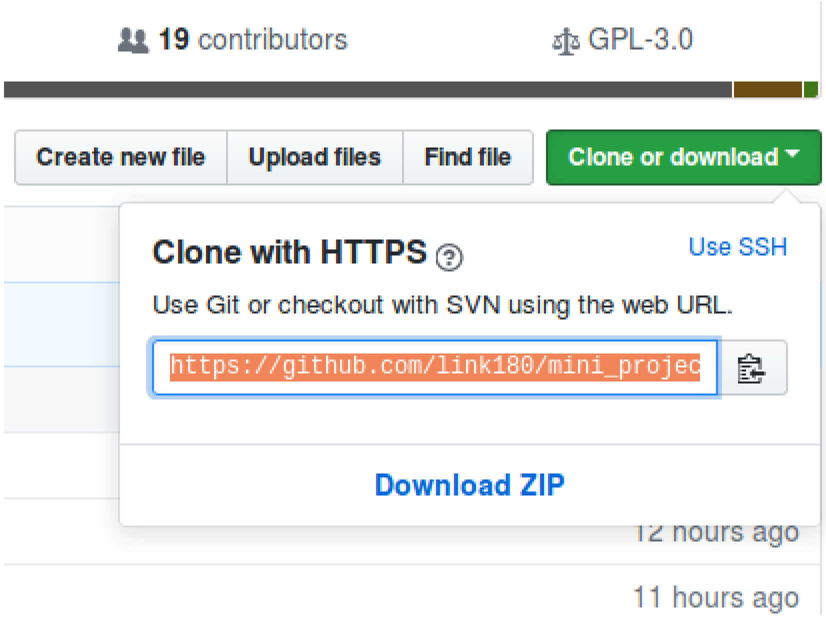
79 commits · 1 branch · 0 releases · 19 contributors · GPL-3.0

This branch is 24 commits behind KOITT3:master.

link180 interconnect test — Latest commit c9582b7 10 hours ago

beomsoo	ccs git test -범수	12 hours ago
bomi	ccs git test-bomi	12 hours ago
donghun	ccs git test - 동훈	12 hours ago
donghyeok	ccs git test - 동혁	11 hours ago
dongju	ccs git test - 김동주	12 hours ago
gihwa	gihwa- test3	11 hours ago
hyeonseung	ccs git test - 현승	11 hours ago
jangho	장호 - 테스트 작업	14 hours ago
juho	ccs git test - juho	12 hours ago
juhwan	ccs git test - 주환	12 hours ago
kyunghwan	경환 - test	11 hours ago

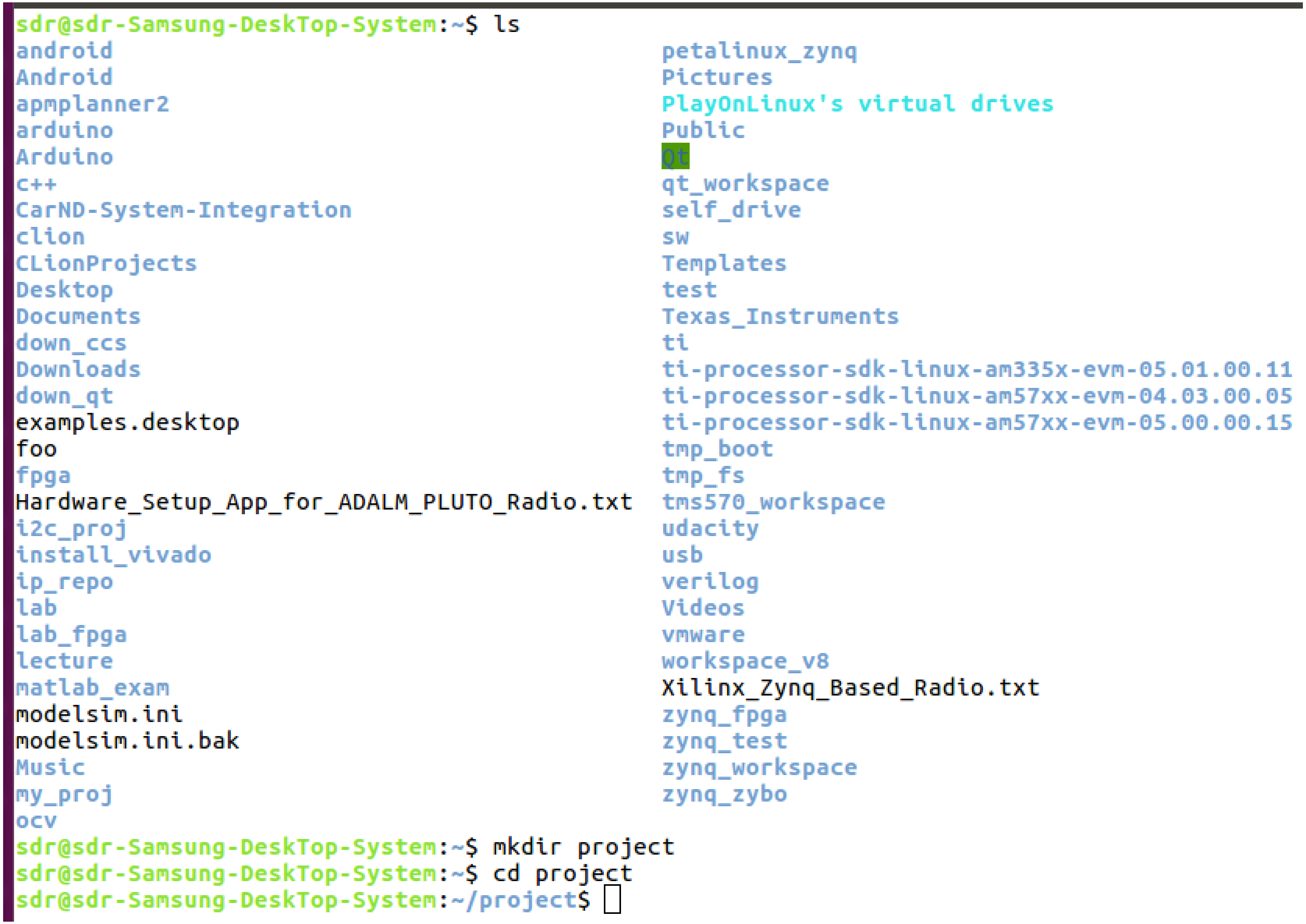
사본의 Clone or download 를 눌러보면 아래와 같이 git 링크를 복사할 수 있다.


19 contributors
GPL-3.0
Create new file
Upload files
Find file
Clone or download
Use SSH
Download ZIP
12 hours ago
11 hours ago

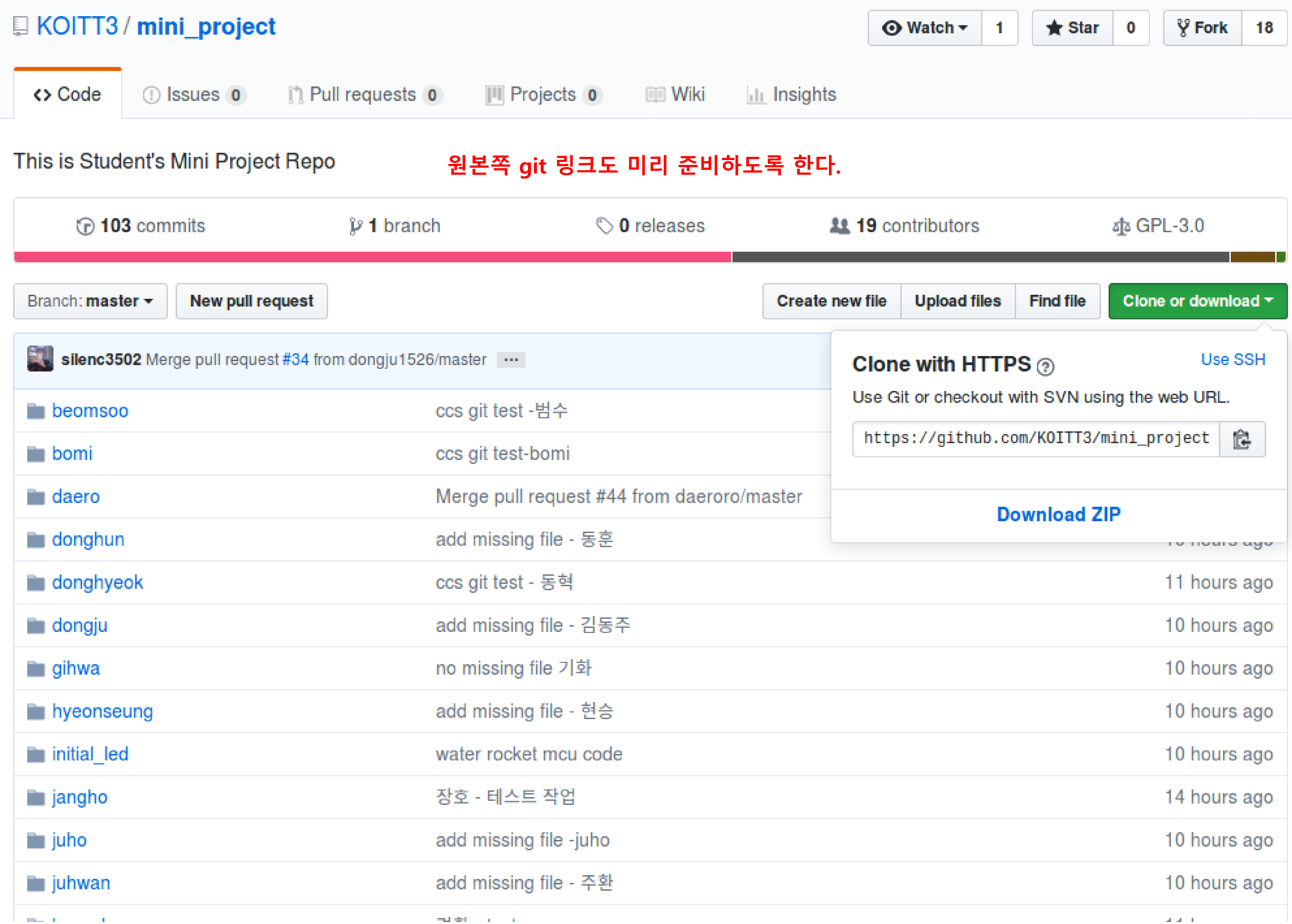
Clone with HTTPS

Use Git or checkout with SVN using the web URL.

`https://github.com/link180/mini_projec`



```
sdr@sdr-Samsung-DeskTop-System:~$ ls
android                                       petalinux_zynq
Android                                       Pictures
apmplanner2                                   PlayOnLinux's virtual drives
arduino                                       Public
Arduino                                       Qt
c++                                           qt_workspace
CarND-System-Integration                      self_drive
clion                                         sw
CLionProjects                                 Templates
Desktop                                       test
Documents                                     Texas_Instruments
down_ccs                                      ti
Downloads                                     ti-processor-sdk-linux-am335x-evm-05.01.00.11
down_qt                                       ti-processor-sdk-linux-am57xx-evm-04.03.00.05
examples.desktop                              ti-processor-sdk-linux-am57xx-evm-05.00.00.15
foo                                           tmp_boot
fpga                                          tmp_fs
Hardware_Setup_App_for_ADALM_PLUTO_Radio.txt  tms570_workspace
i2c_proj                                      udacity
install_vivado                                usb
ip_repo                                       verilog
lab                                           Videos
lab_fpga                                      vmware
lecture                                       workspace_v8
matlab_exam                                   Xilinx_Zynq_Based_Radio.txt
modelsim.ini                                  zynq_fpga
modelsim.ini.bak                              zynq_test
Music                                         zynq_workspace
my_proj                                       zynq_zybo
ocv
sdr@sdr-Samsung-DeskTop-System:~$ mkdir project
sdr@sdr-Samsung-DeskTop-System:~$ cd project
sdr@sdr-Samsung-DeskTop-System:~/project$ 
```



KOITT3 / mini_project
Watch 1
Star 0
Fork 18
Code
Issues 0
Pull requests 0
Projects 0
Wiki
Insights

This is Student's Mini Project Repo

원본쪽 git 링크도 미리 준비하도록 한다.

103 commits
1 branch
0 releases
19 contributors
GPL-3.0
Branch: master
New pull request
Create new file
Upload files
Find file
Clone or download

silenc3502 Merge pull request #34 from dongju1526/master

Clone with HTTPS

Use SSH

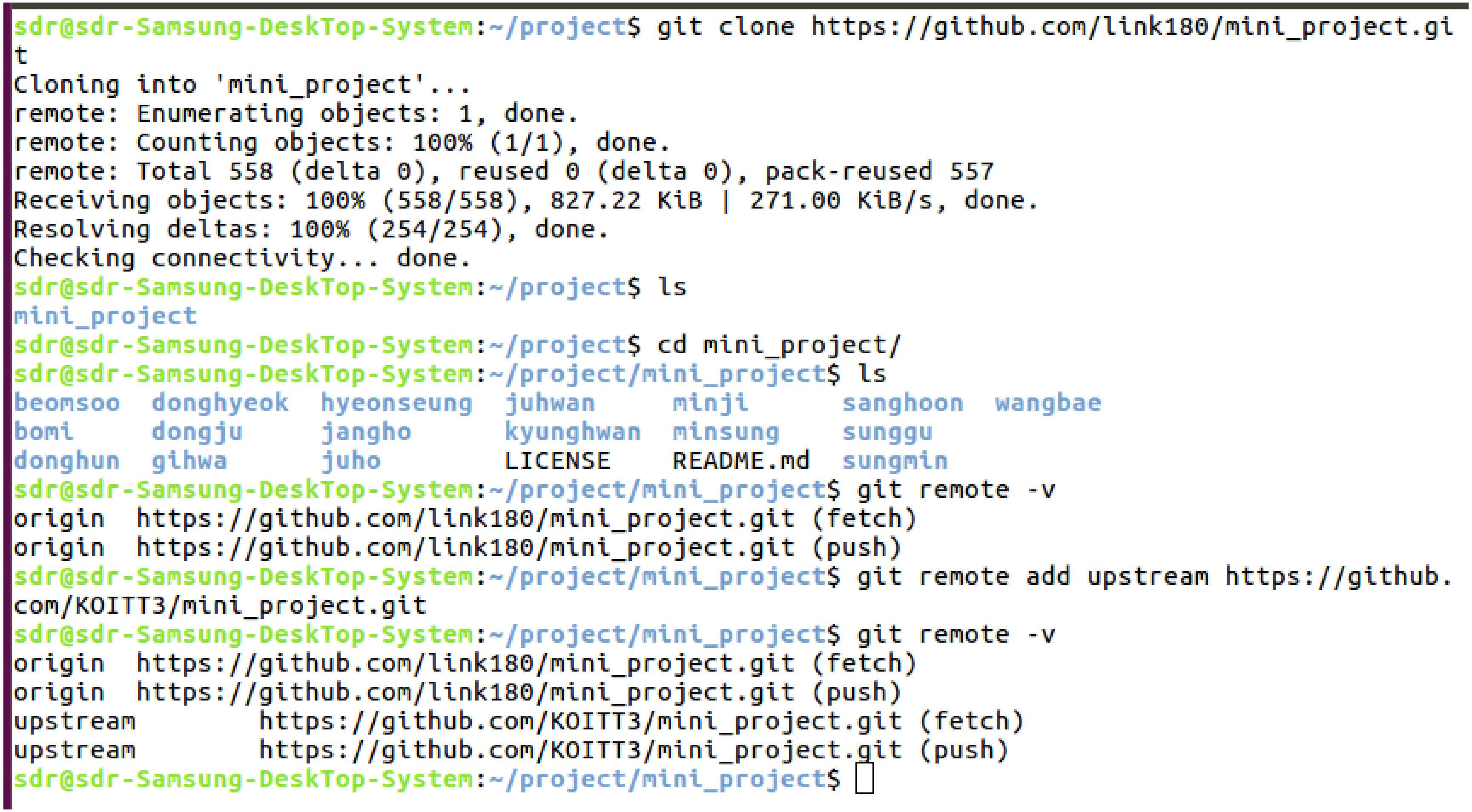
Use Git or checkout with SVN using the web URL.

`https://github.com/KOITT3/mini_project`

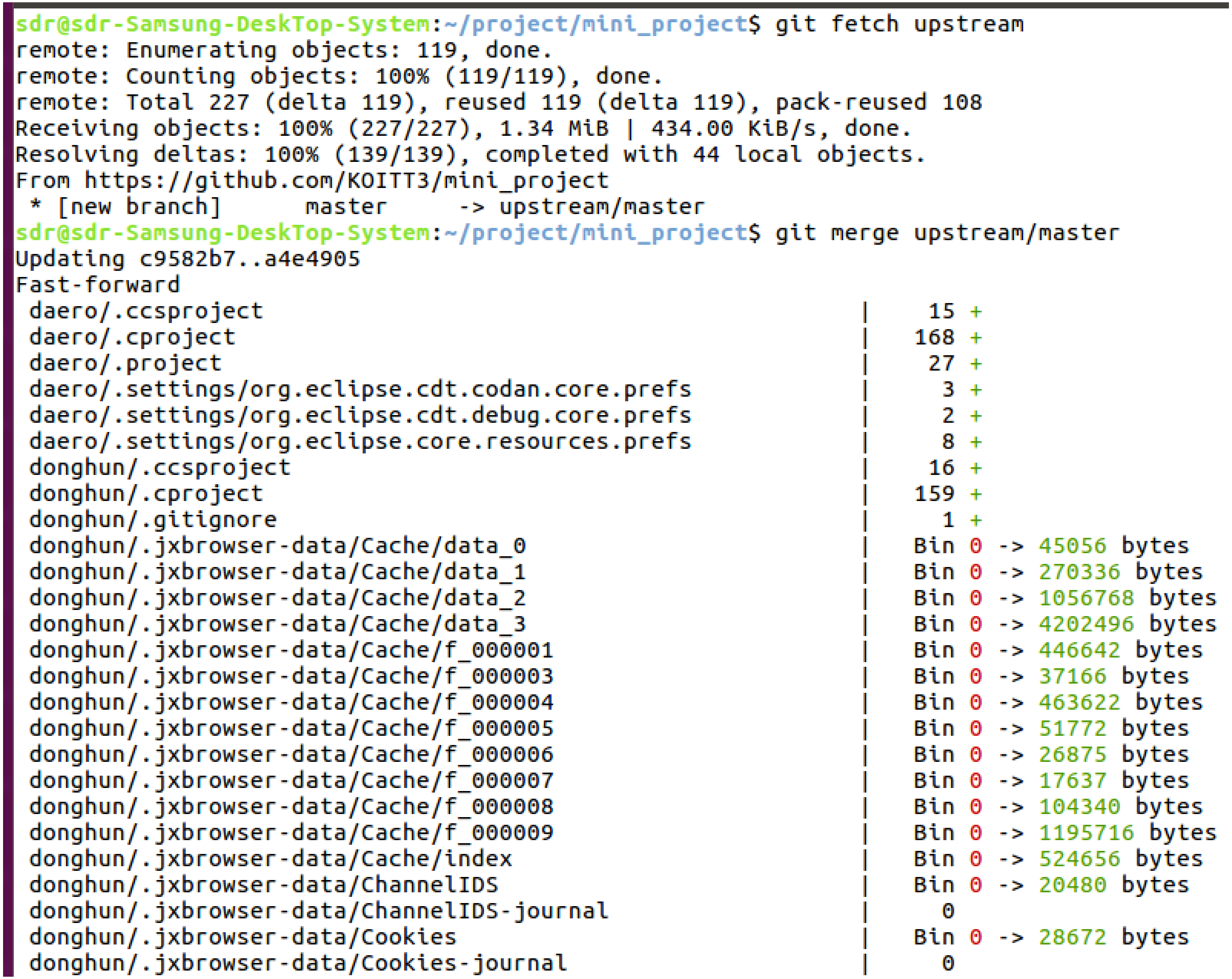
Download ZIP

beomsoo	ccs git test -범수	
bomi	ccs git test-bomi	
daero	Merge pull request #44 from daeroro/master	
donghun	add missing file - 동훈	
donghyeok	ccs git test - 동혁	11 hours ago
dongju	add missing file - 김동주	10 hours ago
gihwa	no missing file 기화	10 hours ago
hyeonseung	add missing file - 현승	10 hours ago
initial_led	water rocket mcu code	10 hours ago
jangho	장호 - 테스트 작업	14 hours ago
juho	add missing file -juho	10 hours ago
juhwan	add missing file - 주환	10 hours ago

먼저 집에서 하는 사람들은 컴퓨터에 저장소가 없을테니 새롭게 clone 받도록 한다.
또 conflict 가 발생했던 사람들은 conflict 해결하는 문서에서 이미 완료했기 때문에 해당 문서를 볼 필요가 없다.
노트북에서 했던 사람들은 이 부분은 필요없다(여기까지 왔다는 것은 이미 되어 있고 conflict 가 나지 않았을테니 말이다)

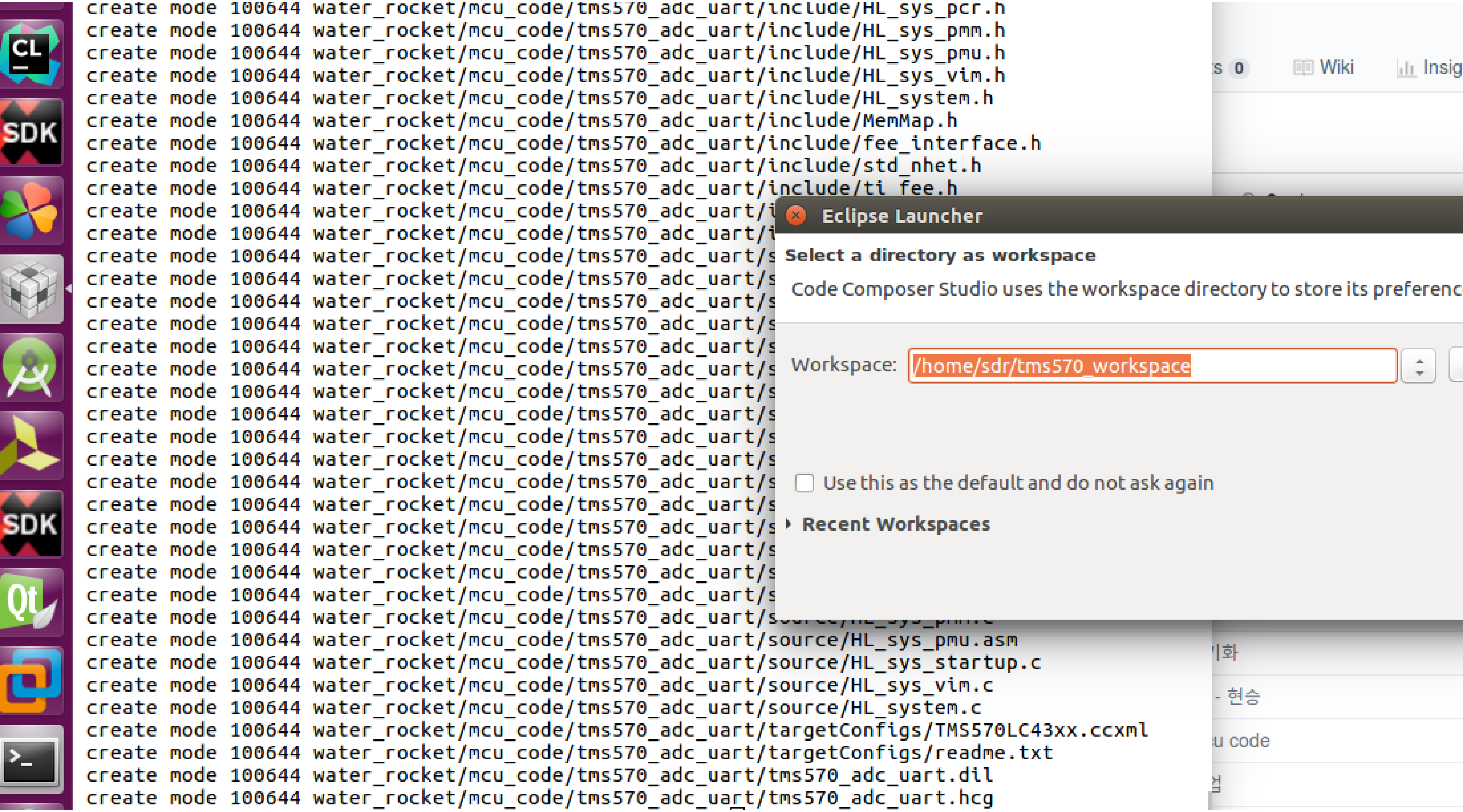


```
sdr@sdr-Samsung-DeskTop-System:~/project$ git clone https://github.com/link180/mini_project.gi
t
Cloning into 'mini_project'...
remote: Enumerating objects: 1, done.
remote: Counting objects: 100% (1/1), done.
remote: Total 558 (delta 0), reused 0 (delta 0), pack-reused 557
Receiving objects: 100% (558/558), 827.22 KiB | 271.00 KiB/s, done.
Resolving deltas: 100% (254/254), done.
Checking connectivity... done.
sdr@sdr-Samsung-DeskTop-System:~/project$ ls
mini_project
sdr@sdr-Samsung-DeskTop-System:~/project$ cd mini_project/
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project$ ls
beomsoo  donghyeok  hyeonseung  juhwan     minji      sanghoon  wangbae
bomi     dongju     jangho      kyunghwan  minsung    sunggu
donghun  gihwa      juho        LICENSE    README.md  sungmin
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project$ git remote -v
origin  https://github.com/link180/mini_project.git (fetch)
origin  https://github.com/link180/mini_project.git (push)
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project$ git remote add upstream https://github.
com/KOITT3/mini_project.git
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project$ git remote -v
origin  https://github.com/link180/mini_project.git (fetch)
origin  https://github.com/link180/mini_project.git (push)
upstream        https://github.com/KOITT3/mini_project.git (fetch)
upstream        https://github.com/KOITT3/mini_project.git (push)
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project$ 
```



```
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project$ git fetch upstream
remote: Enumerating objects: 119, done.
remote: Counting objects: 100% (119/119), done.
remote: Total 227 (delta 119), reused 119 (delta 119), pack-reused 108
Receiving objects: 100% (227/227), 1.34 MiB | 434.00 KiB/s, done.
Resolving deltas: 100% (139/139), completed with 44 local objects.
From https://github.com/KOITT3/mini_project
 * [new branch]      master     -> upstream/master
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project$ git merge upstream/master
Updating c9582b7..a4e4905
Fast-forward
 daero/.ccsproject                                           |    15 +
 daero/.cproject                                             |   168 +
 daero/.project                                              |    27 +
 daero/.settings/org.eclipse.cdt.codan.core.prefs            |     3 +
 daero/.settings/org.eclipse.cdt.debug.core.prefs            |     2 +
 daero/.settings/org.eclipse.core.resources.prefs            |     8 +
 donghun/.ccsproject                                         |    16 +
 donghun/.cproject                                           |   159 +
 donghun/.gitignore                                          |     1 +
 donghun/.jxbrowser-data/Cache/data_0                        |   Bin 0 -> 45056 bytes
 donghun/.jxbrowser-data/Cache/data_1                        |   Bin 0 -> 270336 bytes
 donghun/.jxbrowser-data/Cache/data_2                        |   Bin 0 -> 1056768 bytes
 donghun/.jxbrowser-data/Cache/data_3                        |   Bin 0 -> 4202496 bytes
 donghun/.jxbrowser-data/Cache/f_000001                      |   Bin 0 -> 446642 bytes
 donghun/.jxbrowser-data/Cache/f_000003                      |   Bin 0 -> 37166 bytes
 donghun/.jxbrowser-data/Cache/f_000004                      |   Bin 0 -> 463622 bytes
 donghun/.jxbrowser-data/Cache/f_000005                      |   Bin 0 -> 51772 bytes
 donghun/.jxbrowser-data/Cache/f_000006                      |   Bin 0 -> 26875 bytes
 donghun/.jxbrowser-data/Cache/f_000007                      |   Bin 0 -> 17637 bytes
 donghun/.jxbrowser-data/Cache/f_000008                      |   Bin 0 -> 104340 bytes
 donghun/.jxbrowser-data/Cache/f_000009                      |   Bin 0 -> 1195716 bytes
 donghun/.jxbrowser-data/Cache/index                         |   Bin 0 -> 524656 bytes
 donghun/.jxbrowser-data/ChannelIDS                          |   Bin 0 -> 20480 bytes
 donghun/.jxbrowser-data/ChannelIDS-journal                  |     0
 donghun/.jxbrowser-data/Cookies                             |   Bin 0 -> 28672 bytes
 donghun/.jxbrowser-data/Cookies-journal                     |     0
```

이제 CCS 를 구동시킨다.



```
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/include/HL_sys_pcr.h
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/include/HL_sys_pmm.h
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/include/HL_sys_pmu.h
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/include/HL_sys_vim.h
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/include/HL_system.h
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/include/MemMap.h
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/include/fee_interface.h
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/include/std_nhet.h
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/include/ti_fee.h
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/i
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/i
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/s
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/source/HL_sys_pmm.c
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/source/HL_sys_pmu.asm
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/source/HL_sys_startup.c
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/source/HL_sys_vim.c
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/source/HL_system.c
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/targetConfigs/TMS570LC43xx.ccxml
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/targetConfigs/readme.txt
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/tms570_adc_uart.dil
create mode 100644 water_rocket/mcu_code/tms570_adc_uart/tms570_adc_uart.hcg
```

Eclipse Launcher

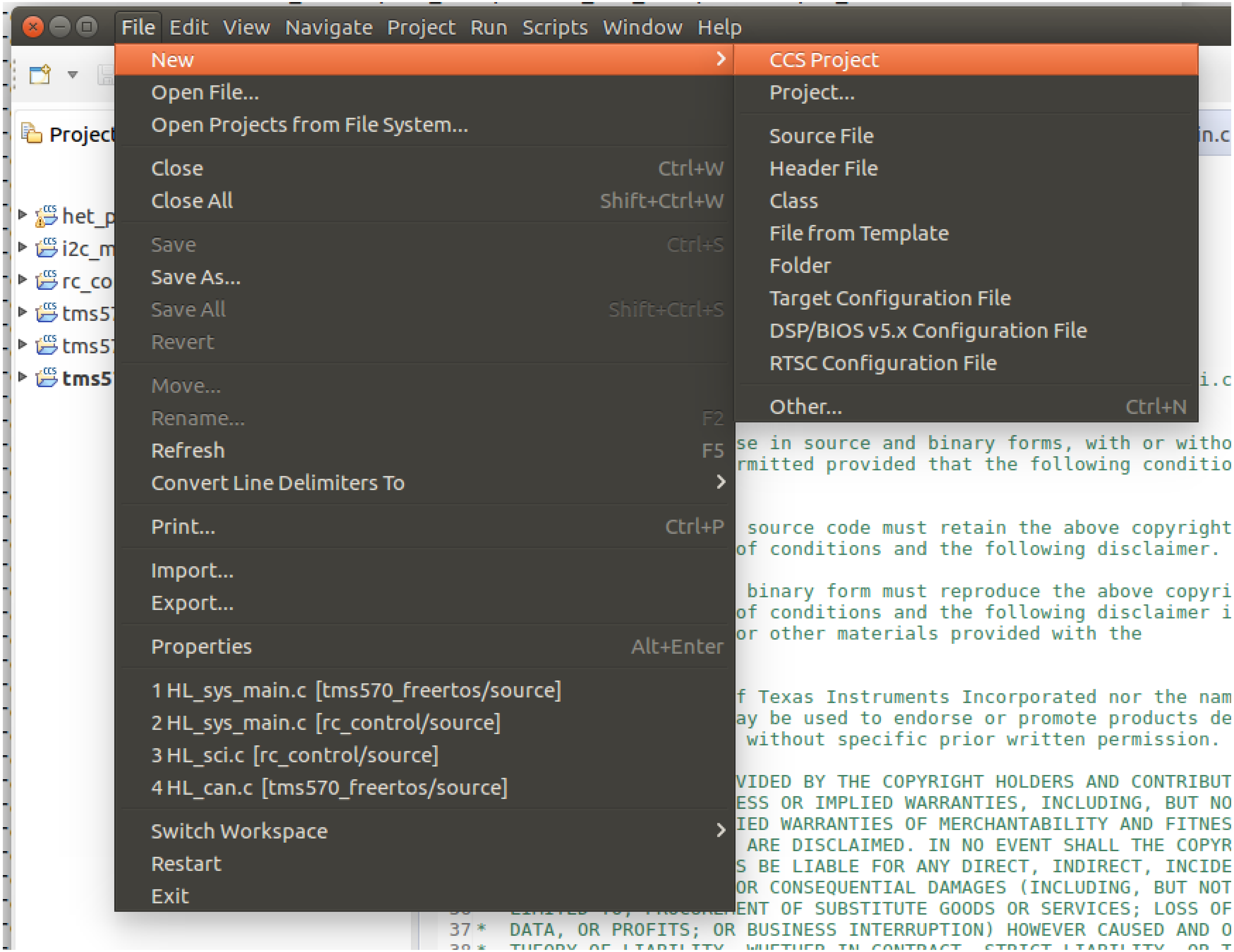
Select a directory as workspace

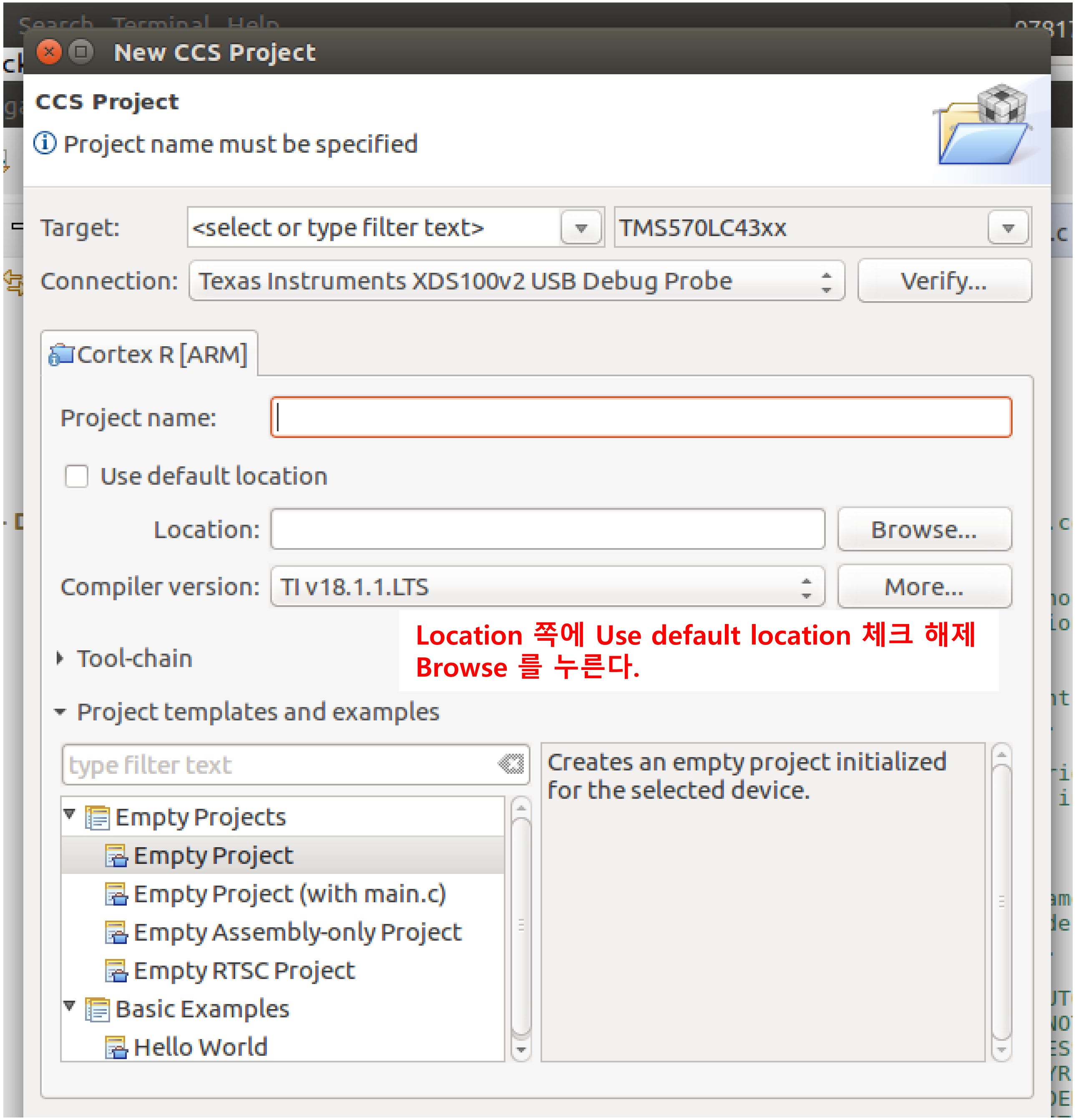
Code Composer Studio uses the workspace directory to store its preferenc

Workspace: /home/sdr/tms570_workspace

☐ Use this as the default and do not ask again

Recent Workspaces


File
Edit
View
Navigate
Project
Run
Scripts
Window
Help
New
Open File...
Open Projects from File System...
Close Ctrl+W
Close All Shift+Ctrl+W
Save Ctrl+S
Save As...
Save All Shift+Ctrl+S
Revert
Move...
Rename... F2
Refresh F5
Convert Line Delimiters To
Print... Ctrl+P
Import...
Export...
Properties Alt+Enter
1 HL_sys_main.c [tms570_freertos/source]
2 HL_sys_main.c [rc_control/source]
3 HL_sci.c [rc_control/source]
4 HL_can.c [tms570_freertos/source]
Switch Workspace
Restart
Exit
CCS Project
Project...
Source File
Header File
Class
File from Template
Folder
Target Configuration File
DSP/BIOS v5.x Configuration File
RTSC Configuration File
Other... Ctrl+N
Project
het_p
i2c_m
rc_co
tms57
tms57
tms5
in.c
i.c
se in source and binary forms, with or witho
rmitted provided that the following conditio
source code must retain the above copyright
of conditions and the following disclaimer.
binary form must reproduce the above copyri
of conditions and the following disclaimer i
or other materials provided with the
f Texas Instruments Incorporated nor the nam
ay be used to endorse or promote products de
without specific prior written permission.
VIDED BY THE COPYRIGHT HOLDERS AND CONTRIBUT
ESS OR IMPLIED WARRANTIES, INCLUDING, BUT NO
IED WARRANTIES OF MERCHANTABILITY AND FITNES
ARE DISCLAIMED. IN NO EVENT SHALL THE COPYR
S BE LIABLE FOR ANY DIRECT, INDIRECT, INCIDE
OR CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT
37 * DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND O



New CCS Project

CCS Project

ⓘ Project name must be specified

Target: <select or type filter text> TMS570LC43xx

Connection: Texas Instruments XDS100v2 USB Debug Probe

Cortex R [ARM]

Project name:

☐ Use default location

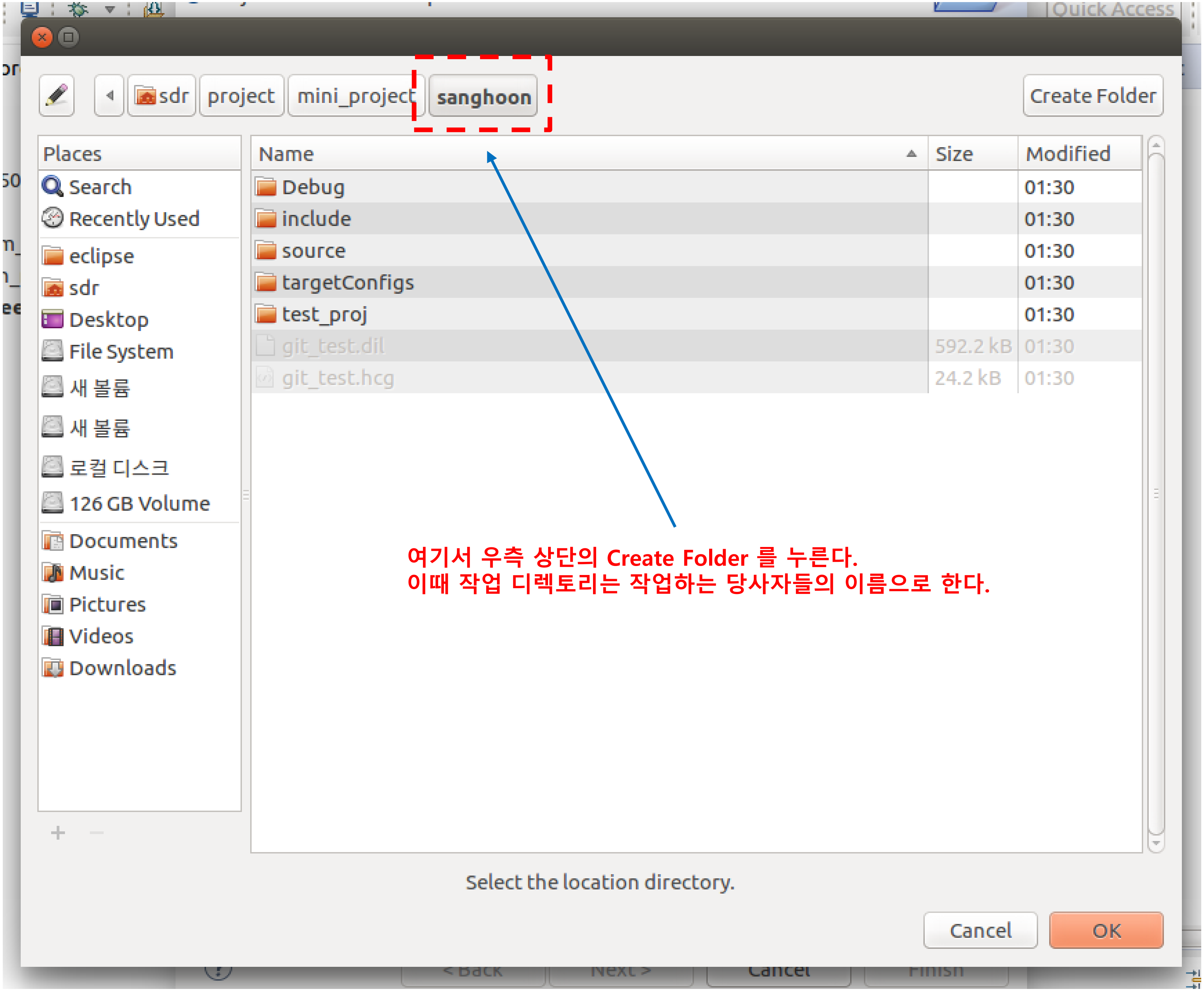
Location:

Compiler version: TI v18.1.1.LTS

Verify...
Browse...
More...
Tool-chain
Project templates and examples
type filter text
Empty Projects
Empty Project
Empty Project (with main.c)
Empty Assembly-only Project
Empty RTSC Project
Basic Examples
Hello World

Creates an empty project initialized for the selected device.

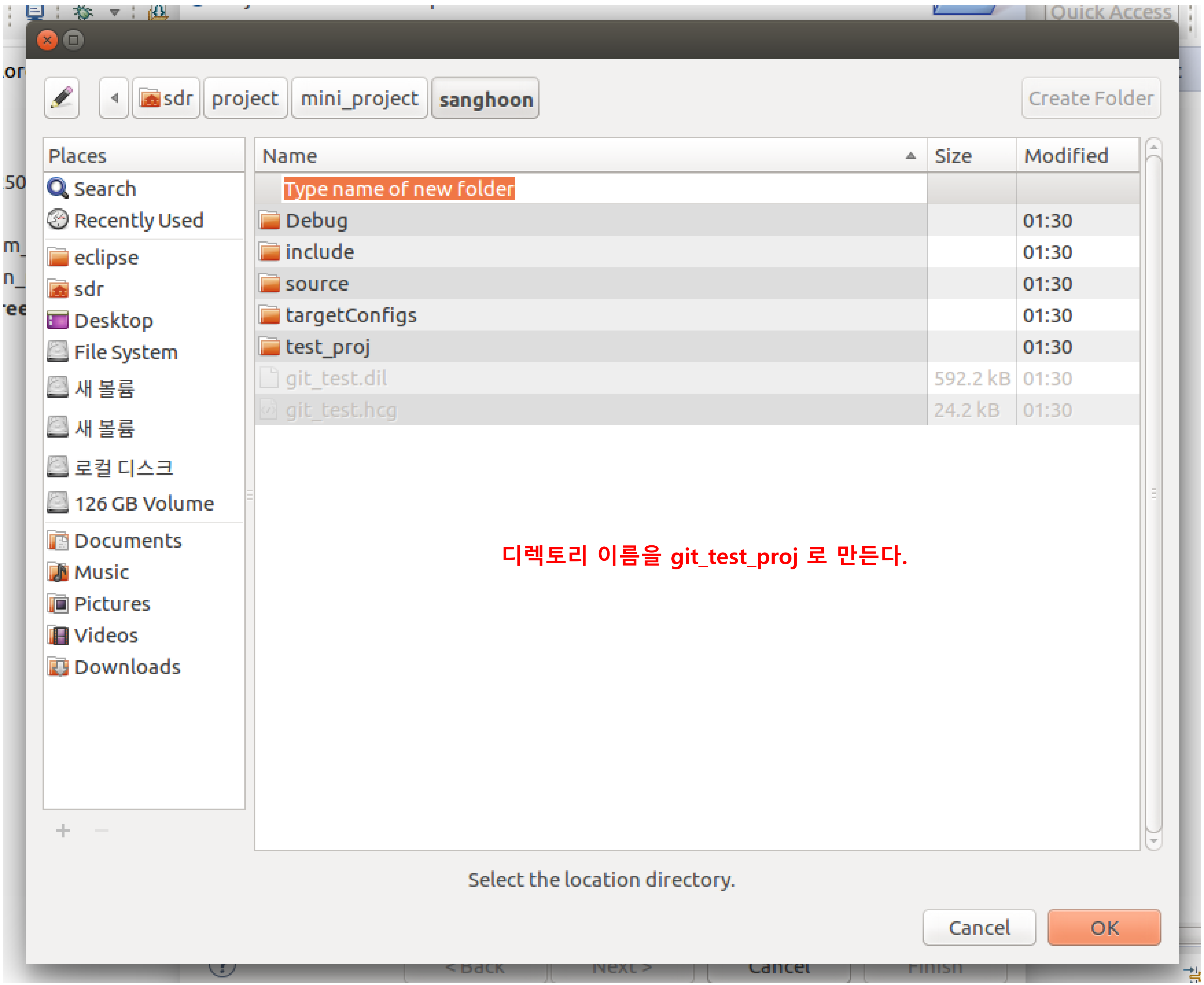
Location 쪽에 Use default location 체크 해제
Browse 를 누른다.


sdr
project
mini_project
sanghoon
Create Folder
Places
Search
Recently Used
eclipse
sdr
Desktop
File System
새 볼륨
새 볼륨
로컬 디스크
126 GB Volume
Documents
Music
Pictures
Videos
Downloads
Cancel
OK

Name	Size	Modified
Debug		01:30
include		01:30
source		01:30
targetConfigs		01:30
test_proj		01:30
git_test.dil	592.2 kB	01:30
git_test.hcg	24.2 kB	01:30

Select the location directory.

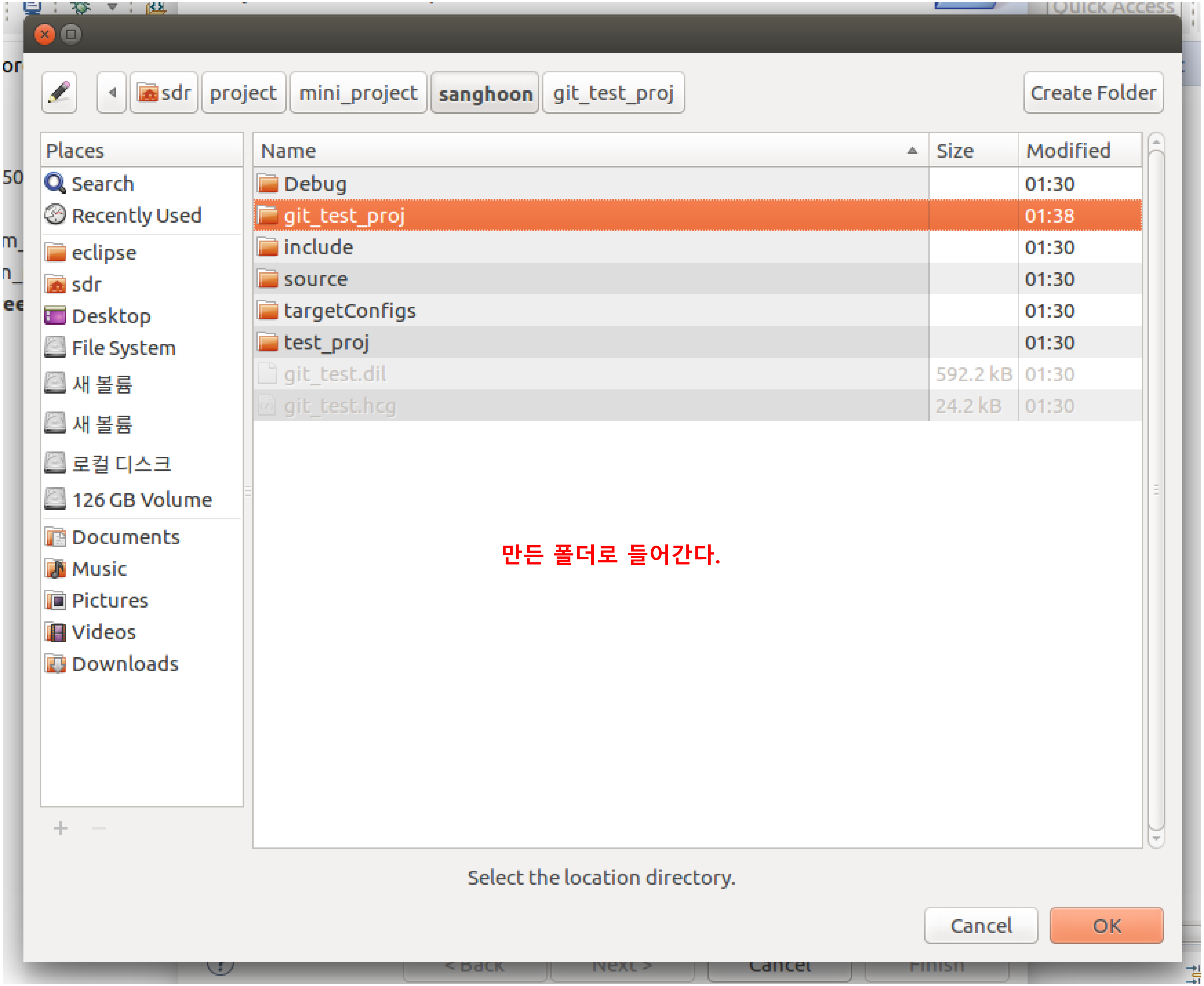
여기서 우측 상단의 Create Folder 를 누른다.
이때 작업 디렉토리는 작업하는 당사자들의 이름으로 한다.


Quick Access
sdr
project
mini_project
sanghoon
Create Folder
Places
Search
Recently Used
eclipse
sdr
Desktop
File System
새 볼륨
새 볼륨
로컬 디스크
126 GB Volume
Documents
Music
Pictures
Videos
Downloads
Cancel
OK
< Back
Next >
Cancel
Finish

Name	Size	Modified
Type name of new folder		
Debug		01:30
include		01:30
source		01:30
targetConfigs		01:30
test_proj		01:30
git_test.dil	592.2 kB	01:30
git_test.hcg	24.2 kB	01:30

디렉토리 이름을 git_test_proj 로 만든다.

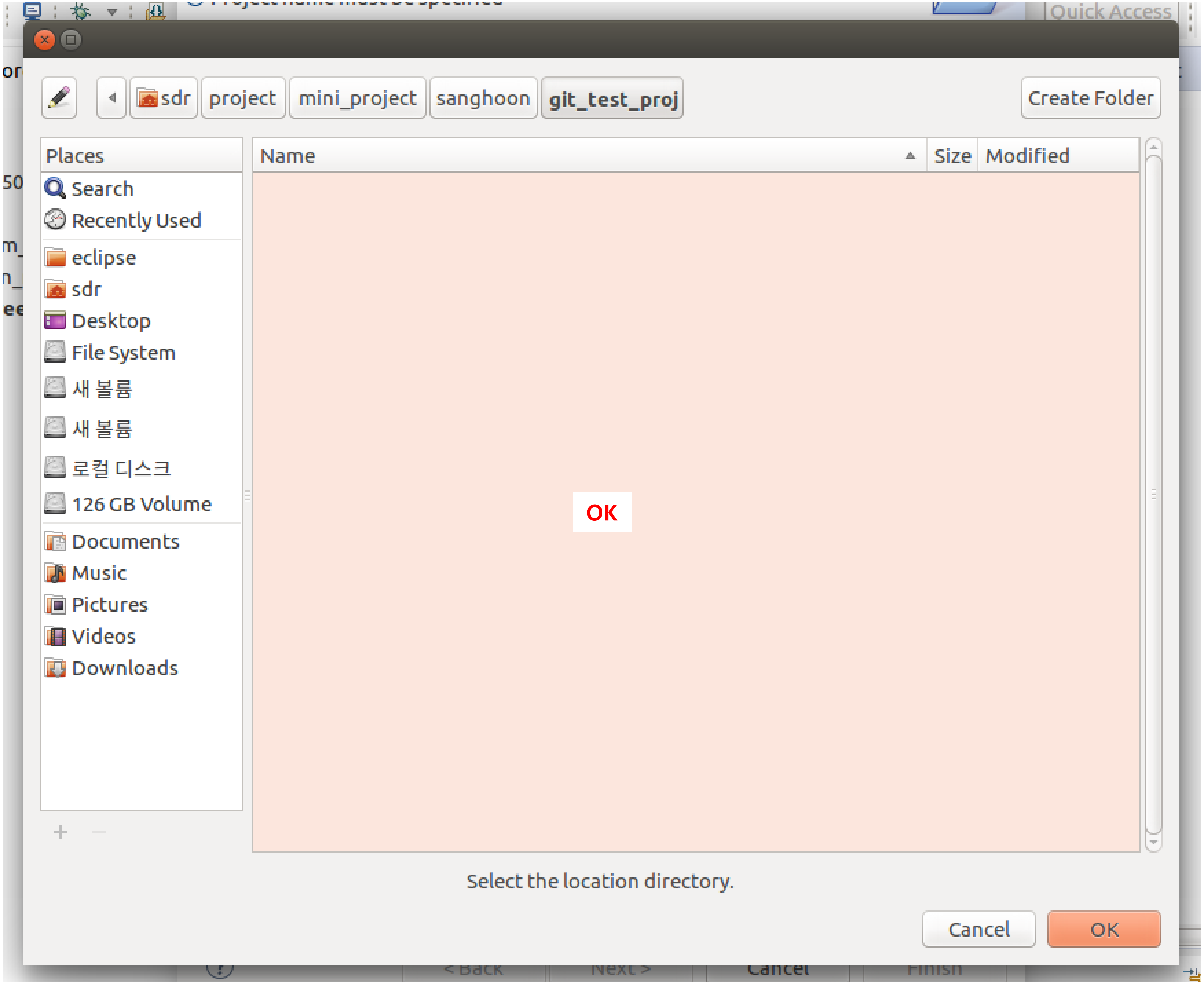
Select the location directory.


sdr
project
mini_project
sanghoon
git_test_proj
Create Folder
Places
Search
Recently Used
eclipse
sdr
Desktop
File System
새 볼륨
새 볼륨
로컬 디스크
126 GB Volume
Documents
Music
Pictures
Videos
Downloads
Cancel
OK

Name	Size	Modified
Debug		01:30
git_test_proj		01:38
include		01:30
source		01:30
targetConfigs		01:30
test_proj		01:30
git_test.dil	592.2 kB	01:30
git_test.hcg	24.2 kB	01:30

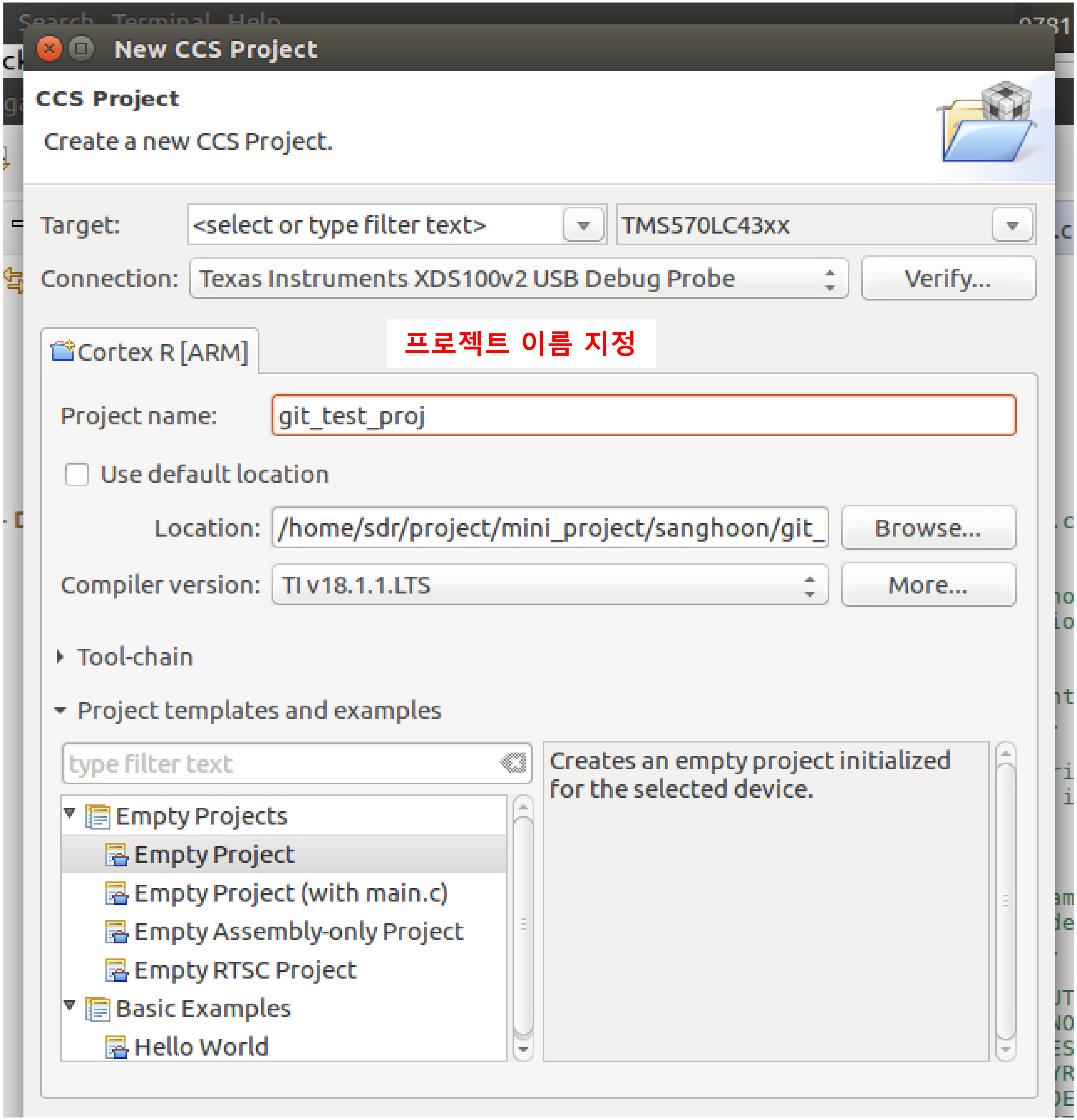
만든 폴더로 들어간다.

Select the location directory.


sdr
project
mini_project
sanghoon
git_test_proj
Create Folder
Places
Search
Recently Used
eclipse
sdr
Desktop
File System
새 볼륨
새 볼륨
로컬 디스크
126 GB Volume
Documents
Music
Pictures
Videos
Downloads
Name
Size
Modified
Cancel
OK
Quick Access
< Back
Next >
Cancel
Finish

OK

Select the location directory.



프로젝트 이름 지정

New CCS Project

CCS Project

Create a new CCS Project.

Target: <select or type filter text> TMS570LC43xx

Connection: Texas Instruments XDS100v2 USB Debug Probe

Cortex R [ARM]

Project name: git_test_proj

☐ Use default location

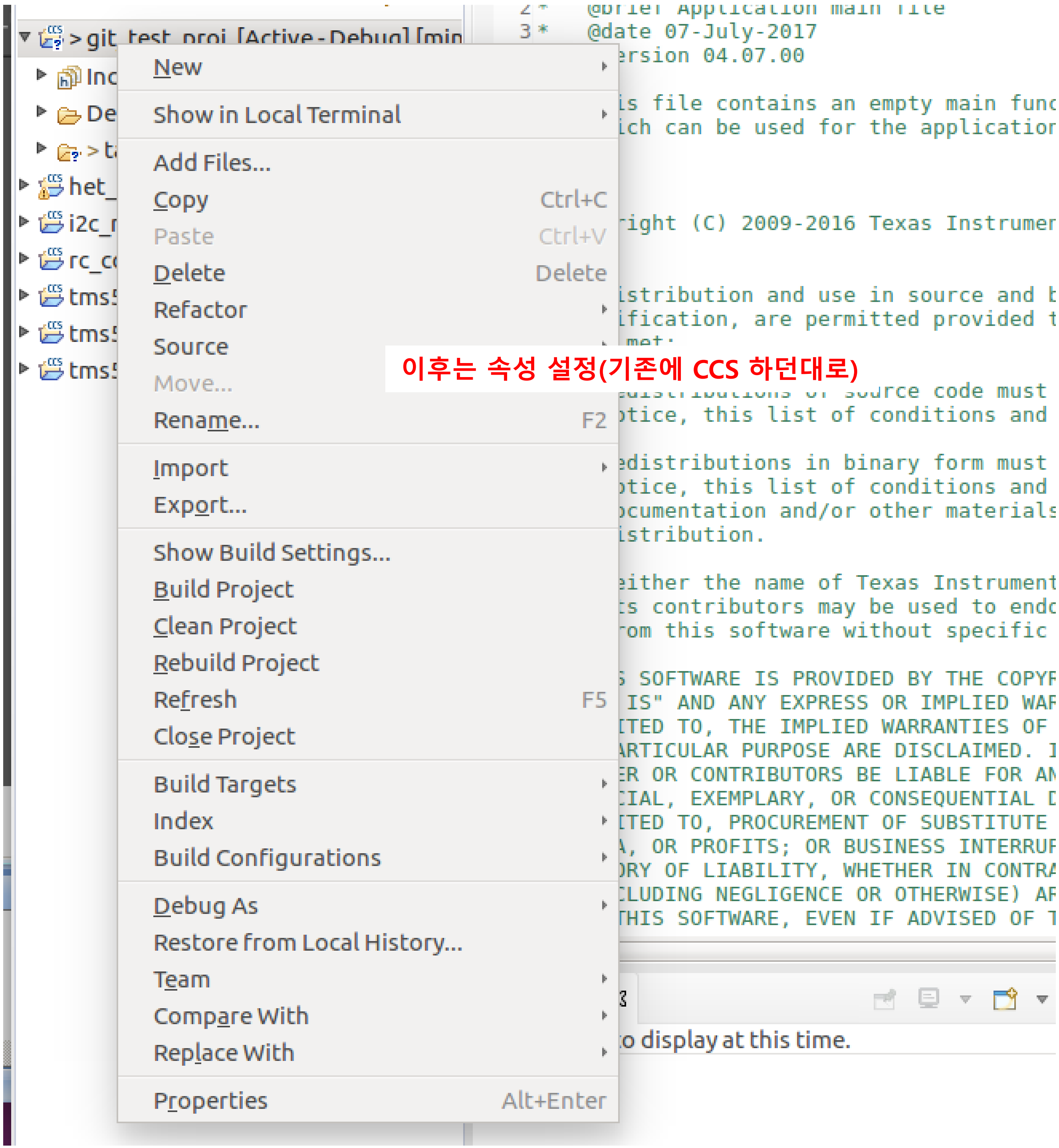
Location: /home/sdr/project/mini_project/sanghoon/git_

Compiler version: TI v18.1.1.LTS

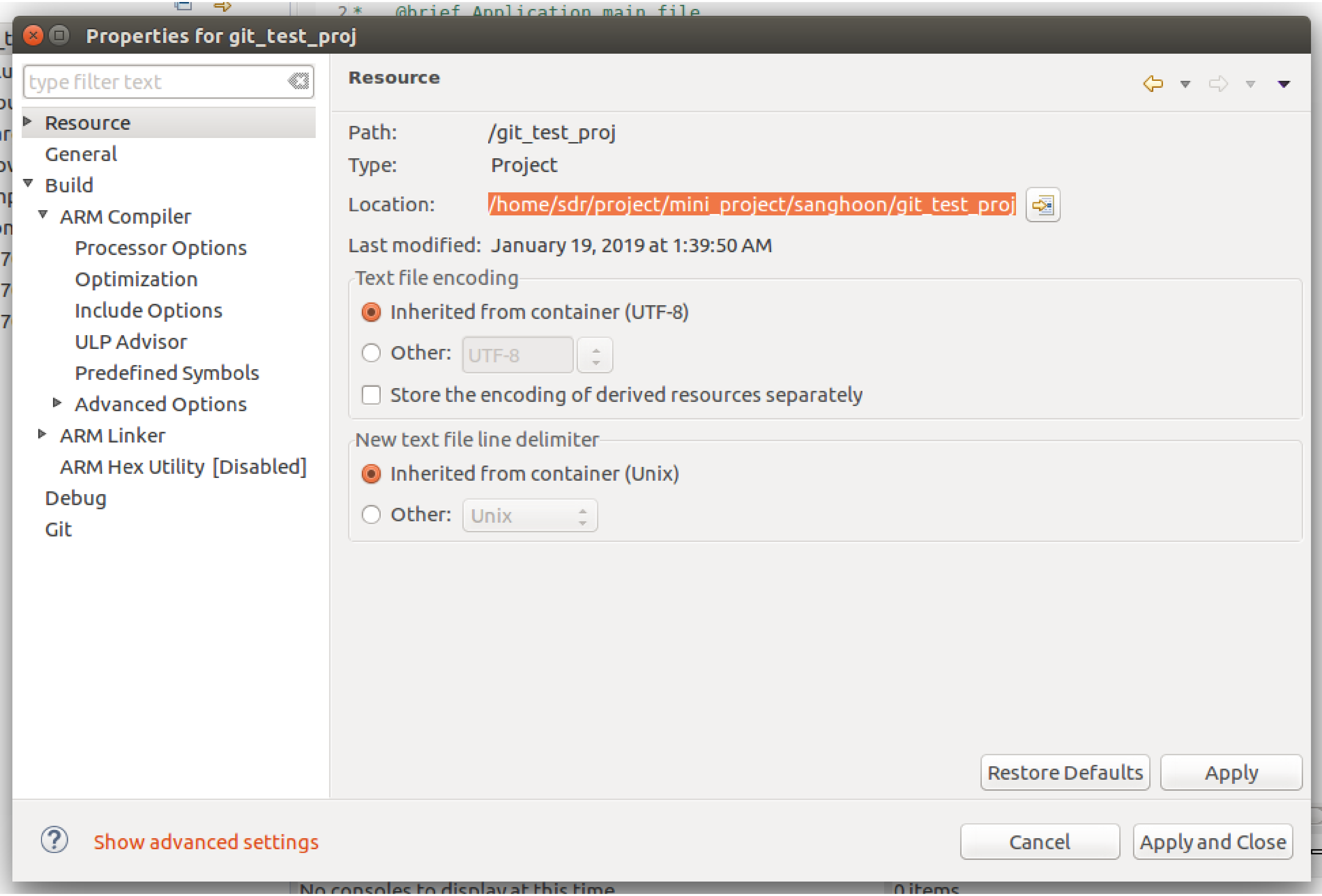
Project templates and examples

- Empty Projects
  - Empty Project
  - Empty Project (with main.c)
  - Empty Assembly-only Project
  - Empty RTSC Project
- Basic Examples
  - Hello World

Creates an empty project initialized for the selected device.


> git_test_proj [Active - Debug] [main
New
Show in Local Terminal
Add Files...
Copy Ctrl+C
Paste Ctrl+V
Delete Delete
Refactor
Source
Move...
Rename... F2
Import
Export...
Show Build Settings...
Build Project
Clean Project
Rebuild Project
Refresh F5
Close Project
Build Targets
Index
Build Configurations
Debug As
Restore from Local History...
Team
Compare With
Replace With
Properties Alt+Enter

이후는 속성 설정(기존에 CCS 하던대로)


type filter text
Resource
General
Build
ARM Compiler
Processor Options
Optimization
Include Options
ULP Advisor
Predefined Symbols
Advanced Options
ARM Linker
ARM Hex Utility [Disabled]
Debug
Git
Restore Defaults
Apply
Show advanced settings
Cancel
Apply and Close

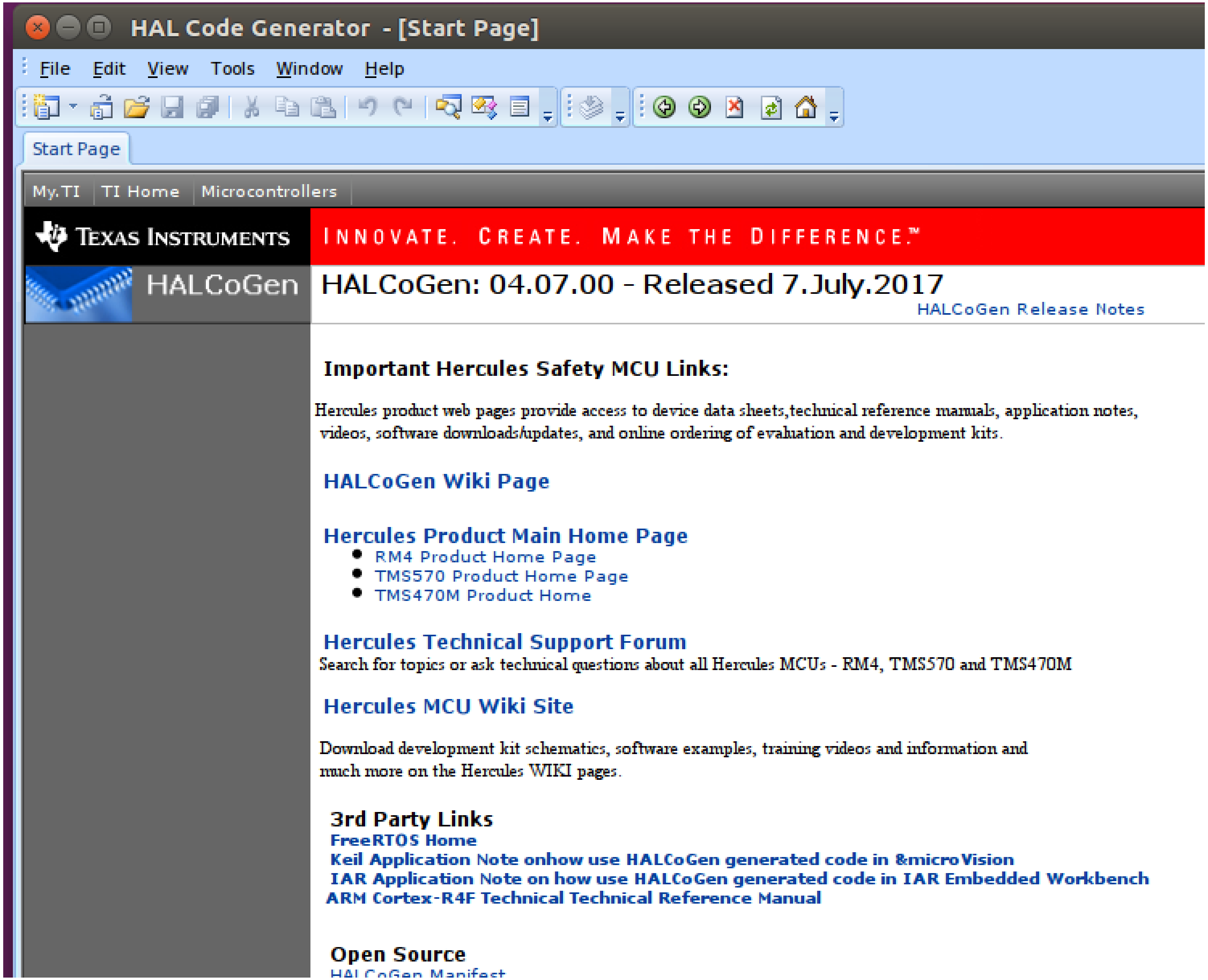
Properties for git_test_proj

Resource

Path: /git_test_proj
Type: Project
Location: /home/sdr/project/mini_project/sanghoon/git_test_proj
Last modified: January 19, 2019 at 1:39:50 AM

Text file encoding
● Inherited from container (UTF-8)
○ Other: UTF-8
☐ Store the encoding of derived resources separately

New text file line delimiter
● Inherited from container (Unix)
○ Other: Unix


HAL Code Generator - [Start Page]
File
Edit
View
Tools
Window
Help
Start Page
My.TI
TI Home
Microcontrollers
Texas Instruments
INNOVATE. CREATE. MAKE THE DIFFERENCE.™
HALCoGen

HALCoGen: 04.07.00 - Released 7.July.2017

HALCoGen Release Notes

Important Hercules Safety MCU Links:

Hercules product web pages provide access to device data sheets,technical reference manuals, application notes, videos, software downloads/updates, and online ordering of evaluation and development kits.

HALCoGen Wiki Page

Hercules Product Main Home Page

- RM4 Product Home Page
- TMS570 Product Home Page
- TMS470M Product Home

Hercules Technical Support Forum

Search for topics or ask technical questions about all Hercules MCUs - RM4, TMS570 and TMS470M

Hercules MCU Wiki Site

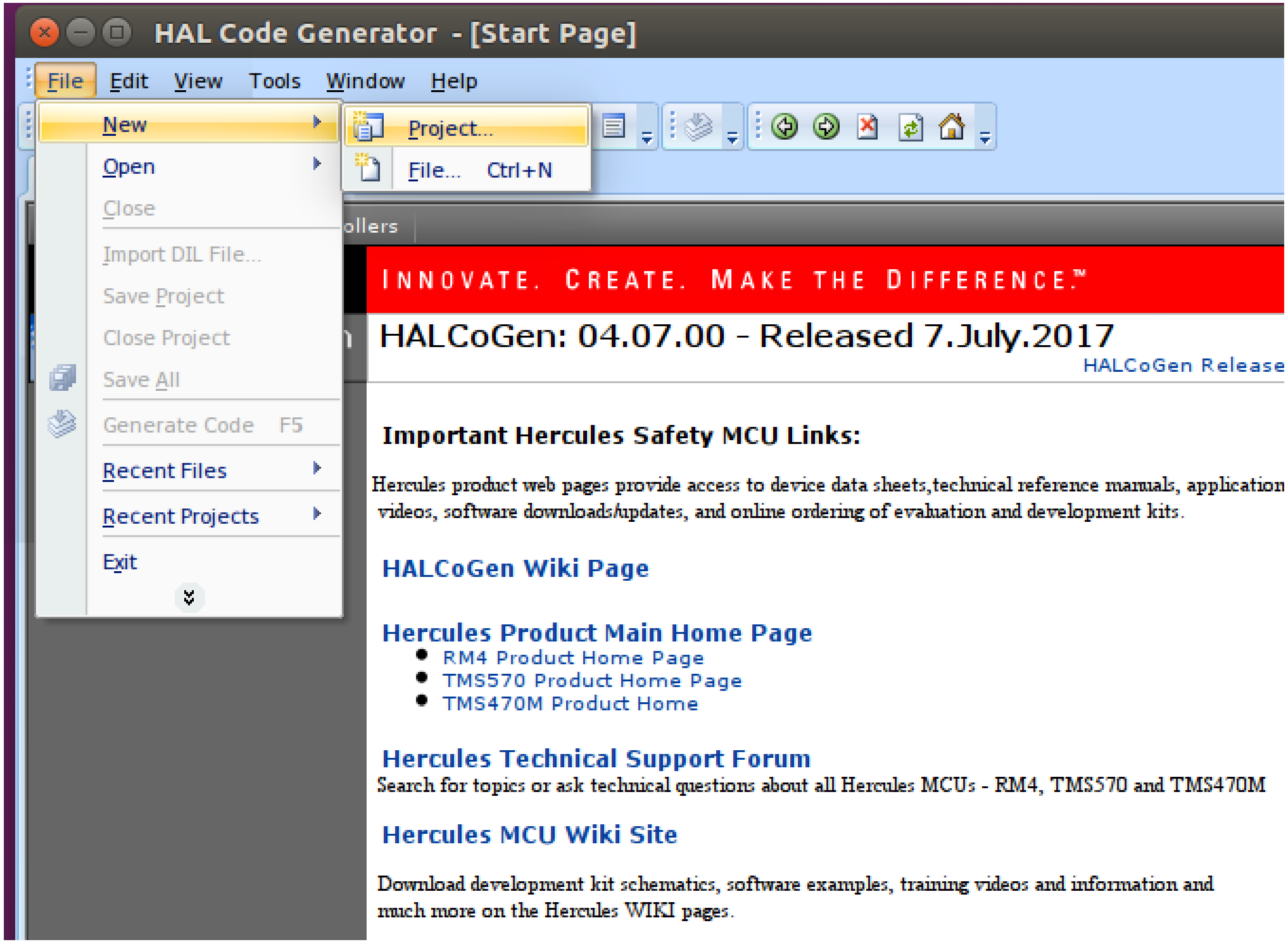
Download development kit schematics, software examples, training videos and information and much more on the Hercules WIKI pages.

3rd Party Links

FreeRTOS Home
Keil Application Note onhow use HALCoGen generated code in &microVision
IAR Application Note on how use HALCoGen generated code in IAR Embedded Workbench
ARM Cortex-R4F Technical Technical Reference Manual

Open Source

HALCoGen Manifest


HAL Code Generator - [Start Page]
File
Edit
View
Tools
Window
Help
New
Open
Close
Import DIL File...
Save Project
Close Project
Save All
Generate Code F5
Recent Files
Recent Projects
Exit
Project...
File... Ctrl+N
ollers

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HALCoGen: 04.07.00 - Released 7.July.2017

HALCoGen Release

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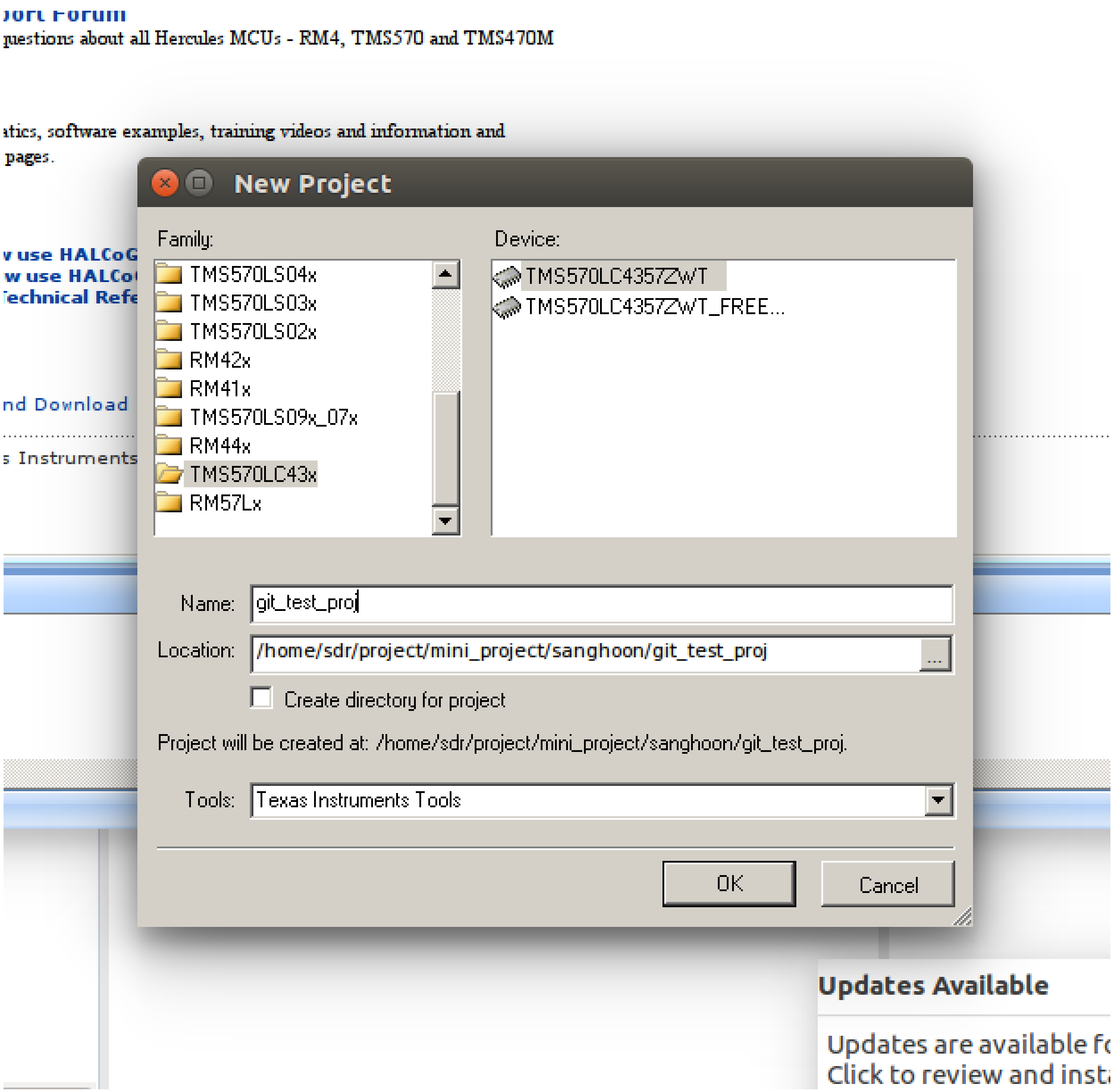


Family:

- TMS570LS04x
- TMS570LS03x
- TMS570LS02x
- RM42x
- RM41x
- TMS570LS09x_07x
- RM44x
- TMS570LC43x
- RM57Lx

Device:

- TMS570LC4357ZWT
- TMS570LC4357ZWT_FREE...

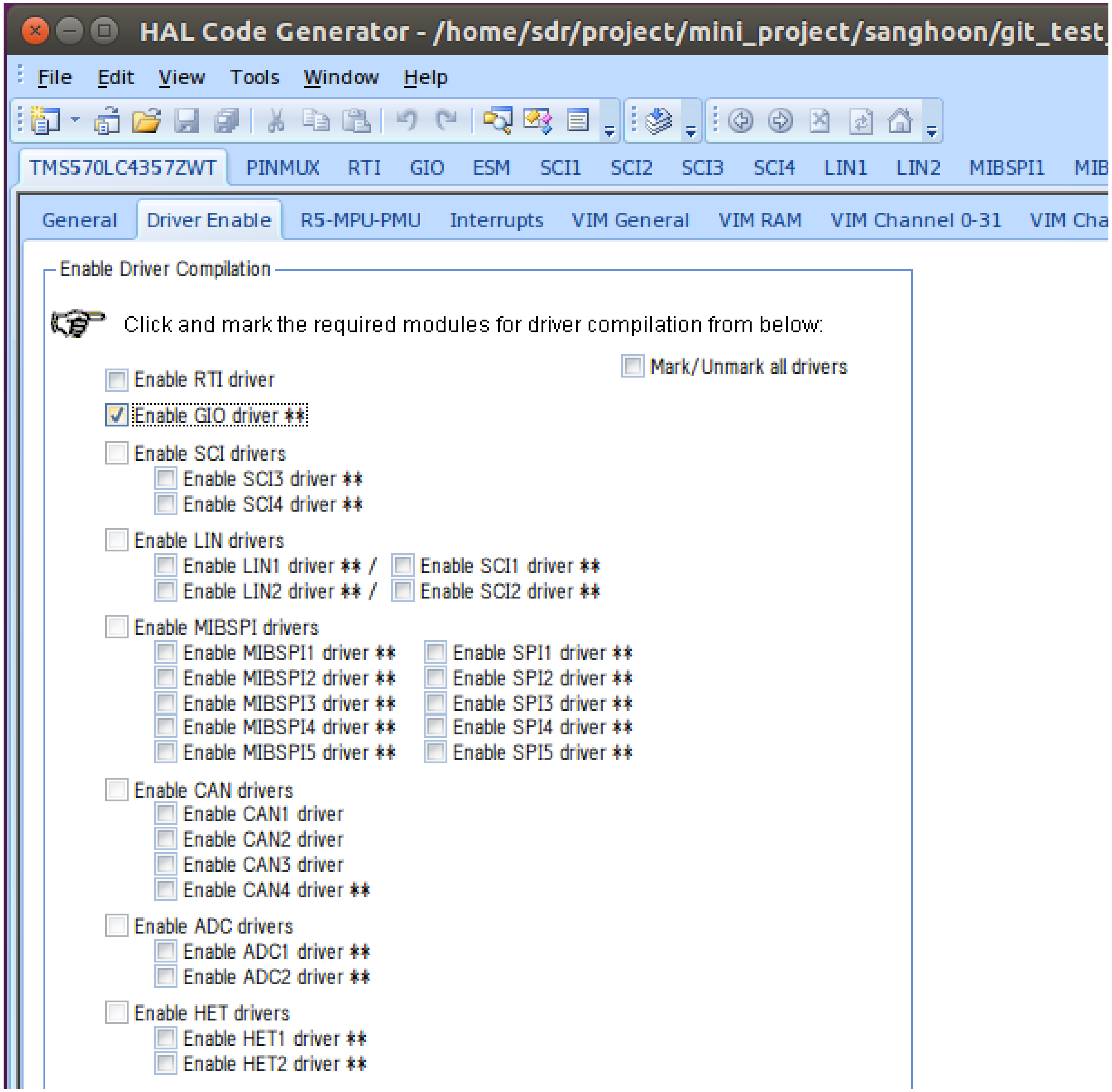
Name: git_test_proj

Location: /home/sdr/project/mini_project/sanghoon/git_test_proj

☐ Create directory for project

Project will be created at: /home/sdr/project/mini_project/sanghoon/git_test_proj.

Tools: Texas Instruments Tools


HAL Code Generator - /home/sdr/project/mini_project/sanghoon/git_test_
File
Edit
View
Tools
Window
Help
TMS570LC4357ZWT
PINMUX
RTI
GIO
ESM
SCI1
SCI2
SCI3
SCI4
LIN1
LIN2
MIBSPI1
MIB
General
Driver Enable
R5-MPU-PMU
Interrupts
VIM General
VIM RAM
VIM Channel 0-31
VIM Cha

Enable Driver Compilation

Click and mark the required modules for driver compilation from below:

☐ Mark/Unmark all drivers

☐ Enable RTI driver

☑ Enable GIO driver **

☐ Enable SCI drivers
  ☐ Enable SCI3 driver **
  ☐ Enable SCI4 driver **

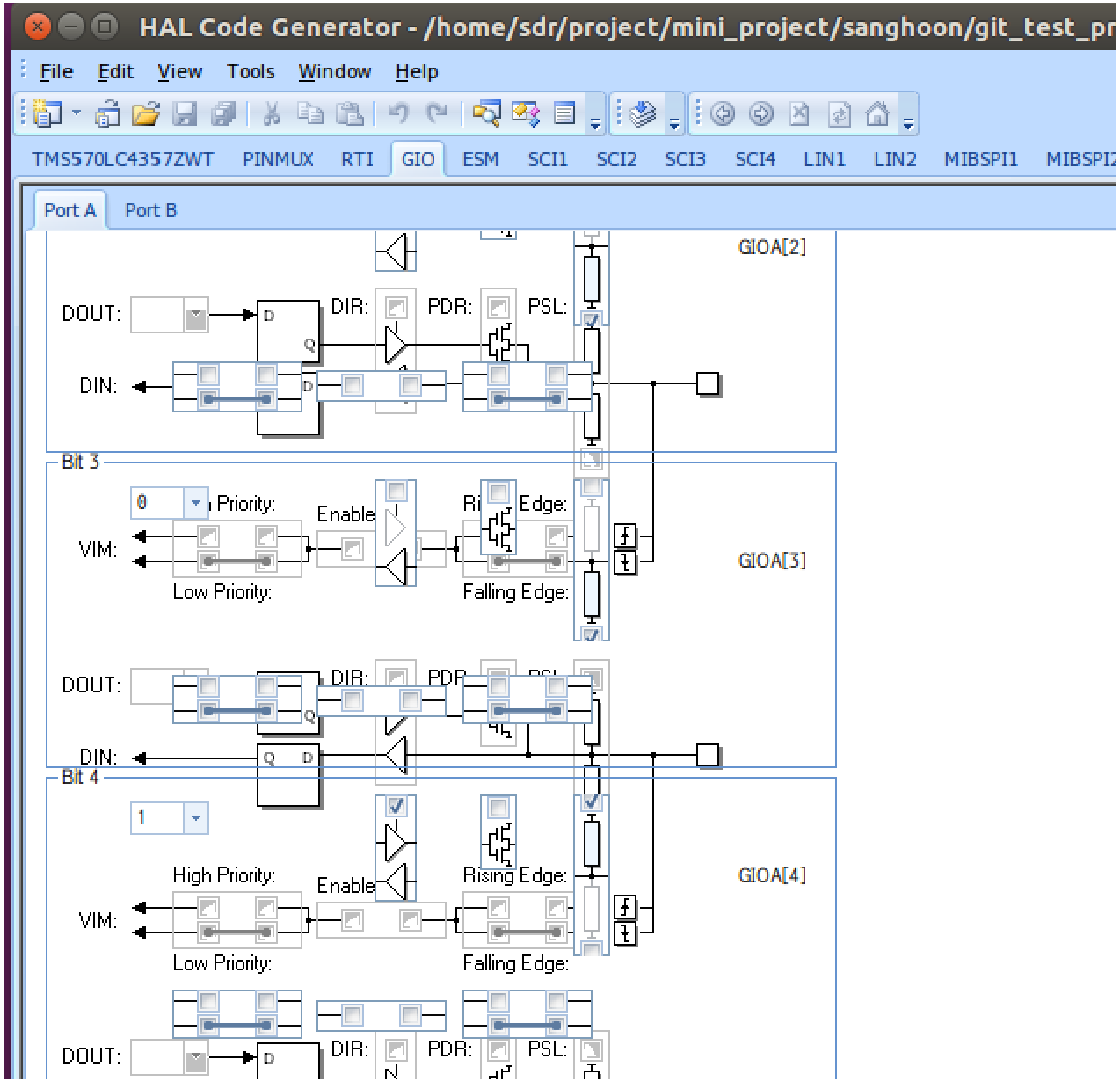
☐ Enable LIN drivers
  ☐ Enable LIN1 driver ** / ☐ Enable SCI1 driver **
  ☐ Enable LIN2 driver ** / ☐ Enable SCI2 driver **

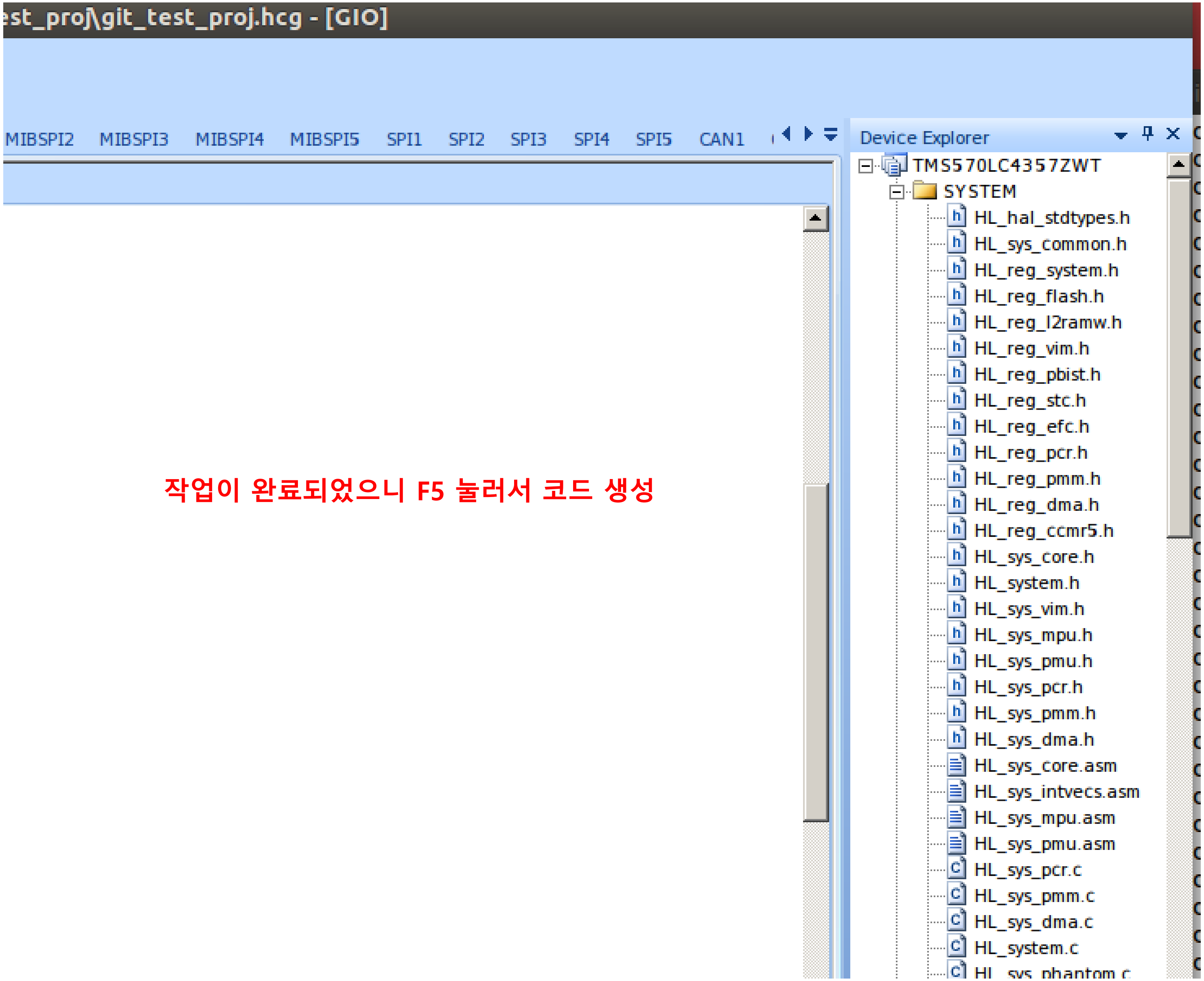
☐ Enable MIBSPI drivers
  ☐ Enable MIBSPI1 driver ** ☐ Enable SPI1 driver **
  ☐ Enable MIBSPI2 driver ** ☐ Enable SPI2 driver **
  ☐ Enable MIBSPI3 driver ** ☐ Enable SPI3 driver **
  ☐ Enable MIBSPI4 driver ** ☐ Enable SPI4 driver **
  ☐ Enable MIBSPI5 driver ** ☐ Enable SPI5 driver **

☐ Enable CAN drivers
  ☐ Enable CAN1 driver
  ☐ Enable CAN2 driver
  ☐ Enable CAN3 driver
  ☐ Enable CAN4 driver **

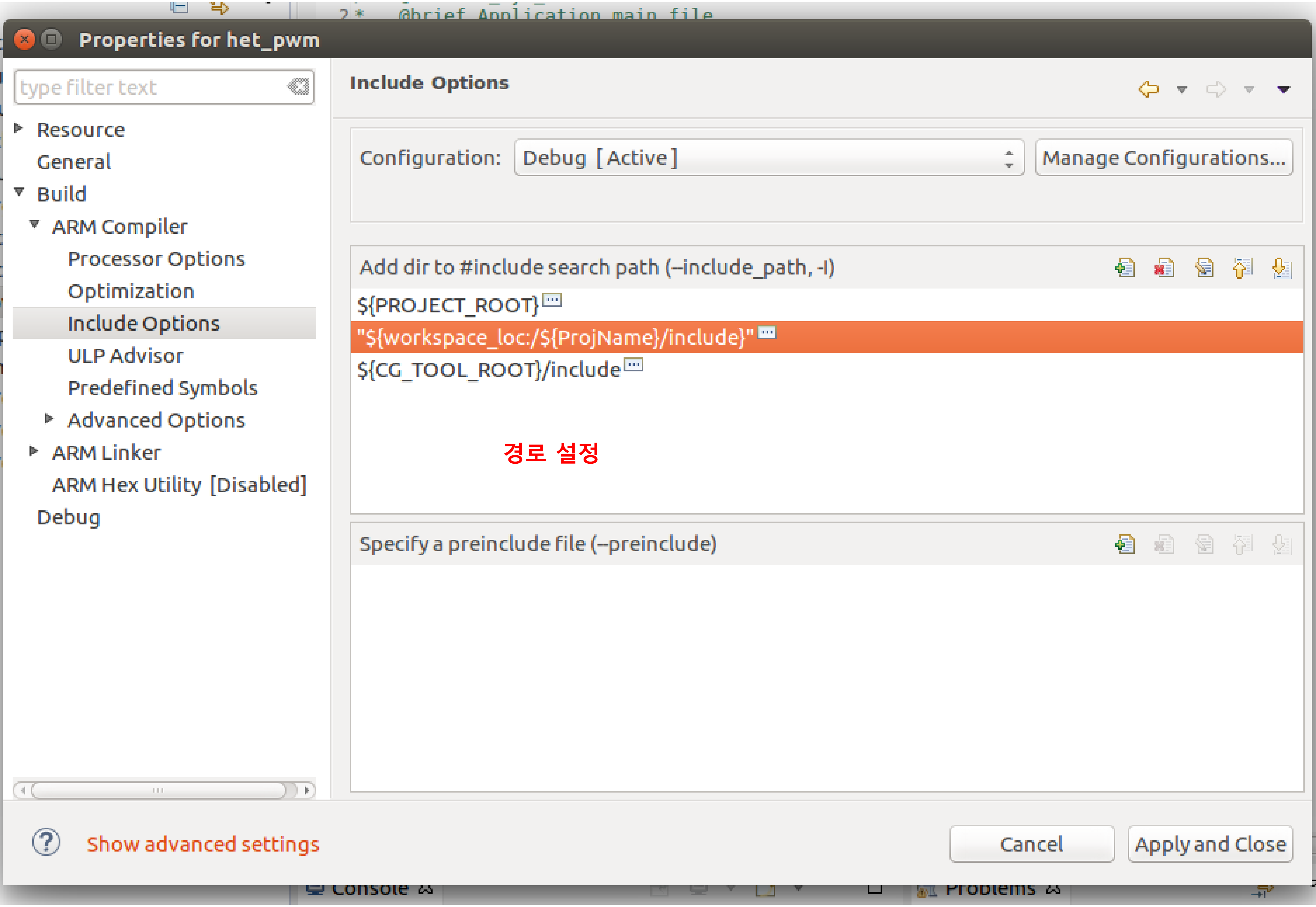
☐ Enable ADC drivers
  ☐ Enable ADC1 driver **
  ☐ Enable ADC2 driver **

☐ Enable HET drivers
  ☐ Enable HET1 driver **
  ☐ Enable HET2 driver **


HAL Code Generator - /home/sdr/project/mini_project/sanghoon/git_test_pr
File
Edit
View
Tools
Window
Help
TMS570LC4357ZWT
PINMUX
RTI
GIO
ESM
SCI1
SCI2
SCI3
SCI4
LIN1
LIN2
MIBSPI1
MIBSPI2
Port A
Port B
GIOA[2]
DOUT:
DIR:
PDR:
PSL:
DIN:
Bit 3
0
Priority:
Enable
Ri
Edge:
VIM:
Low Priority:
Falling Edge:
GIOA[3]
DOUT:
DIR:
PDR
DIN:
Bit 4
1
High Priority:
Enable
Rising Edge:
VIM:
Low Priority:
Falling Edge:
GIOA[4]
DOUT:
DIR:
PDR:
PSL:


est_proj\git_test_proj.hcg - [GIO]
MIBSPI2
MIBSPI3
MIBSPI4
MIBSPI5
SPI1
SPI2
SPI3
SPI4
SPI5
CAN1
Device Explorer
TMS570LC4357ZWT
SYSTEM
HL_hal_stdtypes.h
HL_sys_common.h
HL_reg_system.h
HL_reg_flash.h
HL_reg_l2ramw.h
HL_reg_vim.h
HL_reg_pbist.h
HL_reg_stc.h
HL_reg_efc.h
HL_reg_pcr.h
HL_reg_pmm.h
HL_reg_dma.h
HL_reg_ccmr5.h
HL_sys_core.h
HL_system.h
HL_sys_vim.h
HL_sys_mpu.h
HL_sys_pmu.h
HL_sys_pcr.h
HL_sys_pmm.h
HL_sys_dma.h
HL_sys_core.asm
HL_sys_intvecs.asm
HL_sys_mpu.asm
HL_sys_pmu.asm
HL_sys_pcr.c
HL_sys_pmm.c
HL_sys_dma.c
HL_system.c
HL_sys_phantom.c

작업이 완료되었으니 F5 눌러서 코드 생성


Properties for het_pwm
type filter text
Resource
General
Build
ARM Compiler
Processor Options
Optimization
Include Options
ULP Advisor
Predefined Symbols
Advanced Options
ARM Linker
ARM Hex Utility [Disabled]
Debug
Include Options
Manage Configurations...
Show advanced settings
Cancel
Apply and Close

Configuration: Debug [Active]

Add dir to #include search path (--include_path, -I)

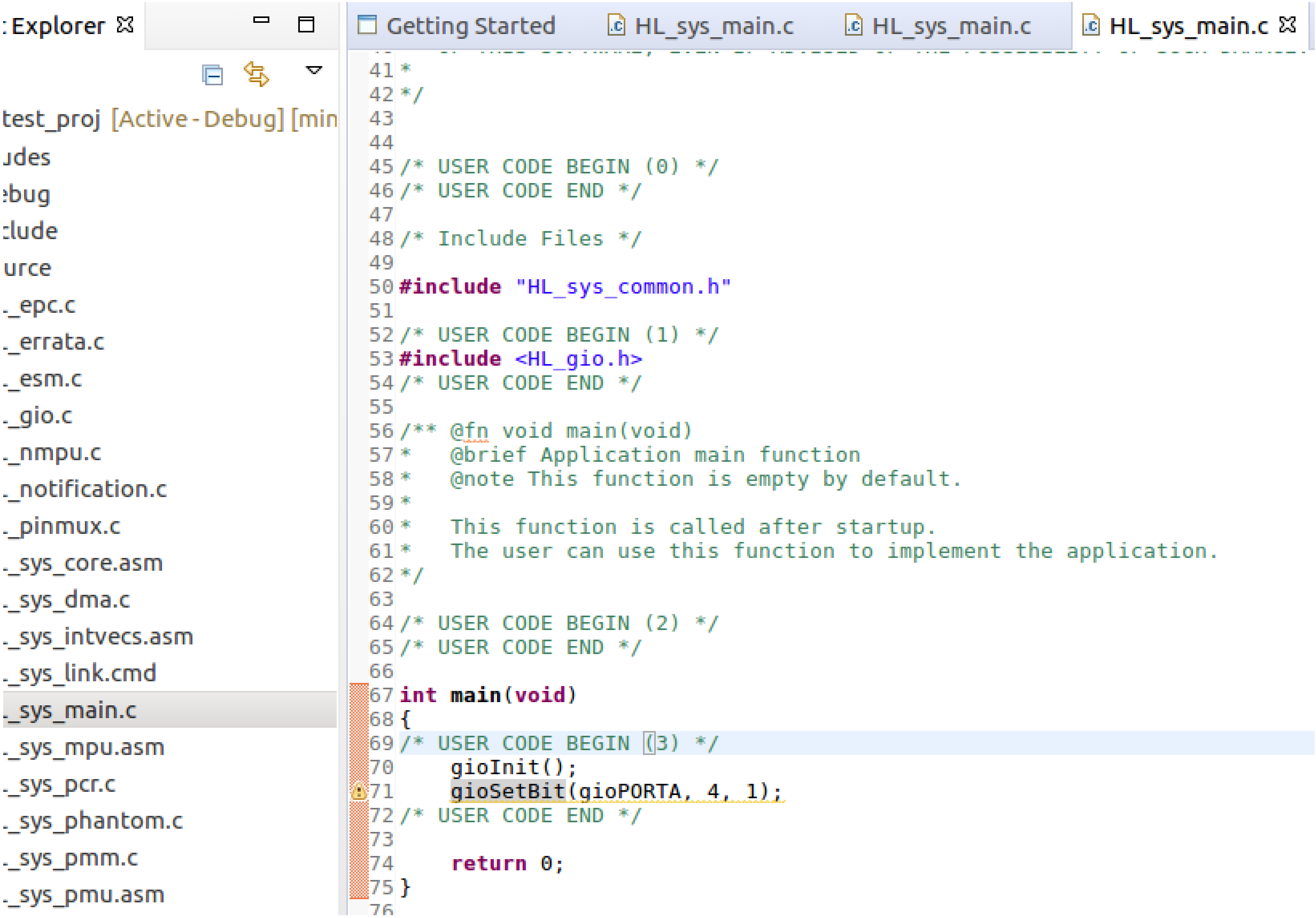
${PROJECT_ROOT}

"${workspace_loc:/${ProjName}/include}"

${CG_TOOL_ROOT}/include

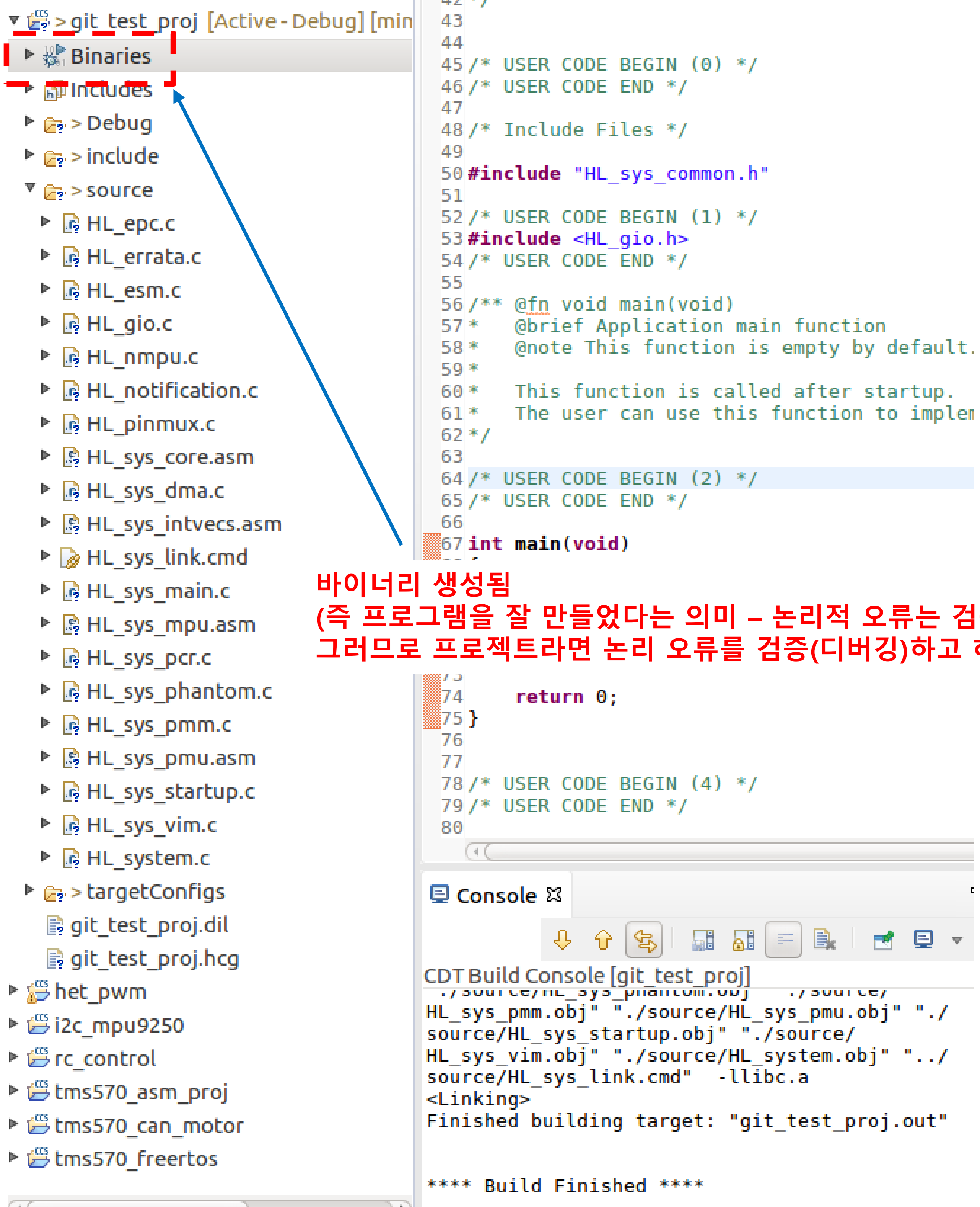
경로 설정

Specify a preinclude file (--preinclude)



test_proj [Active - Debug] [min
ıdes
ebug
clude
urce
._epc.c
._errata.c
._esm.c
._gio.c
._nmpu.c
._notification.c
._pinmux.c
._sys_core.asm
._sys_dma.c
._sys_intvecs.asm
._sys_link.cmd
._sys_main.c
._sys_mpu.asm
._sys_pcr.c
._sys_phantom.c
._sys_pmm.c
._sys_pmu.asm

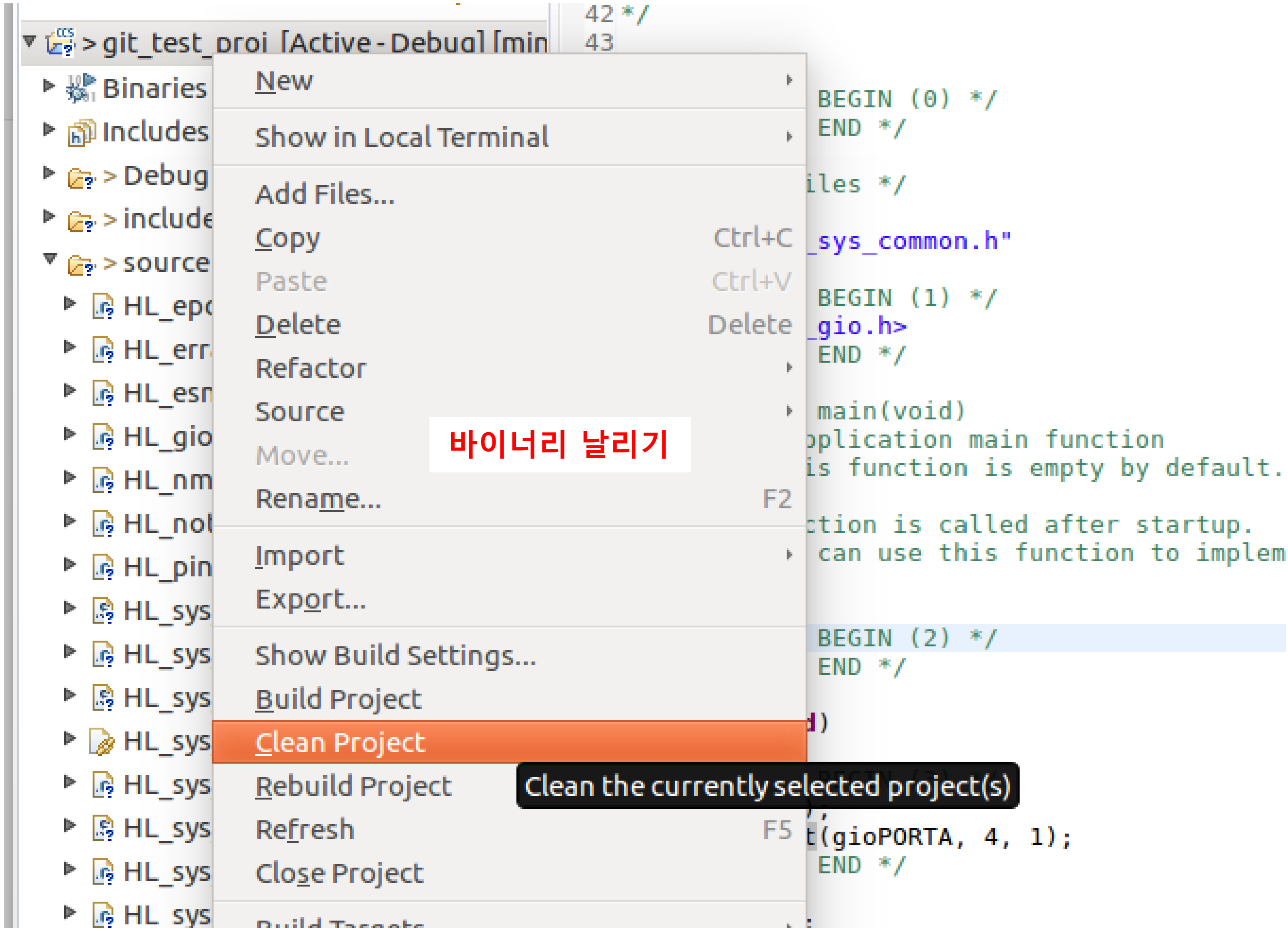
```
*
*/


/* USER CODE BEGIN (0) */
/* USER CODE END */

/* Include Files */

#include "HL_sys_common.h"

/* USER CODE BEGIN (1) */
#include <HL_gio.h>
/* USER CODE END */

/** @fn void main(void)
*   @brief Application main function
*   @note This function is empty by default.
*
*   This function is called after startup.
*   The user can use this function to implement the application.
*/

/* USER CODE BEGIN (2) */
/* USER CODE END */

int main(void)
{
/* USER CODE BEGIN (3) */
    gioInit();
    gioSetBit(gioPORTA, 4, 1);
/* USER CODE END */

    return 0;
}
```


git_test_proj [Active - Debug] [min
Binaries
Includes
Debug
include
source
HL_epc.c
HL_errata.c
HL_esm.c
HL_gio.c
HL_nmpu.c
HL_notification.c
HL_pinmux.c
HL_sys_core.asm
HL_sys_dma.c
HL_sys_intvecs.asm
HL_sys_link.cmd
HL_sys_main.c
HL_sys_mpu.asm
HL_sys_pcr.c
HL_sys_phantom.c
HL_sys_pmm.c
HL_sys_pmu.asm
HL_sys_startup.c
HL_sys_vim.c
HL_system.c
targetConfigs
git_test_proj.dil
git_test_proj.hcg
het_pwm
i2c_mpu9250
rc_control
tms570_asm_proj
tms570_can_motor
tms570_freertos
Console
CDT Build Console [git_test_proj]

```
/* USER CODE BEGIN (0) */
/* USER CODE END */

/* Include Files */

#include "HL_sys_common.h"

/* USER CODE BEGIN (1) */
#include <HL_gio.h>
/* USER CODE END */

/** @fn void main(void)
*   @brief Application main function
*   @note This function is empty by default.
*
*   This function is called after startup.
*   The user can use this function to implem
*/

/* USER CODE BEGIN (2) */
/* USER CODE END */

int main(void)

    return 0;
}


/* USER CODE BEGIN (4) */
/* USER CODE END */
```

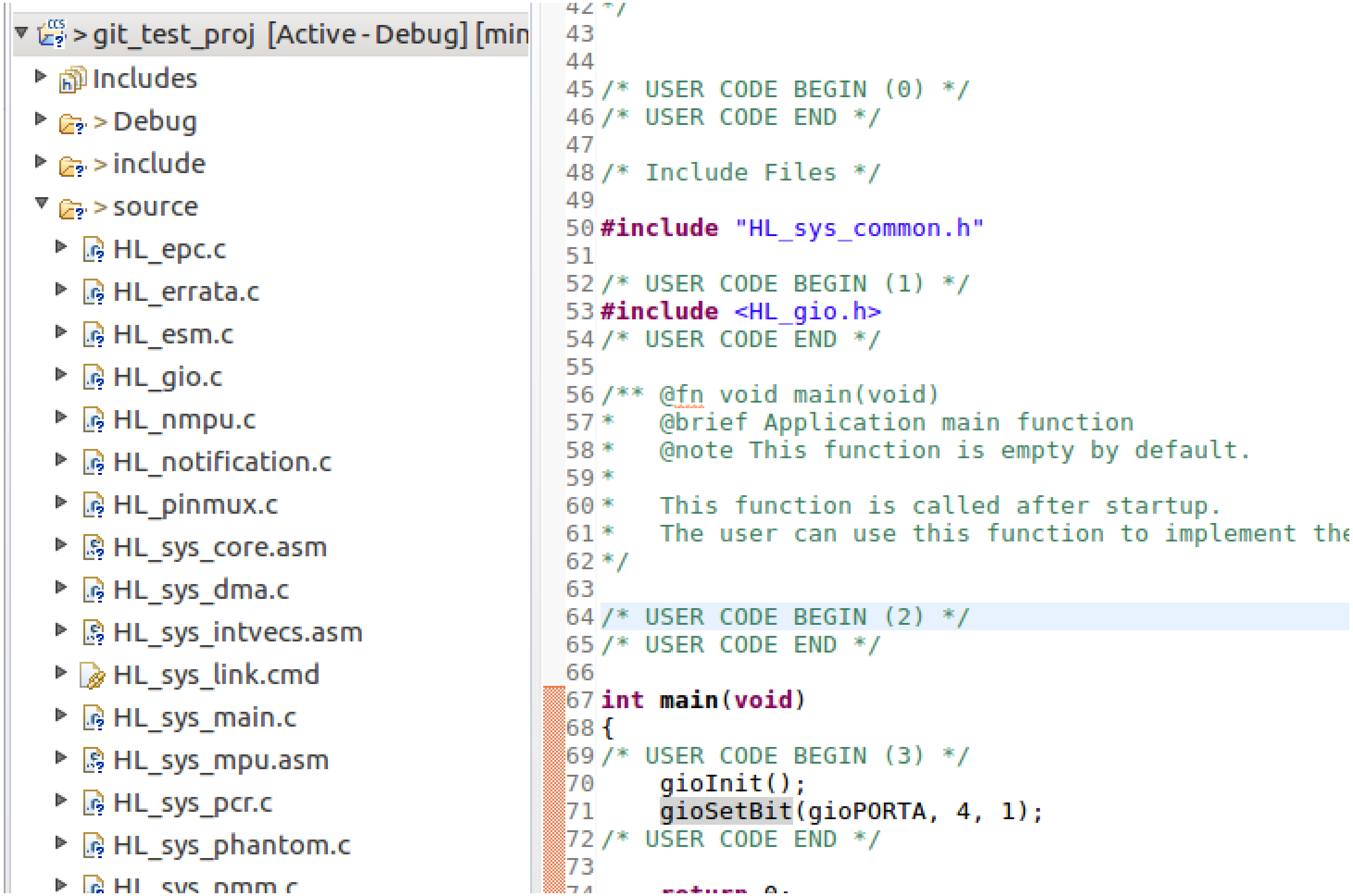
```
HL_sys_pmm.obj" "./source/HL_sys_pmu.obj" "./
source/HL_sys_startup.obj" "./source/
HL_sys_vim.obj" "./source/HL_system.obj" "../
source/HL_sys_link.cmd"  -llibc.a
<Linking>
Finished building target: "git_test_proj.out"


**** Build Finished ****
```

바이너리 생성됨
(즉 프로그램을 잘 만들었다는 의미 – 논리적 오류는 검증이 안된 상태)
그러므로 프로젝트라면 논리 오류를 검증(디버깅)하고 하도록!


> git_test_proj [Active - Debug] [min
Binaries
Includes
> Debug
> include
> source
HL_epc
HL_err
HL_esm
HL_gio
HL_nm
HL_not
HL_pin
HL_sys
HL_sys
HL_sys
HL_sys
HL_sys
HL_sys
HL_sys
HL_sys
New
Show in Local Terminal
Add Files...
Copy Ctrl+C
Paste Ctrl+V
Delete Delete
Refactor
Source
Move...
Rename... F2
Import
Export...
Show Build Settings...
Build Project
Clean Project
Rebuild Project
Refresh F5
Close Project
Build Targets
42 */
43
BEGIN (0) */
END */
iles */
_sys_common.h"
BEGIN (1) */
_gio.h>
END */
main(void)
pplication main function
is function is empty by default.
ction is called after startup.
can use this function to implem
BEGIN (2) */
END */
d)
t(gioPORTA, 4, 1);
END */

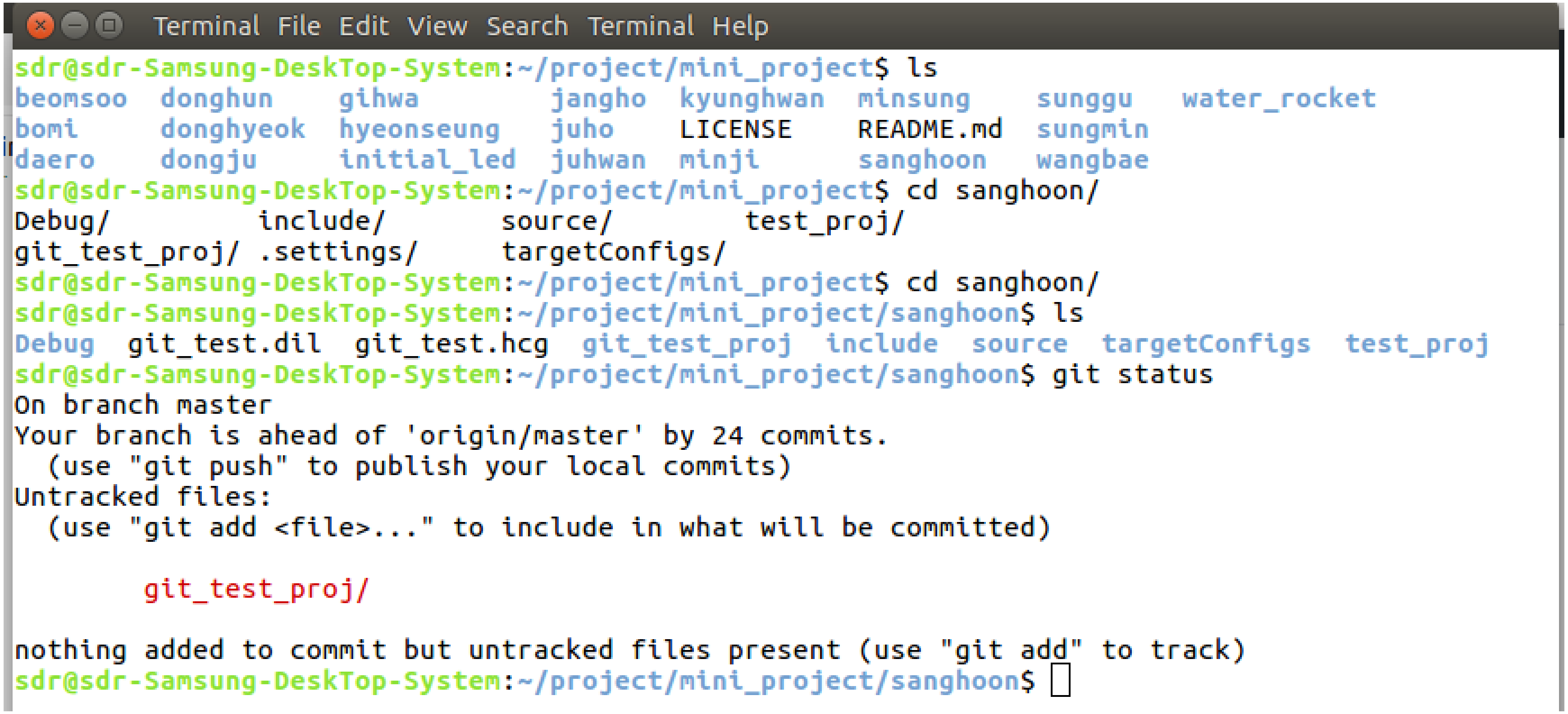
바이너리 날리기

Clean the currently selected project(s)


> git_test_proj [Active - Debug] [min
Includes
> Debug
> include
> source
HL_epc.c
HL_errata.c
HL_esm.c
HL_gio.c
HL_nmpu.c
HL_notification.c
HL_pinmux.c
HL_sys_core.asm
HL_sys_dma.c
HL_sys_intvecs.asm
HL_sys_link.cmd
HL_sys_main.c
HL_sys_mpu.asm
HL_sys_pcr.c
HL_sys_phantom.c
HL_sys_pmm.c

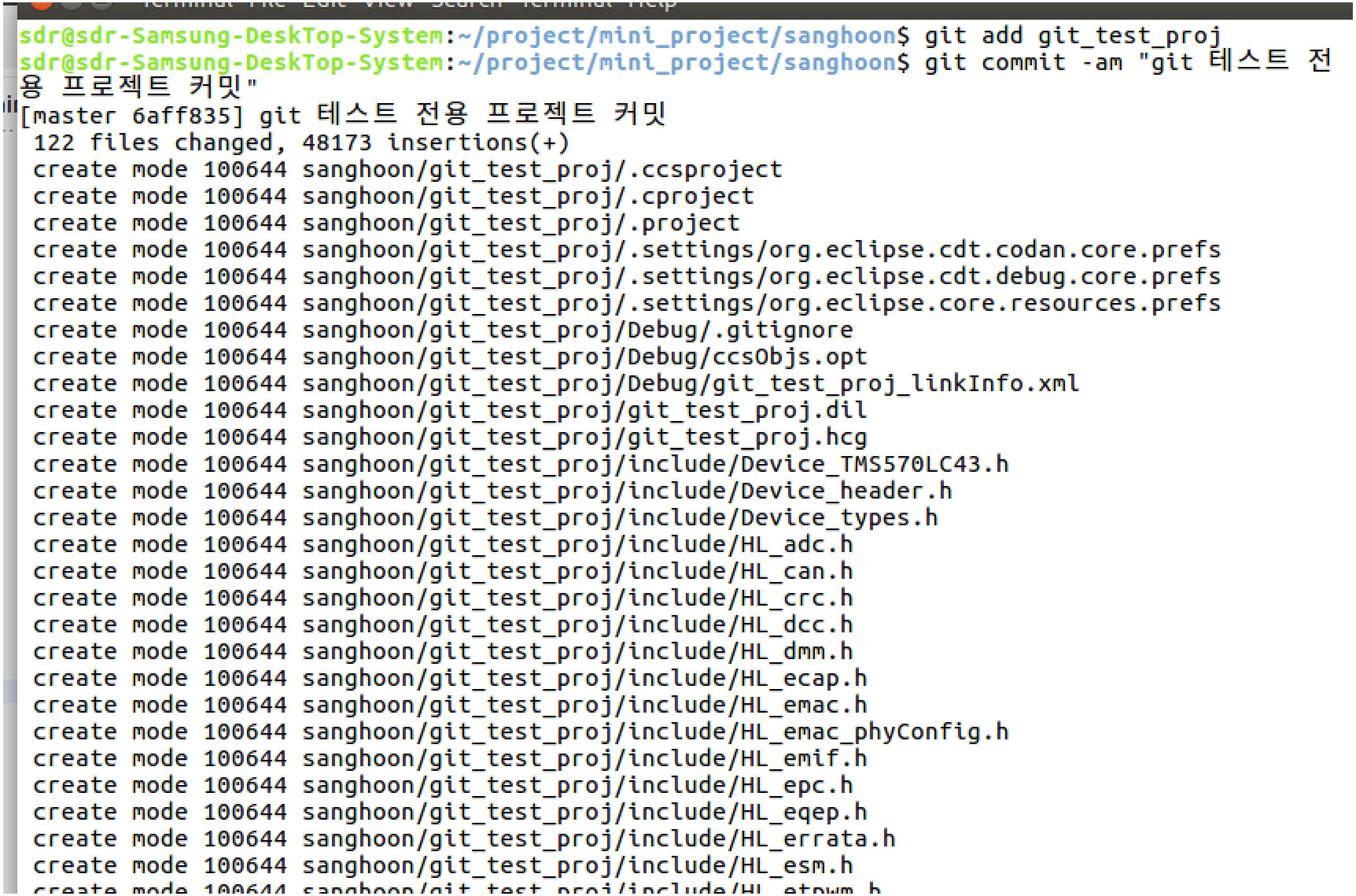
```
*/


/* USER CODE BEGIN (0) */
/* USER CODE END */

/* Include Files */

#include "HL_sys_common.h"

/* USER CODE BEGIN (1) */
#include <HL_gio.h>
/* USER CODE END */

/** @fn void main(void)
*   @brief Application main function
*   @note This function is empty by default.
*
*   This function is called after startup.
*   The user can use this function to implement the
*/

/* USER CODE BEGIN (2) */
/* USER CODE END */

int main(void)
{
/* USER CODE BEGIN (3) */
    gioInit();
    gioSetBit(gioPORTA, 4, 1);
/* USER CODE END */

    return 0;
```

이제 새로운 폴더와 작업 저장소를 만들었으니 git 에 추가해준다.



```
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project$ ls
beomsoo   donghun     gihwa         jangho  kyunghwan  minsung    sunggu   water_rocket
bomi      donghyeok   hyeonseung    juho    LICENSE    README.md  sungmin
daero     dongju      initial_led   juhwan  minji      sanghoon   wangbae
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project$ cd sanghoon/
Debug/         include/       source/        test_proj/
git_test_proj/ .settings/     targetConfigs/
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project$ cd sanghoon/
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project/sanghoon$ ls
Debug  git_test.dil  git_test.hcg  git_test_proj  include  source  targetConfigs  test_proj
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project/sanghoon$ git status
On branch master
Your branch is ahead of 'origin/master' by 24 commits.
  (use "git push" to publish your local commits)
Untracked files:
  (use "git add <file>..." to include in what will be committed)

        git_test_proj/

nothing added to commit but untracked files present (use "git add" to track)
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project/sanghoon$ 
```

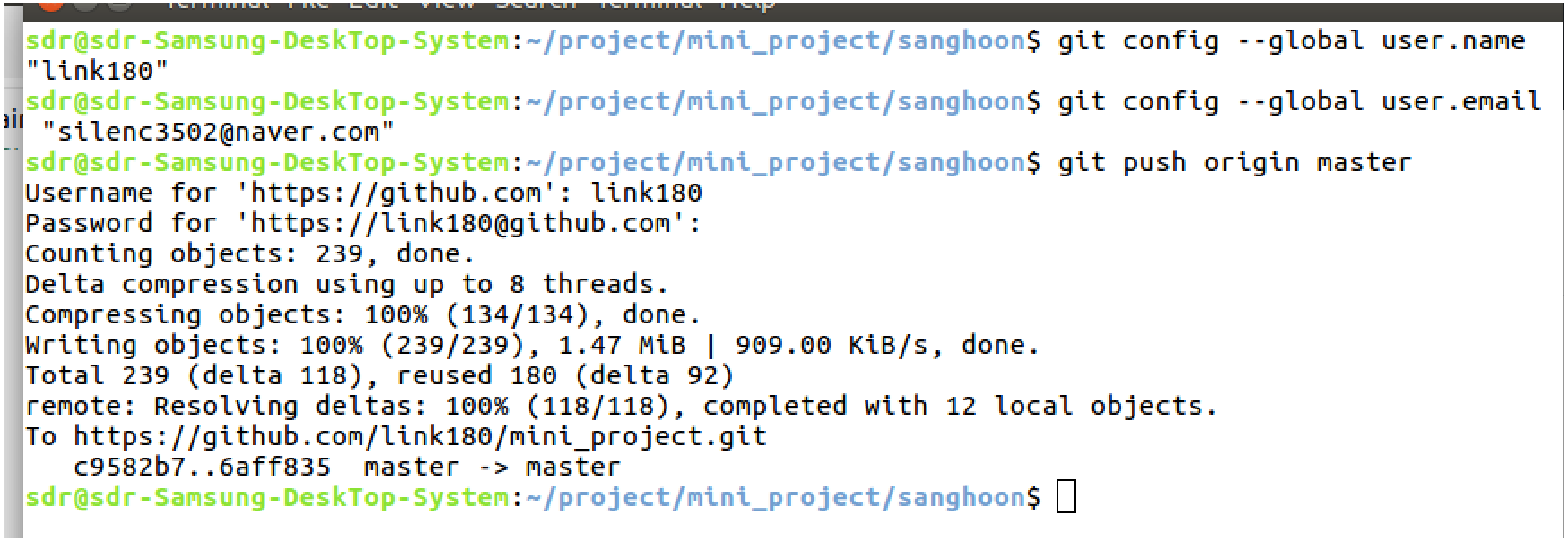
어떤 작업을 했는지 간략한 요약을 commit 메시지에 넣는다.



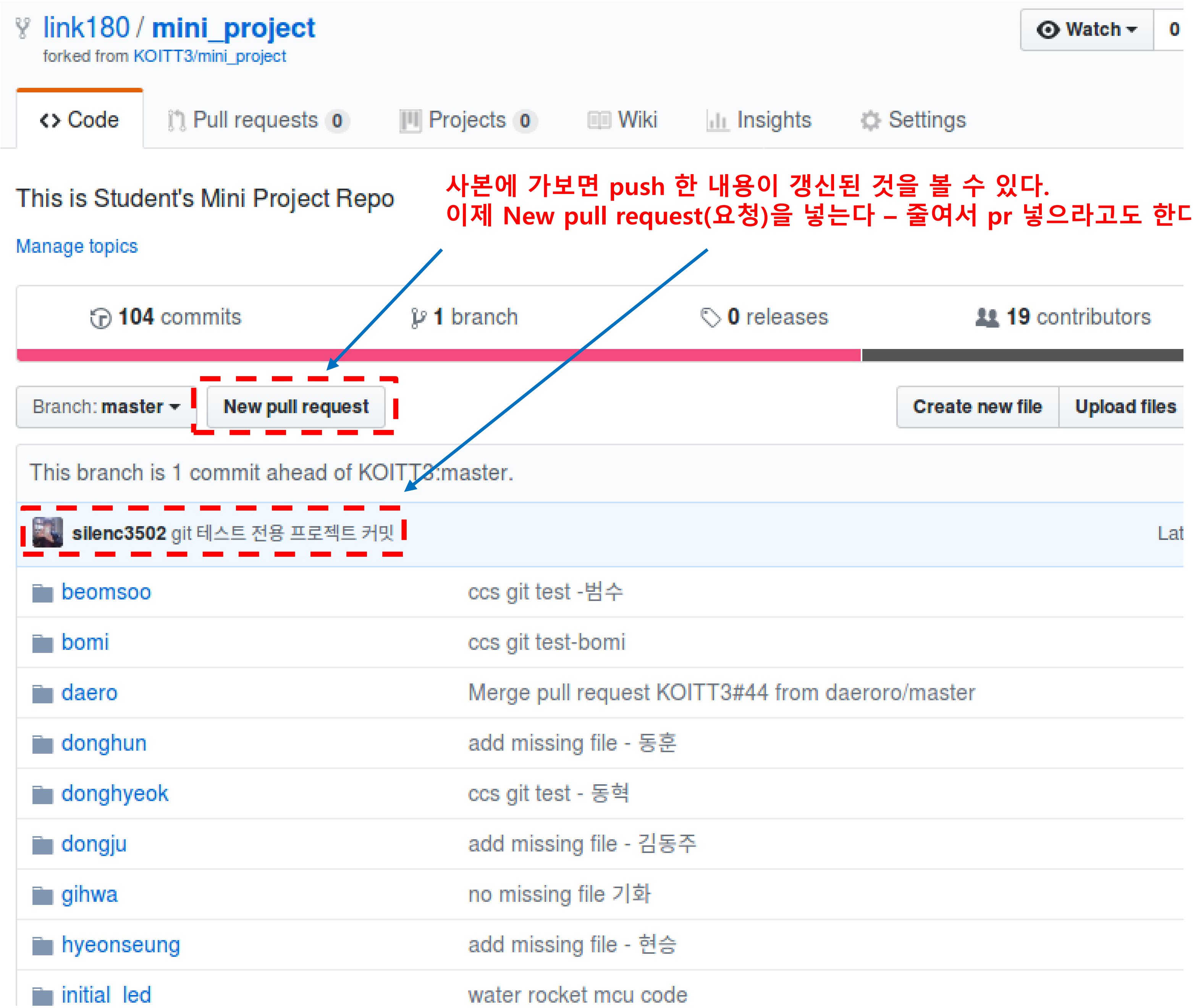
```
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project/sanghoon$ git add git_test_proj
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project/sanghoon$ git commit -am "git 테스트 전
용 프로젝트 커밋"
[master 6aff835] git 테스트 전용 프로젝트 커밋
 122 files changed, 48173 insertions(+)
 create mode 100644 sanghoon/git_test_proj/.ccsproject
 create mode 100644 sanghoon/git_test_proj/.cproject
 create mode 100644 sanghoon/git_test_proj/.project
 create mode 100644 sanghoon/git_test_proj/.settings/org.eclipse.cdt.codan.core.prefs
 create mode 100644 sanghoon/git_test_proj/.settings/org.eclipse.cdt.debug.core.prefs
 create mode 100644 sanghoon/git_test_proj/.settings/org.eclipse.core.resources.prefs
 create mode 100644 sanghoon/git_test_proj/Debug/.gitignore
 create mode 100644 sanghoon/git_test_proj/Debug/ccsObjs.opt
 create mode 100644 sanghoon/git_test_proj/Debug/git_test_proj_linkInfo.xml
 create mode 100644 sanghoon/git_test_proj/git_test_proj.dil
 create mode 100644 sanghoon/git_test_proj/git_test_proj.hcg
 create mode 100644 sanghoon/git_test_proj/include/Device_TMS570LC43.h
 create mode 100644 sanghoon/git_test_proj/include/Device_header.h
 create mode 100644 sanghoon/git_test_proj/include/Device_types.h
 create mode 100644 sanghoon/git_test_proj/include/HL_adc.h
 create mode 100644 sanghoon/git_test_proj/include/HL_can.h
 create mode 100644 sanghoon/git_test_proj/include/HL_crc.h
 create mode 100644 sanghoon/git_test_proj/include/HL_dcc.h
 create mode 100644 sanghoon/git_test_proj/include/HL_dmm.h
 create mode 100644 sanghoon/git_test_proj/include/HL_ecap.h
 create mode 100644 sanghoon/git_test_proj/include/HL_emac.h
 create mode 100644 sanghoon/git_test_proj/include/HL_emac_phyConfig.h
 create mode 100644 sanghoon/git_test_proj/include/HL_emif.h
 create mode 100644 sanghoon/git_test_proj/include/HL_epc.h
 create mode 100644 sanghoon/git_test_proj/include/HL_eqep.h
 create mode 100644 sanghoon/git_test_proj/include/HL_errata.h
 create mode 100644 sanghoon/git_test_proj/include/HL_esm.h
 create mode 100644 sanghoon/git_test_proj/include/HL_etpwm.h
```

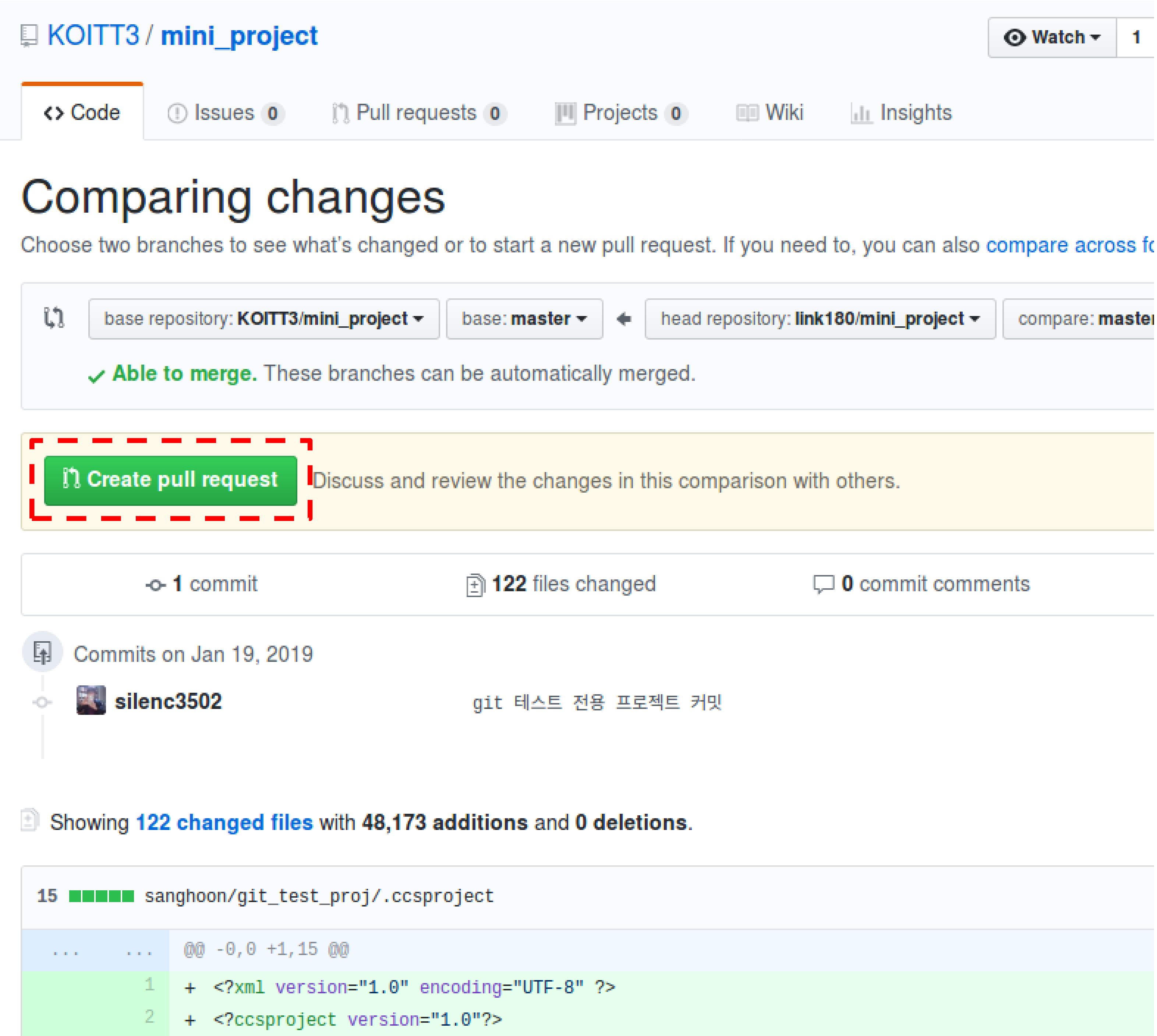
집 컴퓨터에서 하는 사람들은 계정 설정이 안되어 있을테니 계정 설정을 한다.
작업 당사자들의 계정으로 설정하길 바람

노트북에서 했던 사람들은 git push origin master 만 하면 된다.



```
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project/sanghoon$ git config --global user.name
"link180"
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project/sanghoon$ git config --global user.email
 "silenc3502@naver.com"
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project/sanghoon$ git push origin master
Username for 'https://github.com': link180
Password for 'https://link180@github.com':
Counting objects: 239, done.
Delta compression using up to 8 threads.
Compressing objects: 100% (134/134), done.
Writing objects: 100% (239/239), 1.47 MiB | 909.00 KiB/s, done.
Total 239 (delta 118), reused 180 (delta 92)
remote: Resolving deltas: 100% (118/118), completed with 12 local objects.
To https://github.com/link180/mini_project.git
   c9582b7..6aff835  master -> master
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project/sanghoon$ 
```




KOITT3 / mini_project
Watch
1
Code
Issues 0
Pull requests 0
Projects 0
Wiki
Insights

Comparing changes

Choose two branches to see what's changed or to start a new pull request. If you need to, you can also compare across fo

base repository: **KOITT3/mini_project** base: **master** head repository: **link180/mini_project** compare: **maste**

✓ **Able to merge.** These branches can be automatically merged.

Create pull request

Discuss and review the changes in this comparison with others.

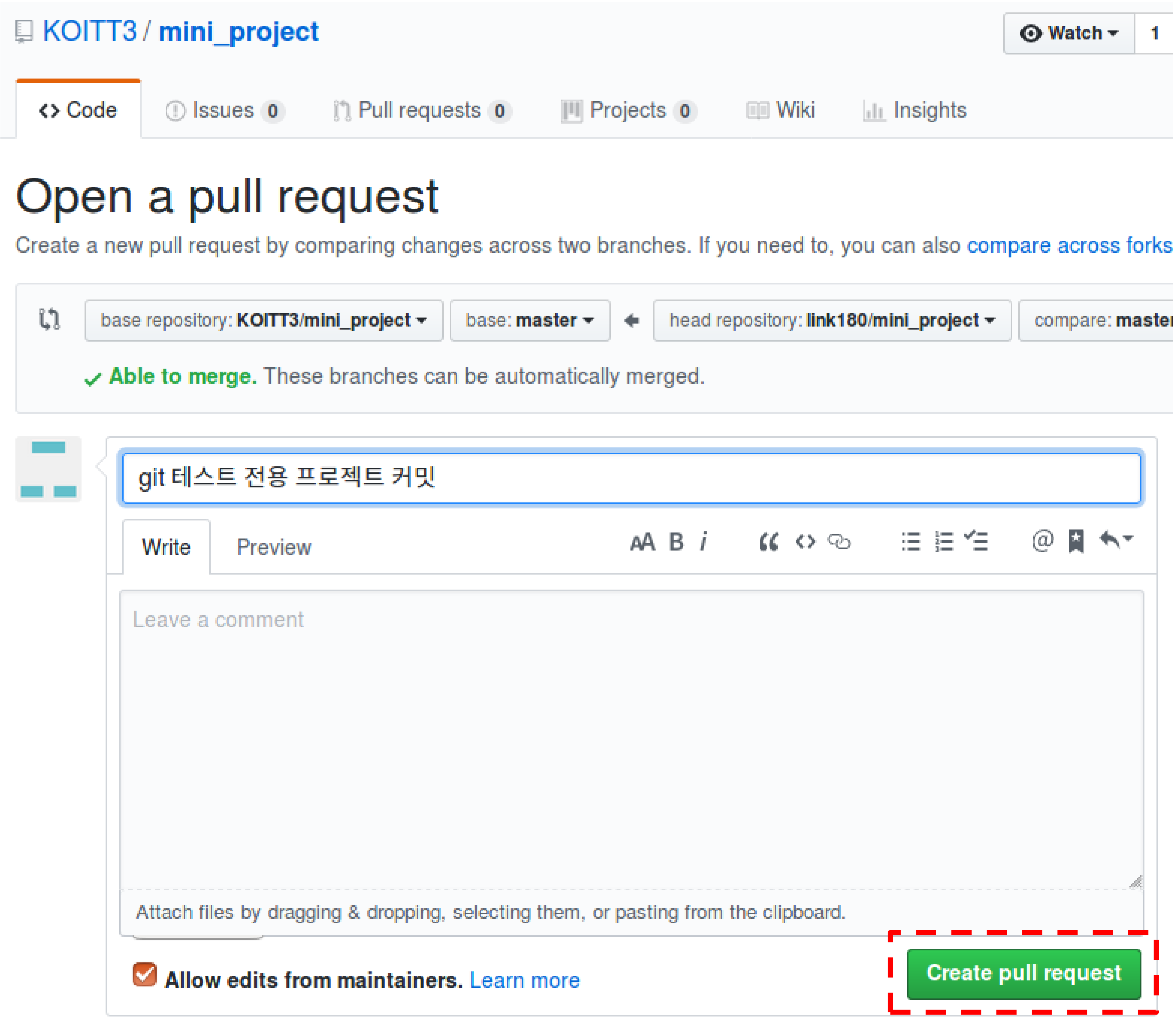
1 commit **122** files changed **0** commit comments

Commits on Jan 19, 2019

silenc3502 git 테스트 전용 프로젝트 커밋

Showing **122 changed files** with **48,173 additions** and **0 deletions**.

```
15 sanghoon/git_test_proj/.ccsproject
...   ...   @@ -0,0 +1,15 @@
        1   + <?xml version="1.0" encoding="UTF-8" ?>
        2   + <?ccsproject version="1.0"?>
```


KOITT3 / mini_project
Watch
1
Code
Issues 0
Pull requests 0
Projects 0
Wiki
Insights
Write
Preview
Leave a comment
Create pull request

Open a pull request

Create a new pull request by comparing changes across two branches. If you need to, you can also compare across forks

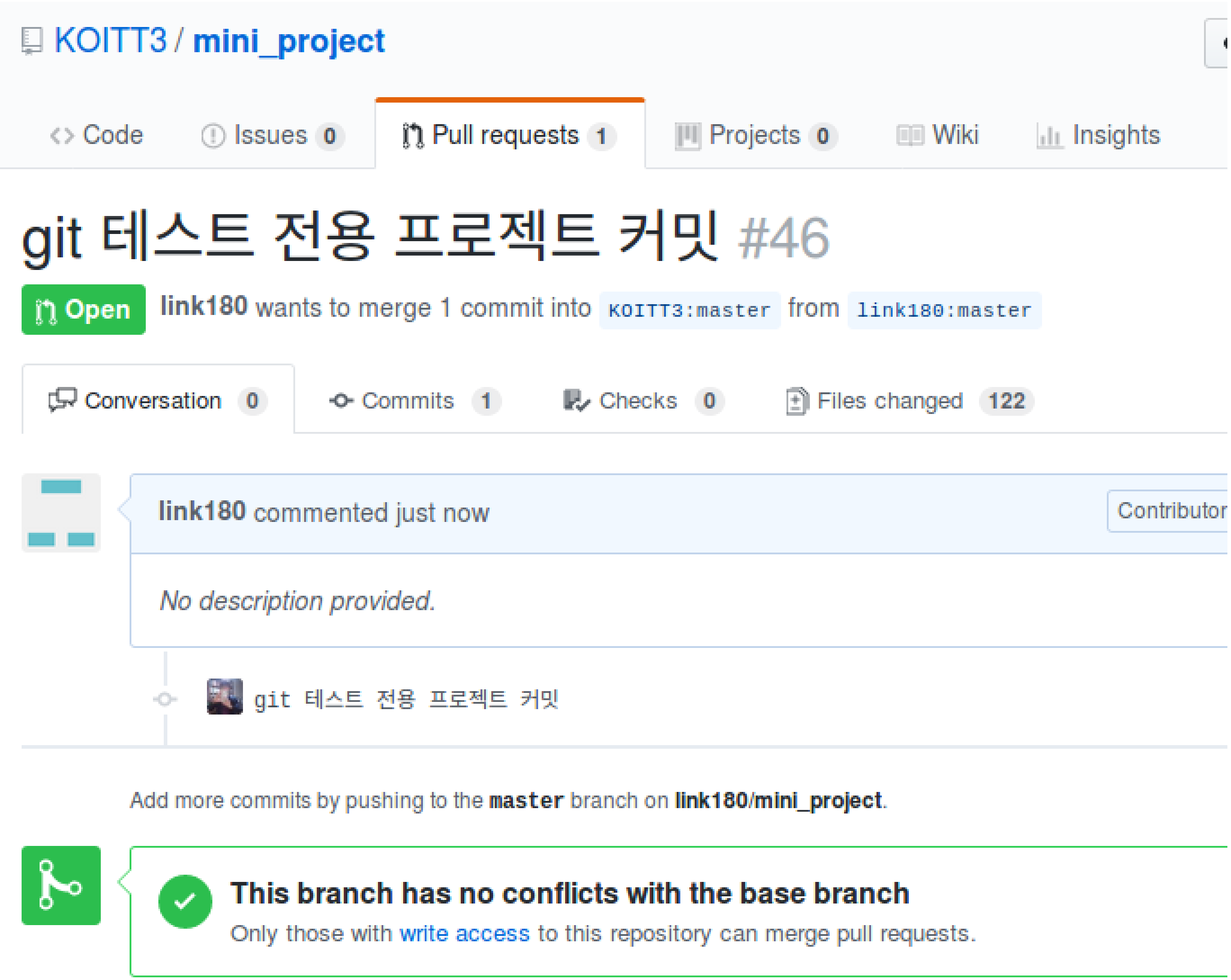
base repository: **KOITT3/mini_project** base: **master** ← head repository: **link180/mini_project** compare: **maste**

✓ **Able to merge.** These branches can be automatically merged.

git 테스트 전용 프로젝트 커밋

Attach files by dragging & dropping, selecting them, or pasting from the clipboard.

☑ **Allow edits from maintainers.** Learn more


KOITT3 / mini_project
Code
Issues 0
Pull requests 1
Projects 0
Wiki
Insights

git 테스트 전용 프로젝트 커밋 #46

Open link180 wants to merge 1 commit into `KOITT3:master` from `link180:master`

Conversation 0
Commits 1
Checks 0
Files changed 122

link180 commented just now

Contributor

No description provided.

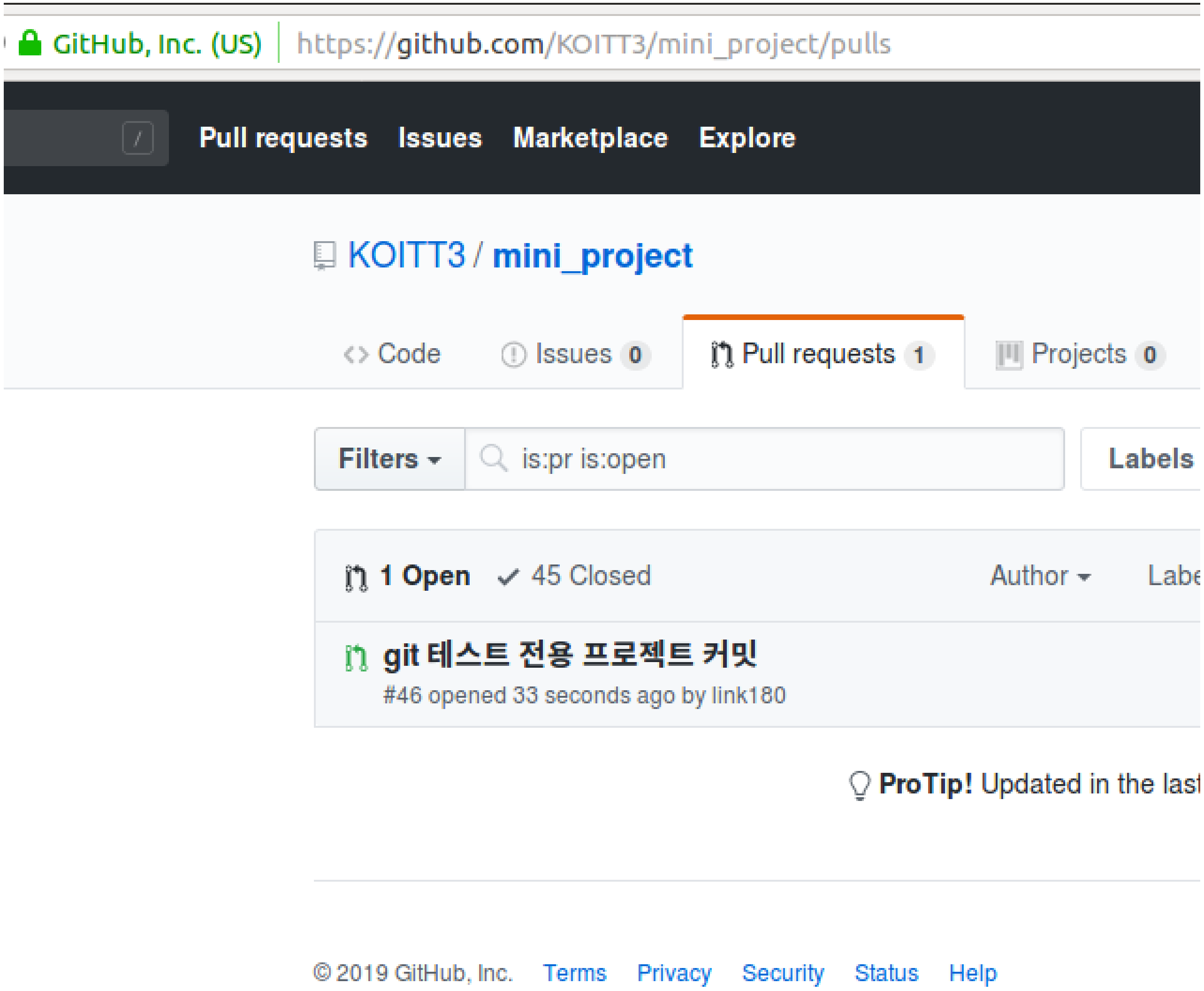
git 테스트 전용 프로젝트 커밋

Add more commits by pushing to the **master** branch on **link180/mini_project**.

This branch has no conflicts with the base branch

Only those with write access to this repository can merge pull requests.

원본쪽에 pr 이 잘 들어간 것을 볼 수 있다.

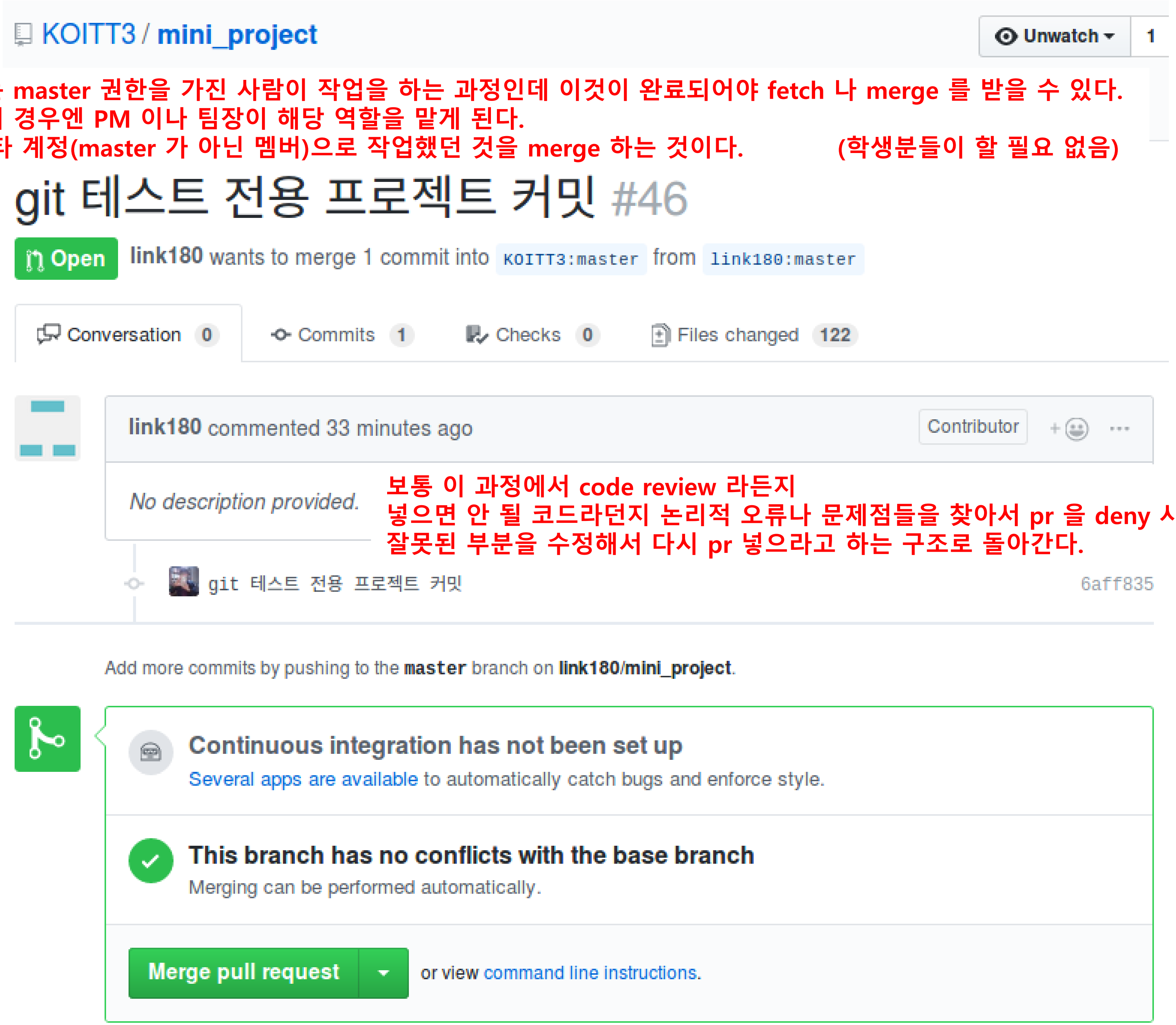


GitHub, Inc. (US)
https://github.com/KOITT3/mini_project/pulls
Pull requests
Issues
Marketplace
Explore
KOITT3 / mini_project
Code
Issues 0
Pull requests 1
Projects 0
Filters
is:pr is:open
Labels
1 Open
45 Closed
Author
Labe

git 테스트 전용 프로젝트 커밋

#46 opened 33 seconds ago by link180

ProTip! Updated in the last
© 2019 GitHub, Inc.
Terms
Privacy
Security
Status
Help



KOITT3 / **mini_project**

Unwatch ▾
1

여기는 master 권한을 가진 사람이 작업을 하는 과정인데 이것이 완료되어야 fetch 나 merge 를 받을 수 있다.
회사의 경우엔 PM 이나 팀장이 해당 역할을 맡게 된다.
아까 타 계정(master 가 아닌 멤버)으로 작업했던 것을 merge 하는 것이다. (학생분들이 할 필요 없음)

git 테스트 전용 프로젝트 커밋 #46

Open **link180** wants to merge 1 commit into `KOITT3:master` from `link180:master`

Conversation 0
Commits 1
Checks 0
Files changed 122

link180 commented 33 minutes ago

Contributor

No description provided.

보통 이 과정에서 code review 라든지
넣으면 안 될 코드라던지 논리적 오류나 문제점들을 찾아서 pr 을 deny 시키고
잘못된 부분을 수정해서 다시 pr 넣으라고 하는 구조로 돌아간다.

`git 테스트 전용 프로젝트 커밋` 6aff835

Add more commits by pushing to the **master** branch on **link180/mini_project**.

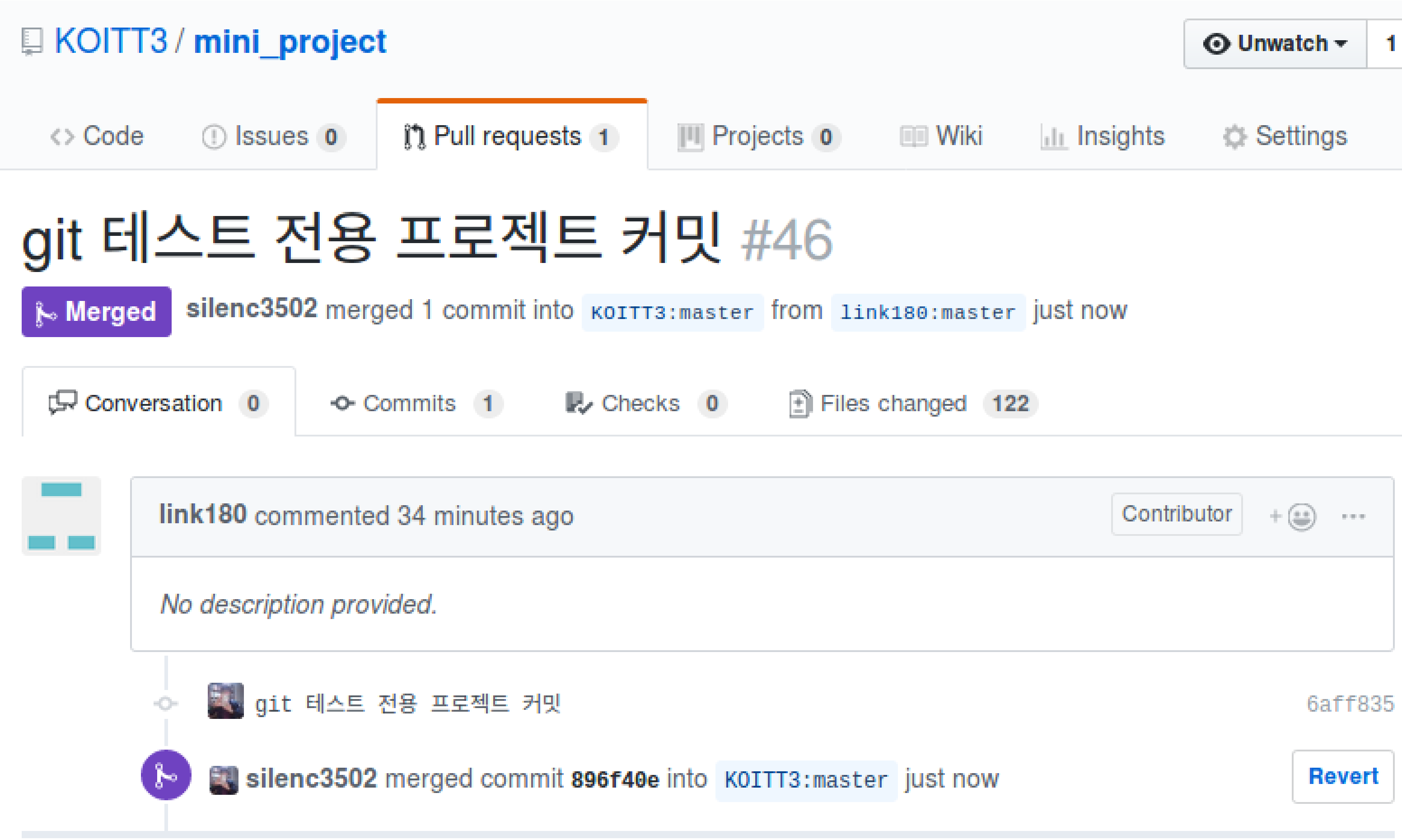
Continuous integration has not been set up
Several apps are available to automatically catch bugs and enforce style.

This branch has no conflicts with the base branch
Merging can be performed automatically.

Merge pull request ▾

or view command line instructions.

Merge 가 완료된 모습



KOITT3 / mini_project

git 테스트 전용 프로젝트 커밋 #46

Merged **silenc3502** merged 1 commit into `KOITT3:master` from `link180:master` just now

link180 commented 34 minutes ago

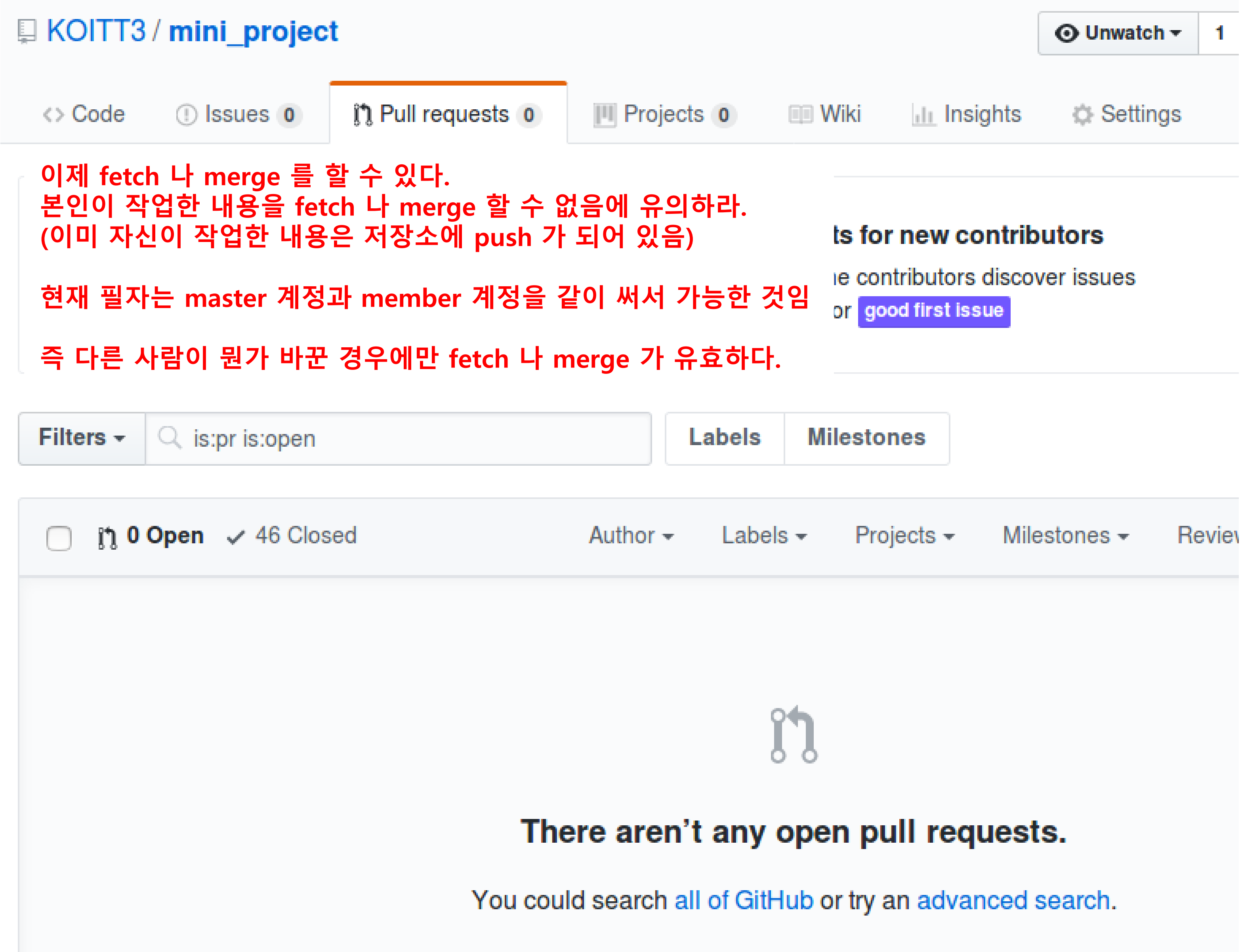
No description provided.

git 테스트 전용 프로젝트 커밋 6aff835

silenc3502 merged commit **896f40e** into `KOITT3:master` just now

Unwatch
Code
Issues 0
Pull requests 1
Projects 0
Wiki
Insights
Settings
Conversation 0
Commits 1
Checks 0
Files changed 122
Contributor
Revert

앞서 요청했던 pr 1 개가 사라진 것을 볼 수 있다.



이제 fetch 나 merge 를 할 수 있다.
본인이 작업한 내용을 fetch 나 merge 할 수 없음에 유의하라.
(이미 자신이 작업한 내용은 저장소에 push 가 되어 있음)

현재 필자는 master 계정과 member 계정을 같이 써서 가능한 것임

즉 다른 사람이 뭔가 바꾼 경우에만 fetch 나 merge 가 유효하다.

KOITT3 / mini_project

ts for new contributors

ıe contributors discover issues

or good first issue

is:pr is:open

There aren’t any open pull requests.

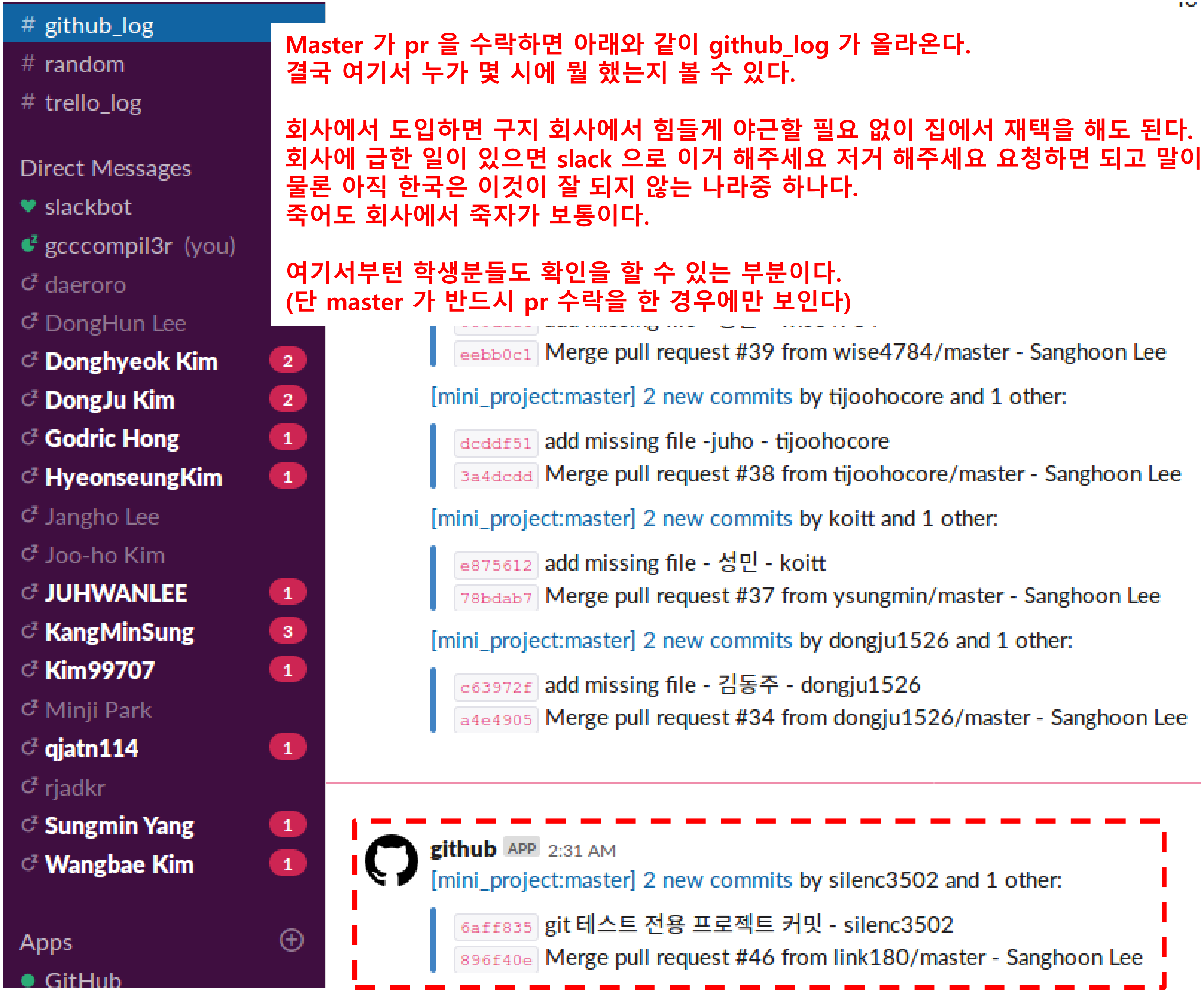
You could search all of GitHub or try an advanced search.

Unwatch ▾
1
Code
Issues 0
Pull requests 0
Projects 0
Wiki
Insights
Settings
Filters ▾
Labels
Milestones
☐
0 Open
✓ 46 Closed
Author ▾
Labels ▾
Projects ▾
Milestones ▾
Reviev

Master 가 pr 을 수락하면 아래와 같이 github_log 가 올라온다.
결국 여기서 누가 몇 시에 뭘 했는지 볼 수 있다.

회사에서 도입하면 구지 회사에서 힘들게 야근할 필요 없이 집에서 재택을 해도 된다.
회사에 급한 일이 있으면 slack 으로 이거 해주세요 저거 해주세요 요청하면 되고 말이다.
물론 아직 한국은 이것이 잘 되지 않는 나라중 하나다.
죽어도 회사에서 죽자가 보통이다.

여기서부턴 학생분들도 확인을 할 수 있는 부분이다.
(단 master 가 반드시 pr 수락을 한 경우에만 보인다)



# github_log
# random
# trello_log
Direct Messages
slackbot
gcccompil3r (you)
daeroro
DongHun Lee
Donghyeok Kim 2
DongJu Kim 2
Godric Hong 1
HyeonseungKim 1
Jangho Lee
Joo-ho Kim
JUHWANLEE 1
KangMinSung 3
Kim99707 1
Minji Park
qjatn114 1
rjadkr
Sungmin Yang 1
Wangbae Kim 1
Apps
GitHub

`eebb0c1` Merge pull request #39 from wise4784/master - Sanghoon Lee

[mini_project:master] 2 new commits by tijoohocore and 1 other:

`dcddf51` add missing file -juho - tijoohocore
`3a4dcdd` Merge pull request #38 from tijoohocore/master - Sanghoon Lee

[mini_project:master] 2 new commits by koitt and 1 other:

`e875612` add missing file - 성민 - koitt
`78bdab7` Merge pull request #37 from ysungmin/master - Sanghoon Lee

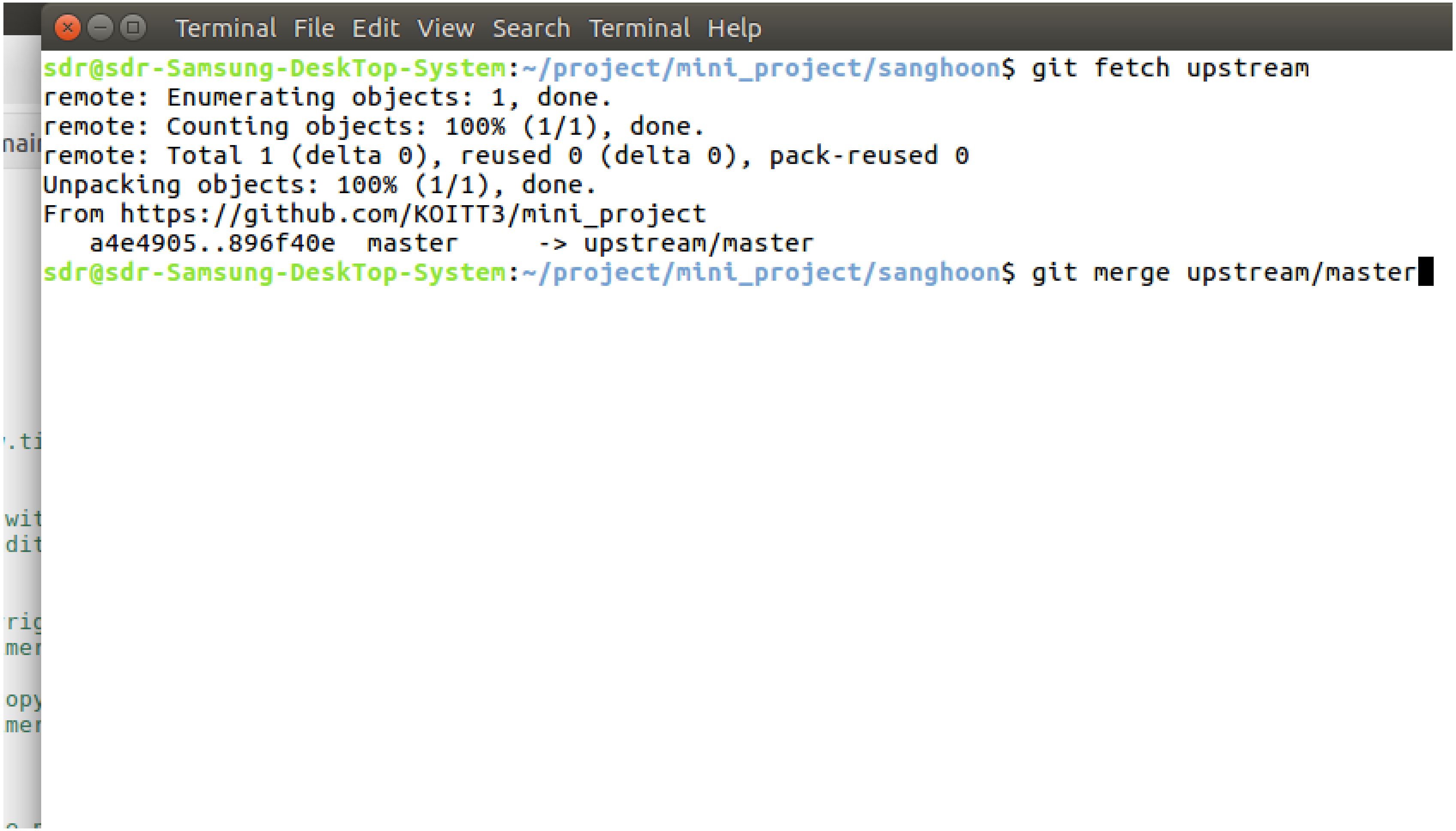
[mini_project:master] 2 new commits by dongju1526 and 1 other:

`c63972f` add missing file - 김동주 - dongju1526
`a4e4905` Merge pull request #34 from dongju1526/master - Sanghoon Lee

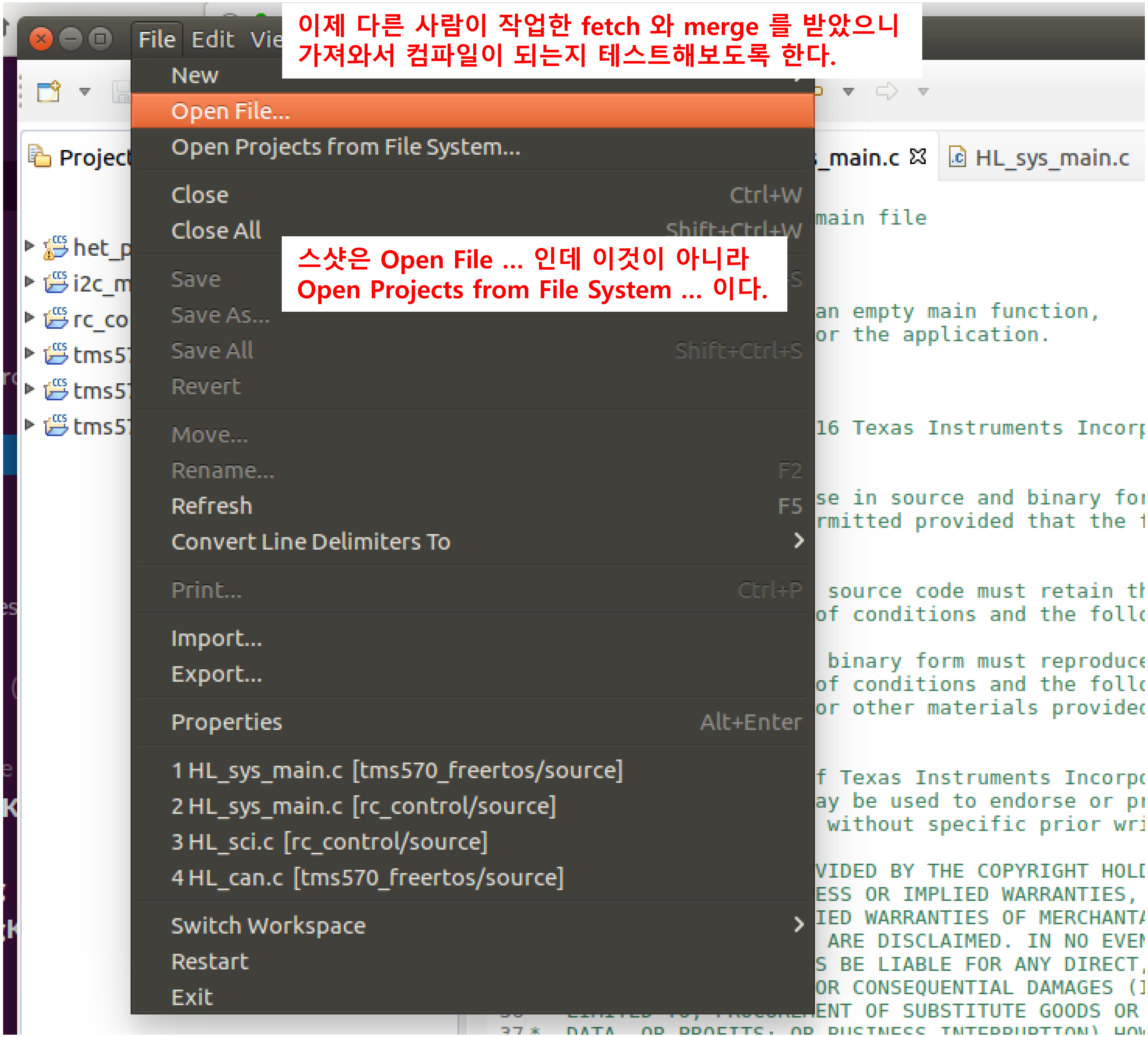
github APP 2:31 AM
[mini_project:master] 2 new commits by silenc3502 and 1 other:

`6aff835` git 테스트 전용 프로젝트 커밋 - silenc3502
`896f40e` Merge pull request #46 from link180/master - Sanghoon Lee

본인이 한 내용이 아닌 팀원이나 반에 다른 학우분들이 pr 한 것이 수락되었다면 fetch 와 merge 를 받을 수 있다.

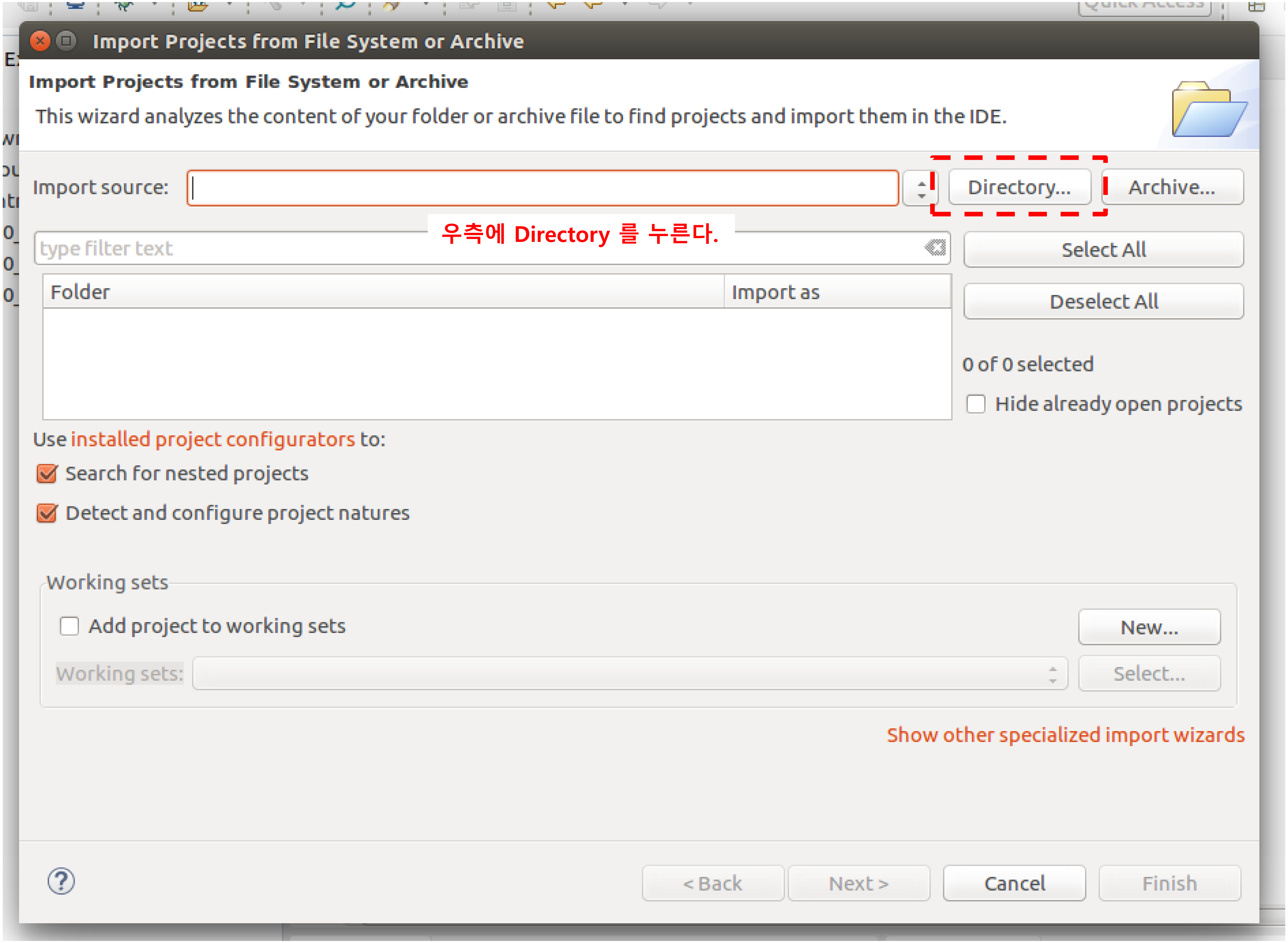


```
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project/sanghoon$ git fetch upstream
remote: Enumerating objects: 1, done.
remote: Counting objects: 100% (1/1), done.
remote: Total 1 (delta 0), reused 0 (delta 0), pack-reused 0
Unpacking objects: 100% (1/1), done.
From https://github.com/KOITT3/mini_project
   a4e4905..896f40e  master     -> upstream/master
sdr@sdr-Samsung-DeskTop-System:~/project/mini_project/sanghoon$ git merge upstream/master
```


File
Edit
Vie
New
Open File...
Open Projects from File System...
Close Ctrl+W
Close All Shift+Ctrl+W
Save
Save As...
Save All Shift+Ctrl+S
Revert
Move...
Rename... F2
Refresh F5
Convert Line Delimiters To
Print... Ctrl+P
Import...
Export...
Properties Alt+Enter
1 HL_sys_main.c [tms570_freertos/source]
2 HL_sys_main.c [rc_control/source]
3 HL_sci.c [rc_control/source]
4 HL_can.c [tms570_freertos/source]
Switch Workspace
Restart
Exit
Project
het_p
i2c_m
rc_co
tms57
tms57
tms57
_main.c
HL_sys_main.c

이제 다른 사람이 작업한 fetch 와 merge 를 받았으니
가져와서 컴파일이 되는지 테스트해보도록 한다.

스샷은 Open File ... 인데 이것이 아니라
Open Projects from File System ... 이다.



Import Projects from File System or Archive

Import Projects from File System or Archive

This wizard analyzes the content of your folder or archive file to find projects and import them in the IDE.

Import source:

우측에 Directory 를 누른다.

Folder | Import as

0 of 0 selected

☐ Hide already open projects

Use installed project configurators to:

☑ Search for nested projects

☑ Detect and configure project natures

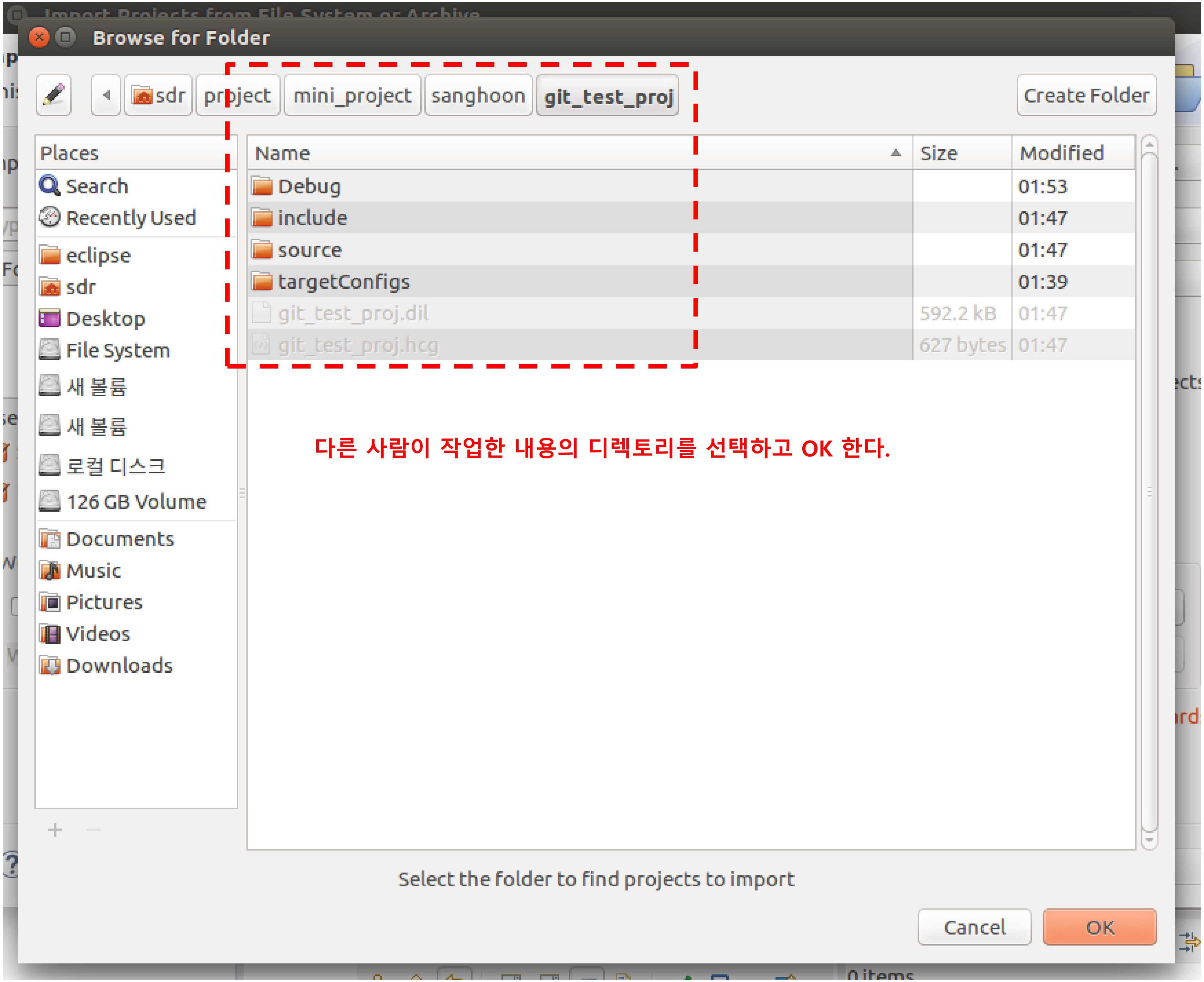
Working sets

☐ Add project to working sets

Working sets:

Show other specialized import wizards

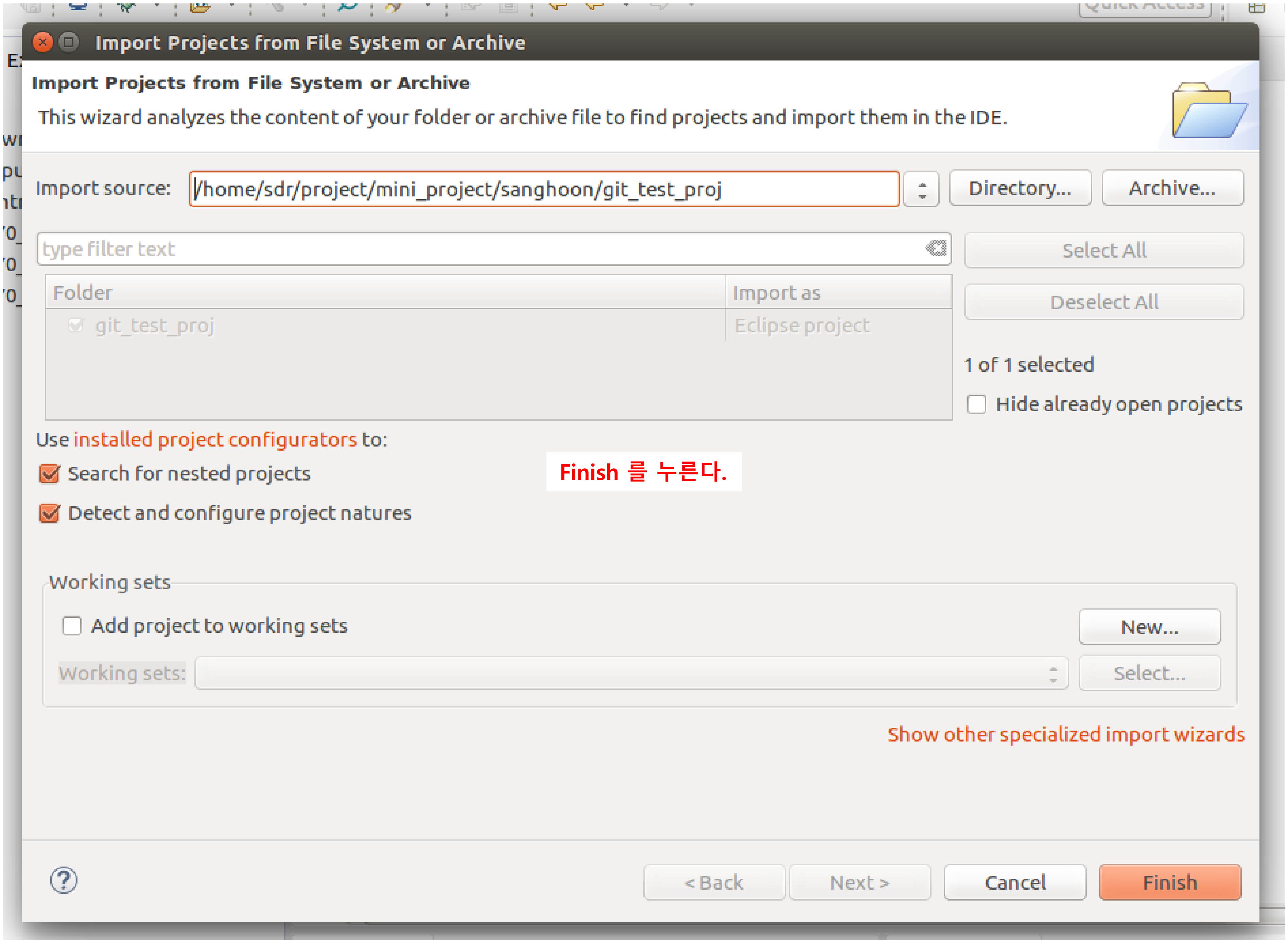
Directory...
Archive...
type filter text
Select All
Deselect All
New...
Select...
< Back
Next >
Cancel
Finish


Browse for Folder
sdr
project
mini_project
sanghoon
git_test_proj
Create Folder
Places
Search
Recently Used
eclipse
sdr
Desktop
File System
새 볼륨
새 볼륨
로컬 디스크
126 GB Volume
Documents
Music
Pictures
Videos
Downloads
Cancel
OK

Name	Size	Modified
Debug		01:53
include		01:47
source		01:47
targetConfigs		01:39
git_test_proj.dil	592.2 kB	01:47
git_test_proj.hcg	627 bytes	01:47

Select the folder to find projects to import

다른 사람이 작업한 내용의 디렉토리를 선택하고 OK 한다.



Import Projects from File System or Archive

Import Projects from File System or Archive

This wizard analyzes the content of your folder or archive file to find projects and import them in the IDE.

Import source: /home/sdr/project/mini_project/sanghoon/git_test_proj

Folder	Import as
☑ git_test_proj	Eclipse project

1 of 1 selected

☐ Hide already open projects

Use installed project configurators to:

☑ Search for nested projects

☑ Detect and configure project natures

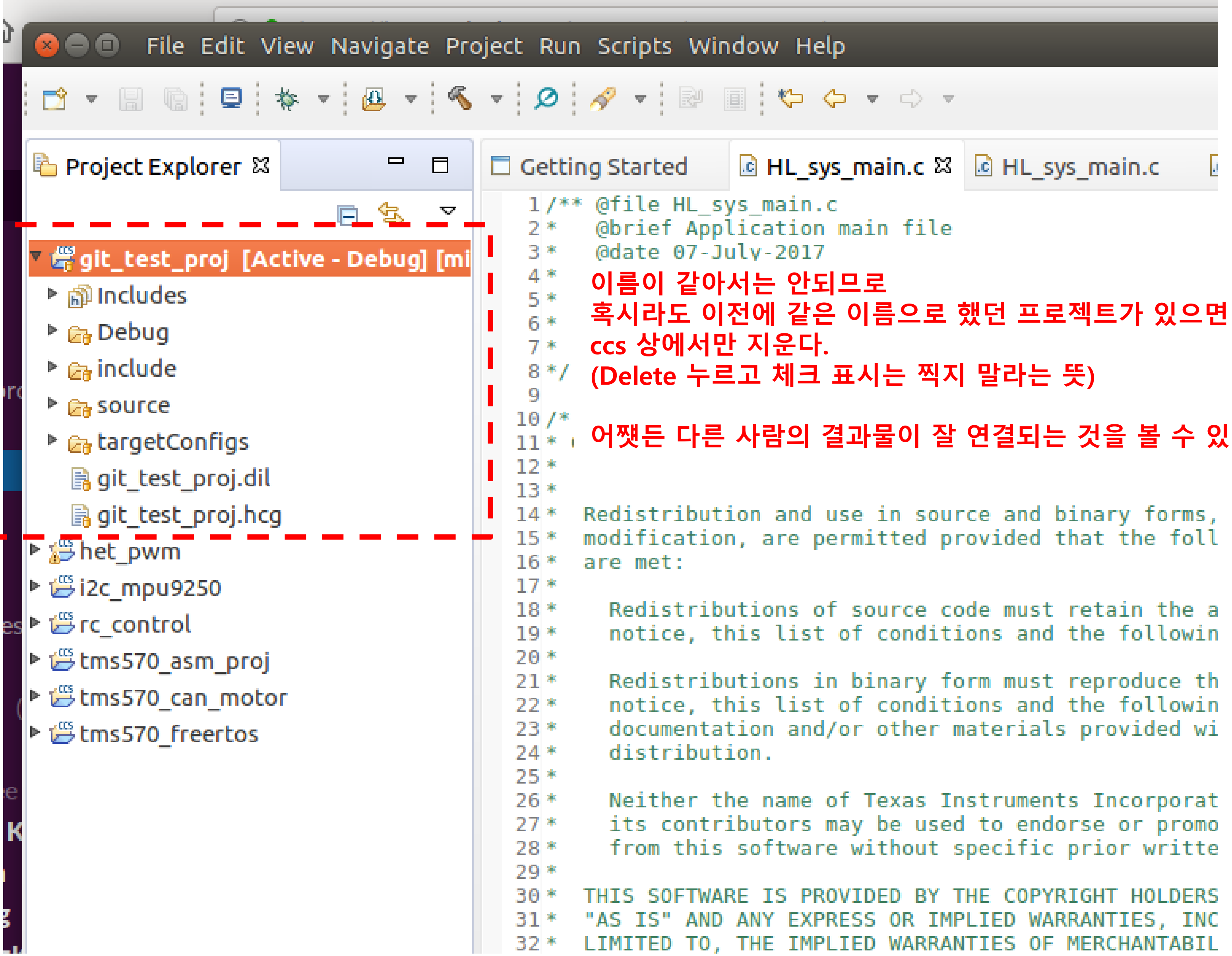
Working sets

☐ Add project to working sets

Working sets:

Finish 를 누른다.

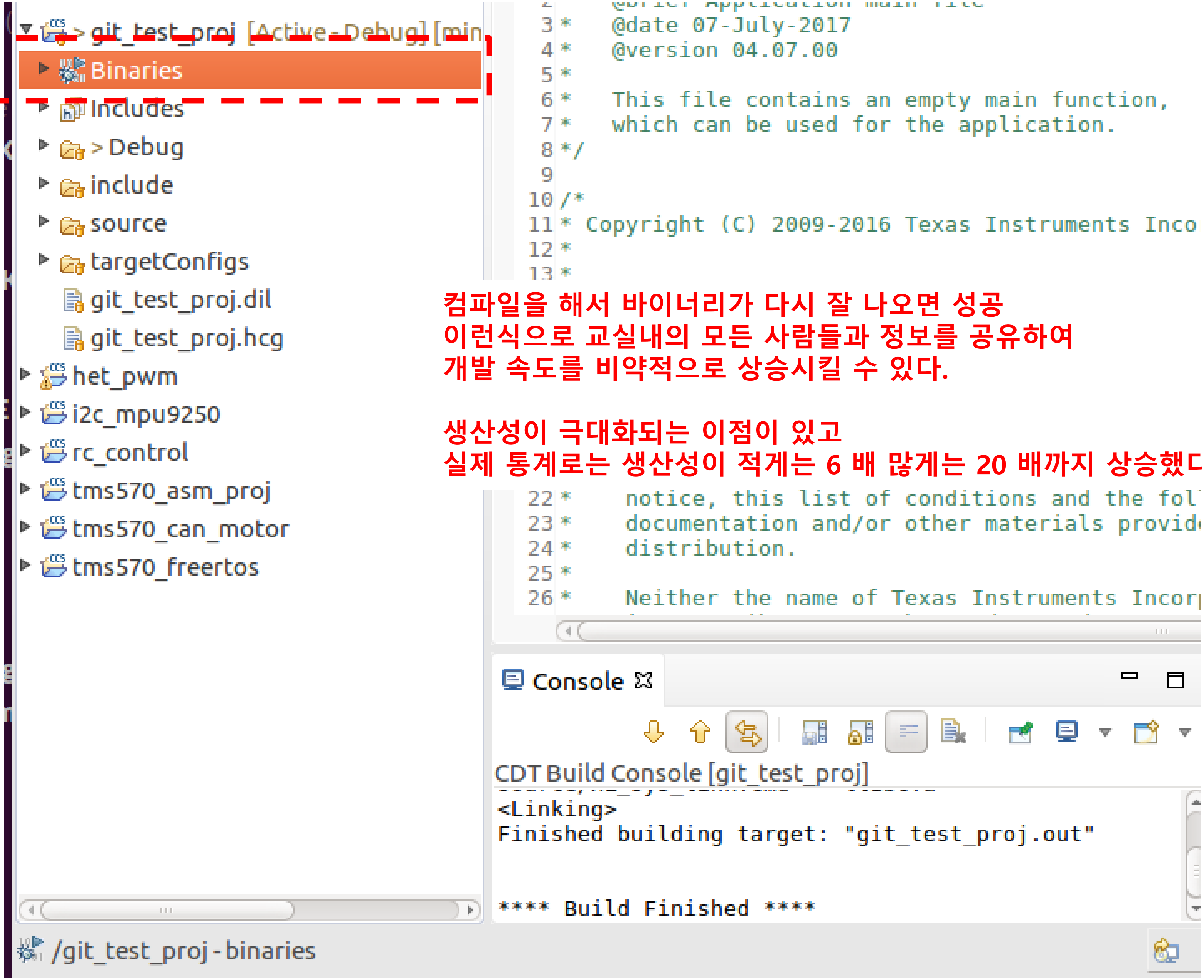
type filter text
Directory...
Archive...
Select All
Deselect All
New...
Select...
Show other specialized import wizards
< Back
Next >
Cancel
Finish


File Edit View Navigate Project Run Scripts Window Help
Project Explorer
Getting Started
HL_sys_main.c
HL_sys_main.c
git_test_proj [Active - Debug] [mi
Includes
Debug
include
source
targetConfigs
git_test_proj.dil
git_test_proj.hcg
het_pwm
i2c_mpu9250
rc_control
tms570_asm_proj
tms570_can_motor
tms570_freertos

```
/** @file HL_sys_main.c
*   @brief Application main file
*   @date 07-July-2017
*
*
*
*
*/

/*
* (
*
*
*  Redistribution and use in source and binary forms,
*  modification, are permitted provided that the foll
*  are met:
*
*    Redistributions of source code must retain the a
*    notice, this list of conditions and the followin
*
*    Redistributions in binary form must reproduce th
*    notice, this list of conditions and the followin
*    documentation and/or other materials provided wi
*    distribution.
*
*    Neither the name of Texas Instruments Incorporat
*    its contributors may be used to endorse or promo
*    from this software without specific prior writte
*
*  THIS SOFTWARE IS PROVIDED BY THE COPYRIGHT HOLDERS
*  "AS IS" AND ANY EXPRESS OR IMPLIED WARRANTIES, INC
*  LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABIL
```

이름이 같아서는 안되므로
혹시라도 이전에 같은 이름으로 했던 프로젝트가 있으면 지우거나
ccs 상에서만 지운다.
(Delete 누르고 체크 표시는 찍지 말라는 뜻)

어쨌든 다른 사람의 결과물이 잘 연결되는 것을 볼 수 있다.


git_test_proj [Active - Debug] [min
Binaries
Includes
Debug
include
source
targetConfigs
git_test_proj.dil
git_test_proj.hcg
het_pwm
i2c_mpu9250
rc_control
tms570_asm_proj
tms570_can_motor
tms570_freertos
Console
/git_test_proj - binaries

```
*   @date 07-July-2017
*   @version 04.07.00
*
*   This file contains an empty main function,
*   which can be used for the application.
*/

/*
* Copyright (C) 2009-2016 Texas Instruments Inco
*
*
```

```
*    notice, this list of conditions and the fol
*    documentation and/or other materials provid
*    distribution.
*
*    Neither the name of Texas Instruments Incor
```

CDT Build Console [git_test_proj]

```
<Linking>
Finished building target: "git_test_proj.out"


**** Build Finished ****
```

컴파일을 해서 바이너리가 다시 잘 나오면 성공
이런식으로 교실내의 모든 사람들과 정보를 공유하여
개발 속도를 비약적으로 상승시킬 수 있다.

생산성이 극대화되는 이점이 있고
실제 통계로는 생산성이 적게는 6 배 많게는 20 배까지 상승했다고 한다.