# **Computer Organization**

Lecture 7 -

**Instruction Sets: Software Concerns** 

Reading:

Homework: see uclass



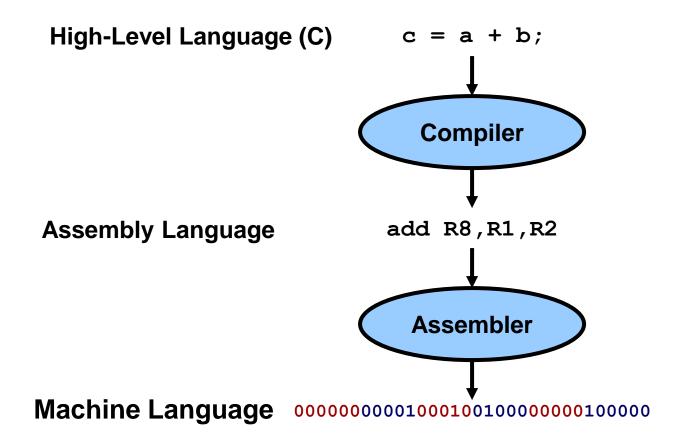


\*use corrected code for 2.4.4-2.4.6 and 2.13.1 (see Lecture 5)

### **Outline - Instruction Sets**

- Instruction Set Overview
- MIPS Instruction Set
- Software Concerns
  - Compiling C Constructs
  - Procedures (Subroutines) and Stacks
- Summary

# Review - Compiler & Assembler



# **Using MIPS Instructions**

- ▶ How does a compiler generate code for...
  - > if statements?
  - ▶ Loops?
  - switch Statements?

# Example: Compiling C if-then-else

Example

```
C Code
    if (i==j)
        f = g + h;
else
    f = g - h;
```

#### Assume the following:

- (1) i and j are in registers \$s3 and \$s4, respectively.
- (2) g and h are in registers \$s1 and \$s2, respectively

```
Assembly bne $s3, $s4, Else
add $s0, $s1, $s2
j Exit; # unconditional jump
Else: sub $s0, $s1, $s2
Exit:
```

### **Compiling C Loops**

#### Simple Loop Example - C Code

```
Loop: g = g + A[i];

i = i + j;

if (i != h) goto Loop;
```

```
\$s1 \leftarrow 0 \ //g

\$s2 \leftarrow h

\$s3 \leftarrow 0 \ //i

\$s4 \leftarrow j

\$s5 \leftarrow \&A[0]
```

```
0 1 2 3 4 5 6 7 8 9

A[10] 10 15 20 25 30 35 40 45 50 55

j = 2
h = 8
```

#### Assembly Language

```
Loop: sll $t1,$s3,2  # scale i for byte offset add $t1,$t1,$s5  # calculate address of A[i] lw $t0,0($t1)  # $t0 = A[i] add $s1,$s1,$t0  # <math>g = g + A[i] add $s3,$s3,$s4  # i = i + j bne $s3,$s2, Loop
```

### Compiling C Loops (cont'd)

while loop example - C Code

```
while (save[i] == k)
i += 1;
```

```
save[10]
k = 10 10 10 10 10 20 20 20 20 20
```

```
$s3 ← 0 // i
$s5 ← k
$s6 ← &save[0]
```

Assembly Language

```
Loop: sll $t1,$s3, 2  # $t1 = i * 4
add $t1,$t1,$s6  # calculate addr of save[i]
lw $t0,0($t1)  # $t0 = save[i]
bne $t0,$s5, Exit # if (save[i] != k)
addi $s3,$s3,1  # i = i + 1
j Loop
Exit: ...
```

### Compiling C switch statements

switch statement selects one of many alternatives

```
switch(k) {
    case 0: f=i+j; break;
    case 1: f=g+h; break; // if k = 1?
    case 2: f=g-h; break;
    case 3: f=i-j; break;
}
```

#### \$t4 ← base address of jump table

```
jump
JumpTable
                              LO:
                                     add $s0,$s3,$s4
                                                           # f=i+i;
            Addr. of L0
                                     j Exit;
                                                           # break;
                                                           # f=q+h;
                              L1:
                                     add $s0,$s1,$s2
            Addr. of L1
                                     i Exit;
                                                           # break;
            Addr. of L2
                              L2:
                                     sub $s0,$s1,$s2
                                                           # f=q-h;
            Addr. of L3
                                                           # break;
                                     i Exit;
                              L3:
                                     sub $s0,$s3,$s4
                                                             f=i-i;
                              Exit:
```

### Compiling C switch statements

\$s5 = k\$t4 = jumpTable

switch implementation: jump table access

```
slt $t3,$s5,$zero # test if k<0 (bounds check)</pre>
bne $t3,$zero,Exit
slti $t3,$s5,4 # test if k \ge 4 (bounds check)
beg $t3,$zero,Exit
sll $t1,$s5,2
                      # scale k for instr. ofset
add $t1,$t1,$t4
                      # t4 - jump table base address
lw $t0,0($t1)
jr $t0
                      # new instruction: "jump reg."
                                            (R-format)
                $t4 JumpTable
                             Addr. of L0
                             Addr. of L1
                             Addr. of L2
                              Addr. of L2
```

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LO:

L1:

L2:

L3:

Exit:

### **Outline - Instruction Sets**

- Instruction Set Overview
- MIPS Instruction Set
- Software Concerns
  - **▶** Compiling C Constructs
  - Procedures (Subroutines) and Stacks
  - Case Study: Internet Worms
  - Multicore Support: Synchronization
  - **▶** Compiling, Linking, and Loading
  - **▶** Example: String Processing / SPIM Demo

Summary

#### **Procedures**

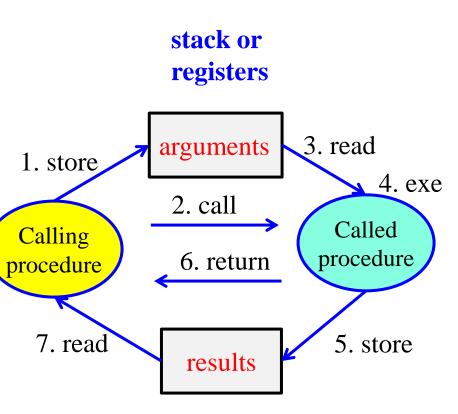
- Common tool to structure programs
  - Break program up into pieces
  - "Main program" calls procedures
  - Procedures may call other procedures
  - Recursion: procedure calls itself
- Other names for procedures
  - Functions (usually with a <u>return value</u>)
  - Subroutines
  - Methods (when associated with a class/object e.g. Java)

# **Example - C Functions**

```
Return Type
                                                          Arguments
                                       DTYPE read input(char* filename) {
                                         return data; - Return Value
main(char argc, *argv[]) {
 DTYPE data;
 data = read input(argv[0]));
                                       void process data(DTYPE pdata) {
 process data(stuff);
 write_output(stuff)
                                       void write data(DTYPE wdata) {
```

# **Steps Required to Call a Procedure**

- 1. Store arguments where called procedure can access them
- Transfer control to called procedure
- 3. Acquire arguments needed for procedure
- 4. Perform desired task
- Place return value where calling procedure can access
- Return control to calling procedure
- 7. Read the results



전역변수의 의미?

### **Procedure Calls in MIPS**

- Register conventions to support procedures:
  - ♦ \$a0-\$a3 four <u>argument registers</u> (\$4-\$7)
  - ▶ \$v0-\$v1 two return value registers (\$2-\$3)
  - ♦ \$±0-\$±9: temporary registers for variables (\$8-\$15)
    - Can be overwritten by callee
  - ♦ \$s0-\$s7: <u>saved registers</u> for variables (\$16-\$23)
    - Must be saved/restored by callee
  - \$gp: global pointer for static data (\$28)
  - \$sp: stack pointer (\$29)
  - ▶ \$fp: frame pointer (\$30)
  - \$ra one return address register (\$31)

### **Procedure Calls in MIPS**

Calling procedures - jump and link (jal)

Returning from procedure - "jump register (jr)

```
jr $ra
effect:
    PC = $ra
```

### **Procedure Calls - Additional Concerns**

- Q: What happens when...
  - ▶ We need more storage than registers can provide?
    - More than four arguments (arg. registers: \$a0-\$a3)
    - Local array variables
    - Data "spilled" from registers (see example p. 114)
  - ▶ We have <u>nested</u> procedure calls (f() calls g())?
  - ▶ We have <u>recursive</u> procedure calls (f() calls itself)?
- ▶ A: We use the stack

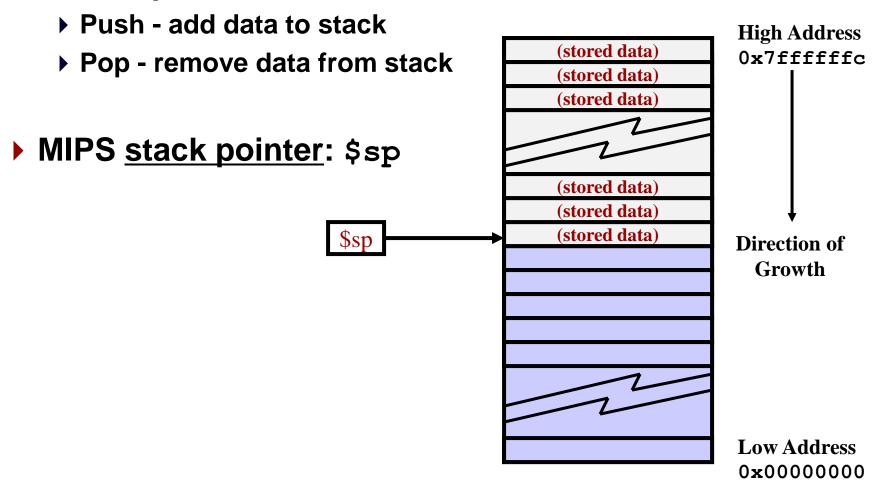
공유메모리?

- register file

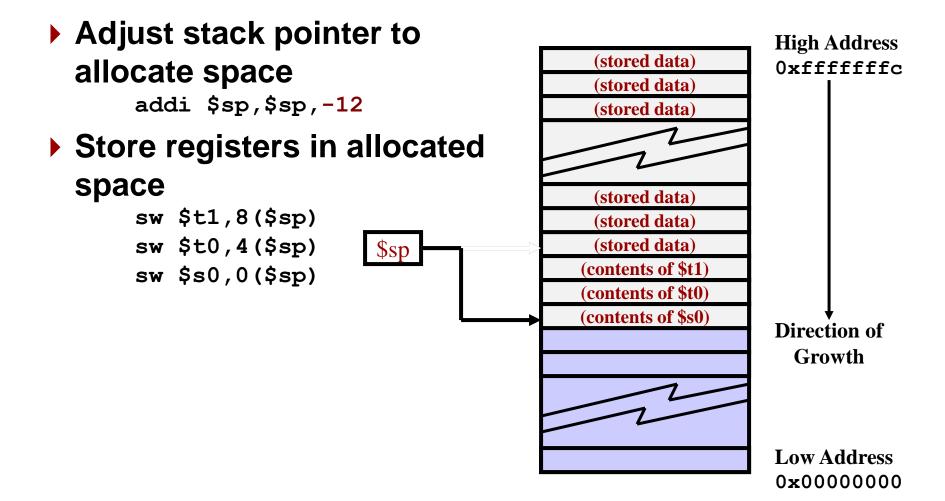
- stack

# **Using the Stack**

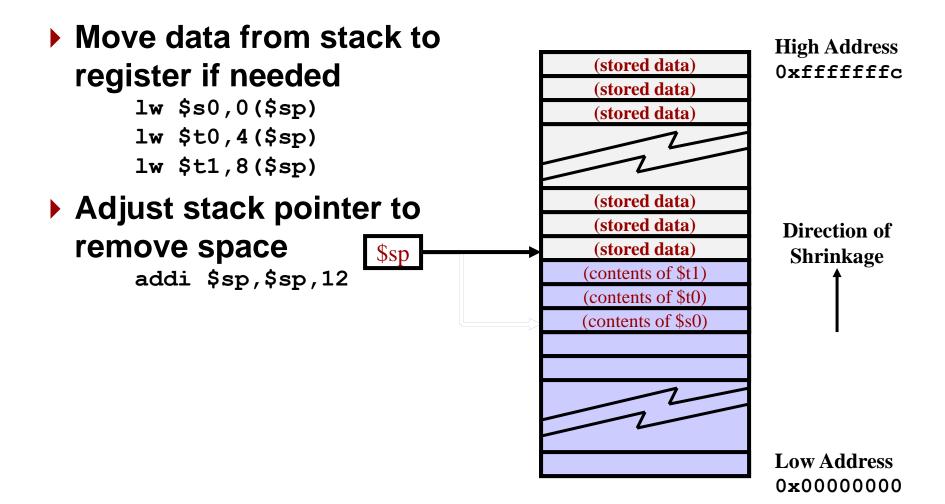
Stack operations



# "Push" - Adding Data to the Stack



# **Pop - Removing Data from Stack**



### **Nested Calls in C Functions**

```
DTYPE read input(char* filename)
                                    return data;
                                                                       int f1( ... ) {
                                                                          return x;
                                  void process data(DTYPE pdata) {
main(char argc, int *argv[])
 DTYPE data;
                                      x = f1(pdata[I]);
  data = read input(argv[0]));
                                                                       int f1( ... ) {
 process data(stuff)
                                      f2(pdata[J]
  write output(stuff)
                                  void write_data(DTYPE wdata) {
```

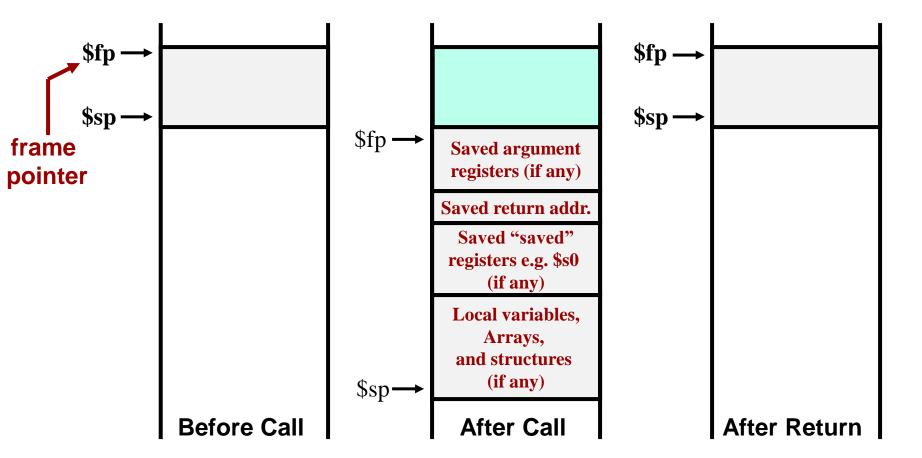
### **Recursion in C Functions**

```
int fact (int n) /* see book p. 117)
{
  if (n < 1) return 1;
  else return (n * fact(n-1));
}</pre>
```

```
int fact (5)
            int fact (4)
  if (n <
  else retu
                     int fact (3)
              if (n
              else
                               int fact (2)
                       if (n
                       else r
                                       int fact (1)
                                 if (
                                 else
                                                  int fact (0)
                                         if (n <
                                         else ret
                                                    if (n < 1) return 1;
                                                    else return (n * fact(n-1));
```

### **Procedures and the Stack**

▶ Each called procedure maintains a stack frame



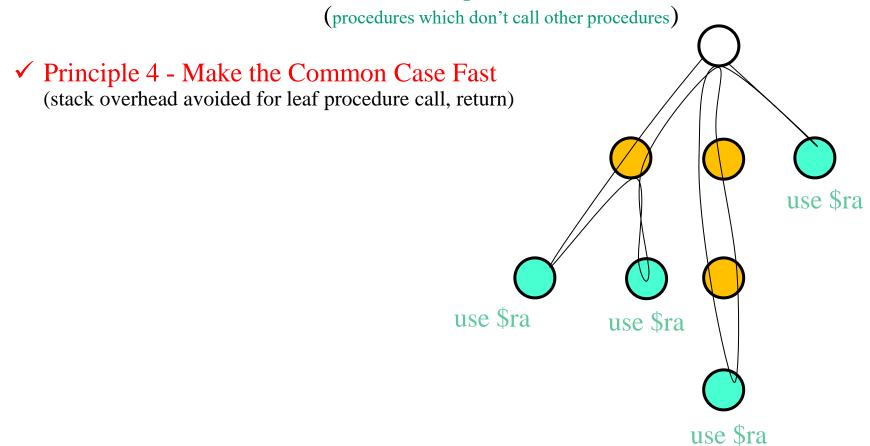
### **Transfer of Control in Procedures**

Some architectures such as 68HC11 transfer control on procedures using:

- jsr instruction
  - Pushes return address onto the stack
  - Changes PC to target address
- rts instruction
  - Pops return address off the stack
  - Changes PC to popped address

### **Transfer of Control in Procedures**

- Why doesn't MIPS architecture do this?
  - ✓ Nested calls are not as common as <u>leaf procedures</u>



### **Example: Leaf Procedure C Code**

```
int leaf_example (int g, h, i, j) //callee
{
  int f;
  f = (g + h) - (i + j);
  return f;
}
```

- ▶ Arguments g, ..., j in \$a0, ..., \$a3
- ▶ f in \$s0 (hence, need to save \$s0 on stack)
- ▶ Result in \$v0

If this procedure wants to use the saved register \$s0, it has to save \$s0 before using it since the caller may have been using the register \$s0. Then, at the end of this procedure, it has to recover \$s0 before returning to the caller.

See the example in the next page!

### **Example: Leaf Procedure MIPS Code**

```
#at the caller
jal leaf_example # $ra = PC+4, PC = leaf_example
add $t3, $v0, $zero
```

```
leaf example:
  addi $sp, $sp, -4
                                Save $s0 on stack
  sw $s0, 0($sp)
  add $t0, $a0, $a1
  add $t1, $a2, $a3
                                Procedure body
  sub $s0, $t0, $t1
  add $v0, $s0, $zero
                                Result in $v0
  lw $s0, 0($sp)
                                Restore $s0
  addi $sp, $sp, 4
  jr
       $ra
                                Return
```

#### **Non-Leaf Procedures**

- Procedures that call other procedures
- ▶ For nested call, caller needs to save on the stack:
  - Its return address
  - Any arguments and temporaries needed after the call
- Restore from the stack after the call

### Non-Leaf Procedure Example

```
C code:
        int fact (int n)
          if (n < 1) return 1;
          else return n * fact(n - 1);
        main() {
return here
          int r = fact(n); //call procedure
          printf("fact value = %d\n", r);
      Argument n in $a0
      ▶ Result in $v0
                                                 use $ra
```

### Non-Leaf Procedure Example

mul \$v0, \$a0, \$v0

\$ra

jr

```
fact(5)?
                 addi $a0, $zero, 5
                 ial fact
                                                    sp
                 XXXX
fact:
   addi $sp, $sp, -8 # adjust stack for 2 items
                                                         ra
   sw $ra, 4($sp) # save return address (main)
   sw $a0, 0($sp) # save argument n
                                                         ra
   slti $t0, $a0, 1 # test for n < 1
   beq $t0, $zero, L1 # if n >= 1, go to L1
                                                         ra
   addi $v0, $zero, 1 # if n < 1, result is 1
   addi $sp, $sp, 8 # pop 2 items from stack
                                                         ra
                        # and return
   jr $ra
L1: addi $a0, $a0, -1
                        # else decrement n
                                                         ra
   jal fact
                        # recursive call
   lw $a0, 0($sp)
                        # restore original n
   lw $ra, 4($sp)
                        # return address
   addi $sp, $sp, 8
                        # pop 2 items from stack
```

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# and return

# multiply to get result

### **Summary**

- Steps required to call a Procedure
- Make the common case fast: \$ra
  - leaf and non-leaf nodes
- How the system stack is changed when a procedure is called?
- How the non-leaf procedure is implemented?