# **Summary | Programming Fundamentals**

### Introduction

### (i) Note

Programming Fundamentals is probably the less-organized section at the moment. Let me know how I can improve this.

This module includes 3 sections:

- Programming basics (with python v3.10.9)
- Theories beyond coding
- Hardware

### **Important points**

### **Confusion about unit prefixes**

In computing, the prefix  $\it kilo$  —just like other prefixes— has been used to refer either  $2^{10}$  or  $10^3$  depending on the context.

- +  $10^3\,$  Marketing of disk capacities (by disk manufacturers)
- $\mathbf{2^{10}}$  Memory capacities, and file sizes, disk capacities by operating systems

To avoid this confusion, 2 unit prefixes are used while measuring amounts of data.

- SI prefixes  $\mbox{ Defined by ISO. Based on powers of } 10^3 \mbox{ . Examples: kilo, mega, giga. }$
- Binary prefixes  $\mbox{ Defined by IEC. Based on powers of } 2^{10} \mbox{ . Examples: kibi, mebi, gibi.}$

# **Interning**

Interning is re-using objects of equal value on-demand instead of creating new objects. This is done for memory efficiency. Frequently used for numbers and strings in different programming languages.

```
a = 120
b = 120

print(a is b) # True

c = 2000
d = 2000

print(c is d) # False
```

In the above code,  $_{120}$  is intered by the Python interpreter but not  $_{2000}$ . Python's integer interning is done only for numbers in the range:  $_{[-5,\ 256]}$ 

Python interpreter also interns small strings.

```
a = "abcd"
b = "abcd"

print(a is b) # True

# Both text are the same
c = "Lorem ipsum dolor sit amet consectetur adipisicing elit. Consequentur perferendis iste ipsa nat
d = "Lorem ipsum dolor sit amet consectetur adipisicing elit. Consequentur perferendis iste ipsa nat
print(c is d) # False
```

Strings in python can be manually interned using sys.intern function.

```
a, b=8, 8
c=8
d=8
```

Likewise, in the above code, only 1 integer object is created.

### **Practice Resources**

### **Programs**

The programs are listed in no specific order.

- 1. **is prime number**: A program that takes in a number n and outputs whether its a <u>prime</u> number or not.
- 2. **factors**: Take in a number from user. Output all of its factors.
- 3. **n-th factorial**: A program that takes in a number n and outputs n-th <u>factorial</u>.
- 4. **is perfect number**: A program that takes in a number n and outputs whether its a <u>perfect number</u>.
- 5. **fibonacci numbers**: A program that takes in a number n and prints all <u>fibonacci numbers</u> less than or equal to n.
- 6. **determinant of matrix**: Take in a matrix from user. Output the determinant of the matrix. First try for  $2 \times 2$ . Then go higher-ordered matrices.
- 7. **pascal's triangle**: Take n from user input. Print <u>pascal's triangle</u> to n rows.
- 8. **is valid palindrome**: Take a string input from user. Output if the input is palindrome or not. A phrase is a palindrome if, after converting all uppercase letters into lowercase letters and removing all non-alphanumeric characters, it reads the same forward and backward. Alphanumeric characters include letters and numbers. Try not to use [::-1].
- 9. **armstrong numbers**: Take n from user input. Print all <u>armstrong numbers</u> (in base 10, of course) between 0 and n (inclusive).
- 10. **letter analysis**: Take a text input from user. Find how many times each letter is being used in that string. Use a dictionary to store the data. Output the final results. Try to read the text from a .txt file as well.
- 11. **word length analysis**: Take a string input from user. Print length of each word separated by a space. Try to include the summary using a dictionary.
- 12. **letter expanding**: A program that converts *b3j8k2* to *bbbjjjjjjjjkk*. The number can be 1 to 99.
- 13. **binary addition**: Take in 2 numbers in binary (as strings) and output the sum of both numbers. Try not to use bin function.
- 14. **big integer addition**: Given a very large integer represented as a list, where each digits[i] is the  $i^{th}$  digit of the integer. The digits are ordered from most significant to least significant in left-to-right order. Increment the large integer by one and return the resulting array of digits. Don't construct a int object.

- 15. stack implementation
- 16. queue implementation

#### **Platforms**

- Codewars <a href="https://codewars.com">https://codewars.com</a> (my most preferred one)
- HackerRank <a href="https://hackerrank.com">https://hackerrank.com</a>
- Leetcode <a href="https://leetcode.com">https://leetcode.com</a> (my least preferred one)

### **Mard Problems**

If a problem from one of these platforms feels too hard for you, you can just skip and do another problem.

# **Number Systems**

A writing system for expressing numbers. Each number system defines a set of symbols that each represent a specific value.

### Base (or radix)

Number of symbols defined by a number system.

# **Commonly used number systems**

- Base 10 0 9
- Base 2 0, 1
- Base 8 0 7
- Base 16 0 9, A F

### 

These are required for s1:

- Converting integers and floats between number systems
- Addition, subtraction, multiplication, division in base 2

But I don't know how to include it in a easy-to-understand way.

# **One's & Two's Complement**

### **One's complement**

The ones' complement of a binary number is the value obtained by flipping all the bits in the binary representation of the number.

- If one's complement of  $oldsymbol{a}$  is  $oldsymbol{b}$  , then one's complement of  $oldsymbol{b}$  is  $oldsymbol{a}$  .
- Binary representation of a+b will include all 1 s.

### One's complement system

In which negative numbers are represented by the inverse of the binary representations of their corresponding positive numbers. First bit denotes the sign of the number.

- Positive numbers are the denoted as basic binary numbers with 0 as the MSB.
- Negative values are denoted by the one's complement of their absolute value.

For example, to find the one's complement system representation of -7, one's complement of 7 must be found.  $7 = 0111_2$ . One's complement of -7 is 1000.

### Two's complement

In which negative numbers are represented using the MSB (sign bit).

If MSB is:

- 1 : negative
- **0**: positive

Positive numbers are represented as basic binary numbers with an additional  ${\bf 0}$  as the sign bit.

For example:

Following equation can be used to convert a number in two's complement form to decimal.

$$b=-2^{n-1}b_{n-1}+\sum_{k=0}^{n-2}2^kb_k$$

#### **Steps**

- 1. Starting with the absolute binary representation of the number
- 2. Add a leading  $\mathbf{0}$  bit being a sign bit
- 3. Find the one's complement: flip all bits (which effectively subtracts the value from -1)
- 4. Add 1, ignoring any overflows

# **Floating-point Representation**

IEEE 754 standard.

2 types:

- single precision
- · double precision

### **Single precision**

Uses 32 bits.

- sign bit 1 bit
- exponent 8 bit
- mantissa 23 bit

#### Sign bit

 ${f 0}$  if positive or zero.  ${f 1}$  if negative.

#### **Exponent**

Exponent field range - [0,255]. In this range [1,254] is defined for normal numbers. 0 and 255 are reserved for subnormal, infinite, signed zeros and NaN.

To support negative exponents, we subtract 127 (half of 254) from this range. [-126,127]. This range is the representable range.

#### **Mantissa**

In scientific notation, the part that doesn't contain the base and the power.

In binary scientific notation, there will always be exactly one  ${f 1}$  bit before the dot. So we don't include that one.

### (i) Example

#### Take 31.3125.

• In binary:  $1111.0101_2$ 

- In binary scientific notation:  $1.1110101_2 imes 2^3$ 

• Add 127 to exponent: 130

• Convert exponent to binary 10000010

ullet Write the final result:  $0\ 10000010\ 0000000000000001110101$ 

#### Take 0.125.

• In binary:  $-0.001_2$ 

- In binary scientific notation:  $-1.0_2 imes 2^{-3}$ 

- Add 127 to exponent: 124

- Convert exponent to binary  $\,01111100\,$ 

## **Double precision**

Uses **64** bits.

- ullet sign bit 1 bit
- exponent 11 bit
- mantissa 53 bit

### Sign bit

 $\mathbf{0}$  if positive or zero.  $\mathbf{1}$  if negative.

### **Exponent**

Exponent field range - [0, 2047]. In this range [1, 2046] is defined for normal numbers. 0 and 2047 are reserved for subnormal, infinite, signed zeros and NaN.

To support negative exponents, we subtract 1023 (half of 2046) from this range. [-1022, 1023]. This range is the representable range.

#### **Mantissa**

In scientific notation, the part that doesn't contain the base and the power.

In binary scientific notation, there will always be exactly one  ${\bf 1}$  bit before the dot. So we don't include that one.

### **(i)** Example

#### Take **31.3125**.

• In binary:  $1111.0101_2$ 

• In binary scientific notation:  $1.1110101_2 imes 2^3$ 

• Add 1023 to exponent: 1026

• Convert exponent to binary: 1000000010

• Write the final result:

#### Take 0.125.

• In binary:  $-0.001_2$ 

• In binary scientific notation:  $-1.0_2 imes 2^-3$ 

• Add 1023 to exponent: 1020

• Convert exponent to binary: 1111111100

• Write the final result:

# **String Representation**

A way of representing non-numerical data.

### **Commonly used encodings**

#### **ASCII**

Abbreviation for American Standard Code for Information Interchange. Uses 7 bits for letter representation and a parity bit (MSB). Can represent latin alphabet, digits, punctuations, and control characters.

Major limitation in ASCII is it can't support multiple languages.

#### Unicode

Uses 32 bits. Supports multiple languages and emojis. Characters are presented by code points. A code point is a integer (in base 16).

# **Data Structures & Algorithms**

#### **Data structures**

Common data types that are useful in many different places.

#### **Abstract Data Type**

A data type that has well defined properties and operations but not implementation.

#### **Examples**

- · Array fixed-length, one-dimensional
- Set
- · Stack Last in; first out
- · Queue First in; first out
- · Binary search tree

### (i) Note

Implementations of stacks, queues, and binary search trees are required in s1.

### **Algorithms**

An algorithm is a finite set of instructions, used to solve a problem.

### (i) Note

In s1, only sorting algorithms are discussed.

#### **Selection sort**

Here is selection sort algorithm that sorts a list of numbers in-place:

```
arr[smallest_index], arr[current_starting_index] = arr[current_starting_index], arr[smallest
```

#### **Bubble sort**

Here is bubble sort algorithm that sorts a list of numbers in-place:

# **Software Engineering**

### **Software**

Refers to all the related things that are required to make a software system work.

Includes:

- programs
- · configuration files
- system and user documentation
- · user support system
- bug fixes and updates

### **Software engineering**

An engineering discipline that is concerned with all aspects of software production. From the initial stage of writing the requirements to maintaining it while being used.

# Software process

Set of activities that are associated with the development of a software product.

Fundamental activities that are common to all types of software development processes:

- Specification defining the software to be produced and the runtime constraints
- Development design and development of the software
- Validation testing phase to check if the software meets the specifications
- Evolution software is modified to adapt to new specifications

#### **Waterfall**

All before-mentioned activities are done sequentially, as clear separate phases. One phase is completed before the next phase is started.

#### **Iterative & incremental**

System is developed in iteration. Smaller parts of the system is completed in each iteration, that includes:

- Small amount of requirements specification
- Design and development for the specification
- Validation for the developed parts

#### **Component based**

Existing components are combined to implement the system. Main concentration is on the integration of the components.

### **Quality of software**

Can be measured using these aspects:

- Maintainability how easy it is to making changes
- · Dependability how secure, reliable it is to failures or other unusual activities
- Efficiency how efficiently hardware resources (such as memory, processor time, disk space) are used
- Usability how easy it is to use the software from user's perspective
- Robustness how resilient it is to invalid inputs

# Challenges in software engineering

- Complexity
  - Essential inherent, difficult to overcome
  - Accidental not inherent, can be overcome
- Conformity
- Changeability expected to be changeable to greater extent
- Invisibility not visualizable
- Can't guarantee defect free software no amount of testing can prove absence of defects

# **Computers**

An electronic device for analyzing and storing data, making calculations, etc.

Originates from the word compute which means calculate. Computers can be programmed to process data following some finite set of instructions.

### Types of computers

### **Personal computers**

Or PCs in short. Intended to be used by a single person. First introduced by IBM. Built around a family of microprocessors referred to as "8086" (manufactured by Intel). Therefore its structure is referred to as "x86 architecture".

An alternate system was Macintosh (introduced by Apply). Due to differences in hardware architecture between x86 and Macintosh, software used by these systems were not portable to the other.

Types of personal computers:

- Desktop designed to sit on or under a desk. too big to be carried around.
- Workstation desktop computers with high specs. powerful. built for specialized use cases.
- Laptop portable. categorized under mobile computers.
- Tablet even lighter than laptops. include touch input support.
- Handheld can fit in our hands. example: personal digital assistant.
- Smartphone cellular phones with advanced features
- Wearable can be worn such as glasses

#### **Network servers**

Powerful personal computers with special hardware and software that enable them to function as primary computers in a network. They may be setup in groups called clusters or server farms.

#### **Mainframe computers**

Used in large organizations where many people, even thousands, frequently need to access the same data. Traditionally, each user accesses a mainframe computer through a device called a terminal.

#### **Minicomputers**

More powerful than personal computers but less powerful compared to mainframes. Aka. midrange computers.

### **Supercomputers**

Most powerful, physically largest computers. Have thousands of processors.

### **Embedded computers**

Computers that reside in a device and not directly visible. Aka. invisible computers.

These devices are referred to as *smart* because they can interact with the environment in a more intelligent, adaptive, and efficient way.

### Components of a computer system

A computer is a system. A complete computer system consists of:

- <u>Hardware</u>: Tangible elements that can be seen and touched
- <u>Software</u>: Programs that allow users to use the computer system and control its activities. Not tangible.
- Data: Individual facts or pieces of information that is input to the computer for processing or produces as output after processing.
- User(s)

### **Information**

Computers can process various types of data. When a communication link (such as Internet) is provided, the data can be transferred to other users despite of distance. The computer and communication technologies that made this possible are together referred to as information technology or IT (or, sometimes as information and communication technology or ICT). Computers are therefore at the heart of IT.

An information system is a system with well-defined procedures and techniques to collect, store, process, and disseminate information.

# **Computer Hardware**

### Major components of a PC

- Motherboard
  - Central Processing Unit
  - Memory
  - Graphic / display controller
  - Network / Wi-Fi controllers
  - Audio Interface (Sound card)
  - Universal Serial Bus (USB) controller
- Input / Output sub system
   enables interaction with the users
  - Display Monitor (VDU)
  - Keyboard
  - Mouse
  - Scanner
  - Printer
  - Microphone / Speakers
  - Joystick / Game controller
- Secondary storage devices
- Chassis

the platform that holds all other components

- Power supply unit
- Fan / cooling system

### **Traditional computer system**

Can be classified into 2 types.

### Single-user computer system

Can only be used by one user at a time.

### Modern computer system

#### **Motherboard**

Large circuit board where all (or majority of the) components of the PC are mounted.

Aka. main board.

# **Computer Memory**

#### 2 types:

- Permanent or non-volatile
- Temporary or volatile will be lost when power is off

Instructions and data for the CPU is sent from the memory. Results are sent back to the memory.

Consists of an array of consecutive memory locations. Each location is identified by a memory address and stores a single piece of data, usually a byte. CPU can either read or write a single memory location at a time.

### **Memory Bus**

A set of electrical connections that connect memory locations with CPU.

#### 3 types:

- · Address bus used to indicate address of a memory location. goes from CPU to memory
- Control bus used to send control information (read request RD or write request WR) from CPU to memory.
- Data bus actual data transmission. bidirectional.

#### Writing data:

- 1. Address bus is set with the memory address
- 2. Data bus is set with the data to be written
- 3. CPU activates WR in control bus

#### Reading data:

- 1. Address bus is set with the memory address
- 2. CPU activates RD in control bus
- 3. Data is fetched using data bus

### **Types of Memory**

#### **Read Only Memory**

Memory is written (hardcoded) when they are fabricated as ICs. Used to store initial start-up programs. Not economical to produce in small quantities.

Example: ICs in the market that have various melodies.

### **Programmable Read Only Memory**

Similar to ROM but, the content can be written (using special equipment) once after the manufacturing process. Cost effective compared to ROMs.

#### **UV Erasable PROM**

Similar to PROM but can be written multiple times. The content should be erased using UV light before new content is written. Both erasing and programming process require special equipment.

#### **Electrical Erasable PROM**

Similar to UVEPROM but the content can be erased by applying a special high voltage.

#### Flash ROM

A special type of EEPROM that can be erased or programmed while in the application circuit. The contents remains unchanged even after a power failure.

Commonly used in modern PCs, various networking devices such as routers and firewalls and memory pens (also referred as memory sticks or USB pens).

### **Read Write Memory**

Aka. RAM. Volatile.

2 types:

- Static RAM uses transistors
- Dynamic RAM uses capacitors. bulk of the PC memory is made using DRAM.

Transistors	Capacitors
Uses semiconductors	Uses semiconductors
High speed switching	Slower performance
Retains state forever (if power is supplied)	Discharges after some time, needs refreshing (in μs scale)
More reliable	Less reliable
Low transistor density	High capacitor density
High power consumption	Low power consumption
High cost per bit	Low cost per bit

# **Memory Modules**

Set of memory ICs presented as a single memory block to the motherboard .

Type of memory modules:

- SIM Single Inline Memory Module
- DIMM Dual Inline Memory Module (64-bit wide 168-pin)
- DDR-DIMM Double Data Rate-DIMM

# **Memory Characteristics**

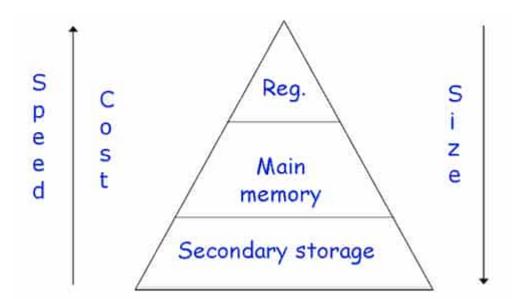
- Access speed time taken for the CPU to read from or write to memory
- Cycle time time taken to complete 1 memory access operation
- Packing Density memory capacity per unit area
- Power consumption
- Cost cost per unit of memory capacity

### **Memory Hierarchy**

Modern CPUs are much faster than the speed of memory. The memory has to be organised in such a way that its slower speed does not reduce the performance of the overall system.

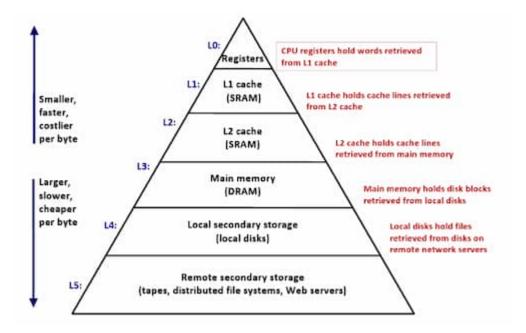
The ultimate objective of having a memory hierarchy is to have a memory system with a sufficient capacity and which is as cheap as the cheapest memory type and as fast as the fastest memory type. The main idea is to use a limited capacity of fast but expensive memory types and a larger capacity of slow but cheap memory types. Special methods are used to store the frequently used items in the faster devices and others in slower devices.

#### **Traditional**



#### Modern

Cache memory is added to this hierarchy to bridge the speed difference between processor and memory. It is used by the microprocessor to store frequently used instructions and data.



#### (i) Swapping & Paging

When free space is needed in cache memory, infrequently used data and instructors are sent back to the main memory. This process is called swapping.

When free space is needed in main memory, some of the data and instructions are sent to the secondary storage. This process is called paging.

### **CPU**

CPU in today's computers are microprocessors. A CPU:

- · performs logical & arithmetic operations
- controls all the other components and subsystems

### **Components of a CPU**

### **Arithmetic & Logical Unit**

Arithmetic unit handles arithmetic operations. Logical unit handles logical operations.

#### **Control Unit**

Controls the operation of the CPU and rest of the machine

#### Registers

A type of memory that can hold a unit of data. Can be used for both data processing and control functionalities.

### Type of registers

### **Program Counter**

Keeps track of memory address of the next instruction to be executed.

### **Instruction Register**

After an instruction is fetched into the CPU, it is stored in IR for execution. Keeping IR closer to CU (in the scale of micrometers) enables faster execution speed.

#### **Accumulator**

Where result of arithmetic or logical operation is stored immediately.

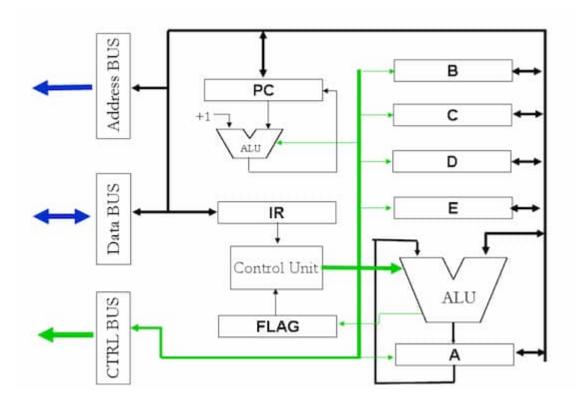
### Flag Register

Stores the status of the last operation carried out by ALU.

### **General Purpose Registers**

Can be used to various tasks. Used to store immediate results of the ALU. Number of GPRs vary depending on the CPU. Usually denoted as "B", "C" and so on.

#### **Internal Structure**



- There is a secondary ALU after PC. That increments the PC after fetching a new instruction.
- ALU accepts maximum 2 inputs. One input should come from accumulator.

# **Execution of a program**

### **Fetch cycle**

Instruction is loaded into the CPU.

### **Execution cycle**

Instruction is executed by the CU. Aka. instruction cycle.

# **Enhancing CPU Performance**

### **Instruction pre-fetching**

When an instruction is in the execution cycle, next instruction will be fetched.

### **Instruction pipelining**

Instruction cycle is divided into sub-operations and different segments of CPU handle each sub-operations.

Increases thorough put of the microprocessor.

#### **Hyper Threading**

Allows different resources of the CPU to be used at the same time. CPU, BIOS, OS, and chipset have to support HT technology to use this.

#### **Multicore processors**

2 or more separate microprocessors, combined onto a single Silicon chip. Higher performance gain compared to HT.

### **CPU Support Chips**

Carries out tasks on behalf of the CPU.

#### Examples:

- Direct Memory Access controllers
- Disk controllers
- Real-Time Clock
- Display controllers
- Interrupt controllers
- · Communication controllers

#### **DMA** controllers

Provides a way of bypassing the CPU when transferring data between memory and Input/Output (IO) devices. Resides between memory and CPU.

#### **Disk controllers**

When reading/writing to/from a disk the CPU will create a buffer (special memory area) containing the sector address and the data to be written or read. Then the CPU informs the controller about the location of the buffer. The controller then transfers the content of the buffer directly from memory to the disk sector.

#### Examples:

- Floppy Disk Controller
- ATA Controller

#### **Real-Time Clocks**

Used to keep track of time of the day. Usually backed-up by an extra power source such as a Li battery. Used to store some of the configuration information such as CMOS setup memory.

#### **Display controllers**

Used to generate images and text that you see on the displaying device on behalf of the CPU. The actual image (which is binary data) is generated in its memory called "refresh buffer", then passed to video controller to be displayed.

#### Video controllers

Used to display the image on monitors. Classified based on their video processor and video memory.

#### Some video standards are:

- Video Graphics Array minimum resolution: 320x240 in 256 colors or 640x480 in 16 colors
- Super VGA supports resolution up to 800x600
- Video Electronics Standards Association SVGA was developed to standardize SVGA. Also includes a video standard for connecting high-speed adaptors directly to the processor bus.

### **Von Neumann Architecture**

An architecture on how a computer can be built. Simple and implementable proposal but not the best mechanism for operations such as disk access or time critical operations. A computer structure resulting from this architecture is called "Von Neumann Machine".

Parts of a stored-program computer (under this architecture):

- A memory contains both data and instructions
- · A calculating unit capable of performing both arithmetic and logical operations on the data
- A control unit which could interpret an instruction retrieved from the memory and select alternative courses of action based on the results of previous operations

It's a CPU centric system.

- · Each operation is carried only by the CPU
- · Every movement of data is made via the CPU
- · Memory is the only "direct access" storage device for the CPU
- Only one operation is carried out by the CPU at any time

# **Secondary Storage**

Non-volatile or permanent. High capacity. Low cost-per-megabyte.

Can be categorized into 3 types (Magnetic storages are included in a separate page):

- Magnetic
- Optical
- Floptical combination of optical and magnetic devices

# **Optical**

Uses tiny visible light beams or laser.

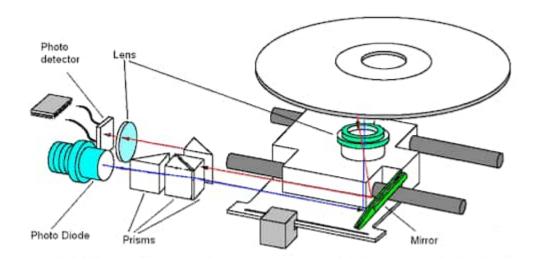
Examples:

- Compact Disk Read Only Memory
- Compact Disk Recordable
- Compact Disk ReWritable
- Digital Video Disk
- Digital Video Disk Recordable
- Digital Video Disk ReWritable

#### **Compact Disk**

Made of polycarbonate wafer, 120mm in diameter and 1.2mm thick, with a 15mm hole in the centre.

The wafer base is stamped or moulded with a single physical track in a spiral configuration starting from the inside of the disk and spiralling outwards. When examined under a microscope, pits (raised bumps along the track) and lands (flat areas between the pits) can be seen. Pits are 0 and lands are 1.



Light beam is generated by the photo diode. It is directed through several prisms. Then the reflecting mirror rotates the light beam by 90 $\square$ . The servomotor positions the beam onto the correct track on the CD by moving the reflective mirror. Then the reflected light from the surface of the CD is sent out through a focusing lens to the same mirror. Mirror rotates the beam by 90 $\square$  and it is sent through a beam splitter (set of prisms). The beam splitter directs the returning light towards another focusing lens. Then the directed light is detected by a photo detector (phototransistor) and it will invert the light into set of electrical impulses. These electrical impulses will indicate whether each bit is 1 or 0.

### Flash storage

Built using EEPROMs. High capacity and high transfer speed compared to other forms of portable media. Low latency, low noise, low power consumption, high reliability.

HDDs are now being replaced with SSDs which uses flash memory technology. RAM modules are also starting to use flash memory technology.

Categorized into 2 divisions based on operational characteristics:

- NAND Flash
- NOR Flash

Flash memory stores information in an array of memory cells made from floating gate transistors. In the traditional flash drives, each cell stores a single bit of data. However, newer flash devices known as multi- level cell devices can store more than one bit per cell by choosing between multiple levels of electrical charge.

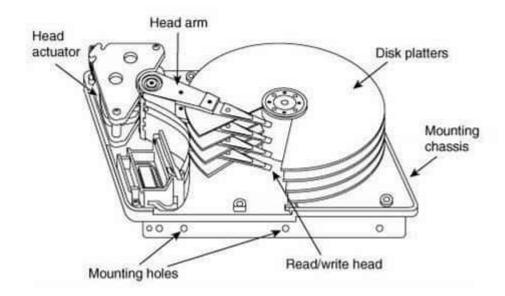
# **Magnetic Storage**

### Tape drive

Stores data on magnetic tapes. high capacity. cost-effective. slow. mainly used for data backups nowadays. Aka. DAT drives.

### Hard disk drive

Magnetic storage. Operationally identical to a conventional radio cassette tape. Uses a disk coated with magnetic medium. High data transfer speed.



Capacity can be improved by adding more platters. Platters are kept in dust free environment to avoid damages because of high speed. Each side of the platter require a read/write head.

Categorized by their capacity, controller, and platter rotation speed. Capacity ranges from 500GB to 8TB. Platter rotation speeds are 3600, 5400 and 7200 RPM.

Available disk controllers:

- Integrated Device Electronics
- Small Computer System Interface
- Serial AT Attachment Interface

#### Track

Single ring of data on one side of a platter.

#### Sector

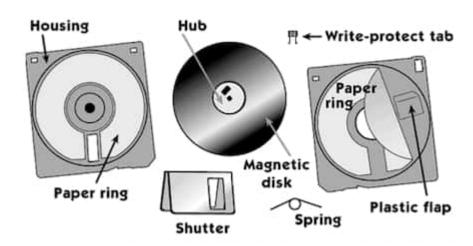
Fixed size (usually 512 bytes) divisions (usually about 900) of a track.

### Cylinder

Set of tracks on a disk, that are on each side of all platters in a stack and are at the same distance from the center of the disk.

### Floppy disk drive

Removable. Has a flexible magnetic medium that is enclosed in a (semi-)rigid plastic case.



Functionality of Floppy Disk

The magnetic coated, semi rigid, plastic disk is kept in a plastic housing and its two surfaces are covered by two paper rings (for protection). The disk is mounted on a hub and a rectangular shaped cut-out is used by the disk driver to firmly grab the disk while rotating. The read/write head access the disk through a small opening called the flap. The spring-loaded shutter is used to cover up the flap so that prevents any damages to the disk by dust particles. The shutter will open up only when the disk is inside the disk driver and when it is ejected the spring will automatically close the shutter. A write protect tab is used to prevent the disk been overwritten and it either opens or closes the write protect hole. If the write protect hole is closed the disk cannot be overwritten (then light cannot penetrate through the hole) and if the hole is open (light can penetrate through the hole) it can be overwritten. The head actuator mechanism is slightly different to the mechanism in a hard disk. In a hard disk the head arm moves laterally from centre of the disk towards to the edge while in a floppy disk the movement is horizontal. The read/write head is mounted on a spindle and the spindle is controlled by a stepper motor. The high density hole is only available in High Density floppy disks.

# **Computer Software**

A program is an ordered sequence of instructions that the hardware can execute. Software can be further categorized as system software and application software.

# **Embedded System**

TODO

# **Trends in Computing**

TODO

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