

3C7 DIGITAL SYSTEM DESIGN LABORATORY

Lab Session C

Department of Electronic and Electrical Engineering (e-Report submission)



Lab:

Construct 6-bit ripple adder/subtractor

Submitted by:

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Lab – 6-bit ripple adder/subtractor

The given assignment starts with initialization of new project with the provided module. The provided module full_adder was used to setup another 6-bit adder module.

Now, the testbench was setup using previous template, finally the behavioural simulation was started.

```
6bit_ripple_tb.v × 6bit_ripple_adder.v × full_adder.v × Untitled 3* ×
C:/Users/Rohan/Desktop/College/TCD/JS/S2/3C7/labC/6bit_ripple_tb.v
Q 🛗 🐟 🥕 🐰 🖺 🖍 📈 🎟 🔉
             wire test overflow;
             wire test cout;
            ripple adder uut (.x(test in0),.y(test in1),.sum(test out),.sel(select),.overflow(test overflow),.c out(test cout));
10
11 🖯
12 😓
            begin
13
14
15
                 test_in0 = 6'b000001;
                 test_in1 = 6'b000100;
16
                 select = 0;
     0
                 #200;
18
     0000000
                 test_in0 = 6'b000000;
19
                 test_in1 = 6'b000000;
                 select = 0;
21
                 #200;
                 test_in0 = 6'b000000;
23
                 test_in1 = 6'b111111;
                 select = 1;
     0
25
                 #200;
     0
26
                 test_in0 = 6'b100001;
27
                 test_in1 = 6'b000001;
     0
28
                 select = 0;
29
                 #200;
     0
30
                 test_in0 = 6'b011111;
31
                 test_in1 = 6'b000001;
     0
32
                 select = 1;
     0
33
                 #200;
34
     0
                 test_in0 = 6'b100000;
     0
35
                 test_in1 = 6'b100000;
36
                 select = 0;
     0
37 !
                 #200;
```

Figure 1.1. Testbench for the ripple adder module

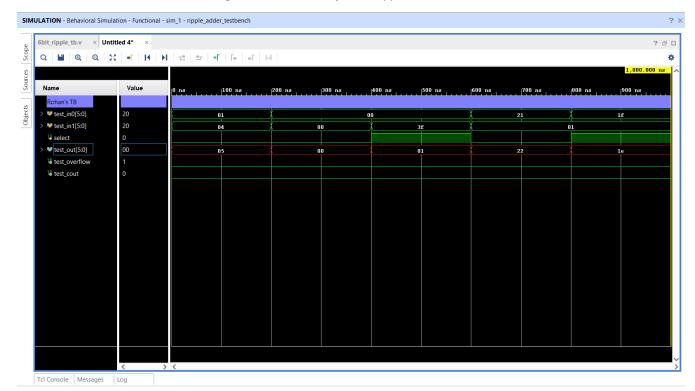


Figure 1.2. 6-bit ripple adder/subtractor simulation for the testbench

The test cases table:

				Expected			Observed			Pass/Fail
Test ID	X	Y	Sel	Cout	Overflow	Sum	Cout	Overflow	Sum	
1	6'b000001	6'b000100	0	0	0	6'b000101	0	0	6'b000101	Pass
2	6'b000000	6'b000000	0	0	0	6'b000000	0	0	6'b000000	Pass
3	6'b000000	6'b111111	1	0	0	6'b000001	0	0	6'b000001	Pass
4	6'b100001	6'b000001	0	0	0	6'b100010	0	0	6'b100010	Pass
5	6'b111111	6'b000001	1	0	0	6'b111110	0	0	6'b111110	Pass
6	6'b100000	6'b000001	0	0	1	6'b100001	0	1	6'b100001	Pass