

NP-completeness, Cook-Levin, and additional reductions

CS 580

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What we need to show: Given as input a Clique instance, i.e. an undirected graph $G = (V, E)$ and a number k , we can construct in polynomial time a 3SAT formula ϕ such that the graph G has a k -clique if and only if ϕ has a satisfying assignment.

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1. For each i , there is an i -th vertex in the clique: $\bigvee_{v \in V} x_{i,v}$.
2. For all i, j , the i -th vertex is different from the j -th vertex: for each $v \in V$, add $\bar{x}_{i,v} \vee \bar{x}_{j,v}$.

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2. For all i, j , the i -th vertex is different from the j -th vertex: for each $v \in V$, add $\bar{x}_{i,v} \vee \bar{x}_{j,v}$.
3. For each non-edge $(u, v) \notin E$, u and v cannot both belong to the clique: for each i, j , we have $\bar{x}_{i,u} \vee \bar{x}_{j,v}$.

NP-completeness

Definition (NP-complete): A language L is NP-complete if:

- $L \in \text{NP}$
- Every language $A \in \text{NP}$ is poly-time reducible to L .

Recall: A language $A \subseteq \Sigma^*$ is polynomial time (mapping) reducible to a language $B \subseteq \Sigma^*$ if there exists a polynomial time computable function $f : \Sigma^* \rightarrow \Sigma^*$ such that for every $x \in \Sigma^*$, we have:

$$x \in A \text{ if and only if } f(x) \in B.$$

The function f is the **reduction**.

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Proof: Let $L \in \text{NP}$ be a language.

Since A is NP-complete, there exists a poly-time reduction f such that for every $x \in \Sigma^*$, we have: $x \in L$ if and only if $f(x) \in A$. Since $A \in \text{P}$, there is a poly-time TM M to decide A .

Then we can get a poly-time algorithm for L . On each input $x \in \Sigma^*$:

- Compute $f(x) = y$
- Run the TM M on y and output the same answer as M .

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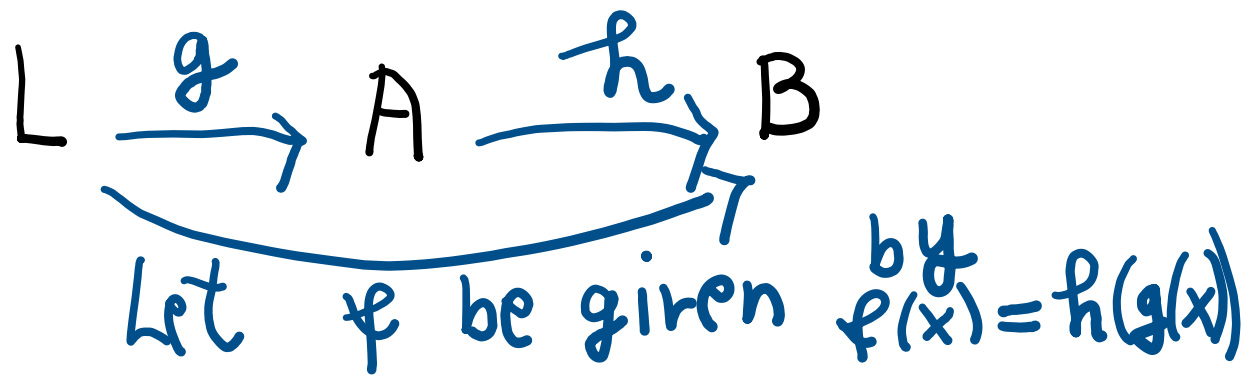
Proof: Since A is NP-complete, every language $L \in \text{NP}$ reduces to A in polynomial time. Given such a language L , let f be the poly-time computable function that reduces L to A . Suppose g is the polynomial time computable function that reduces A to B . Then the composition gives a poly-time reduction from L to B .

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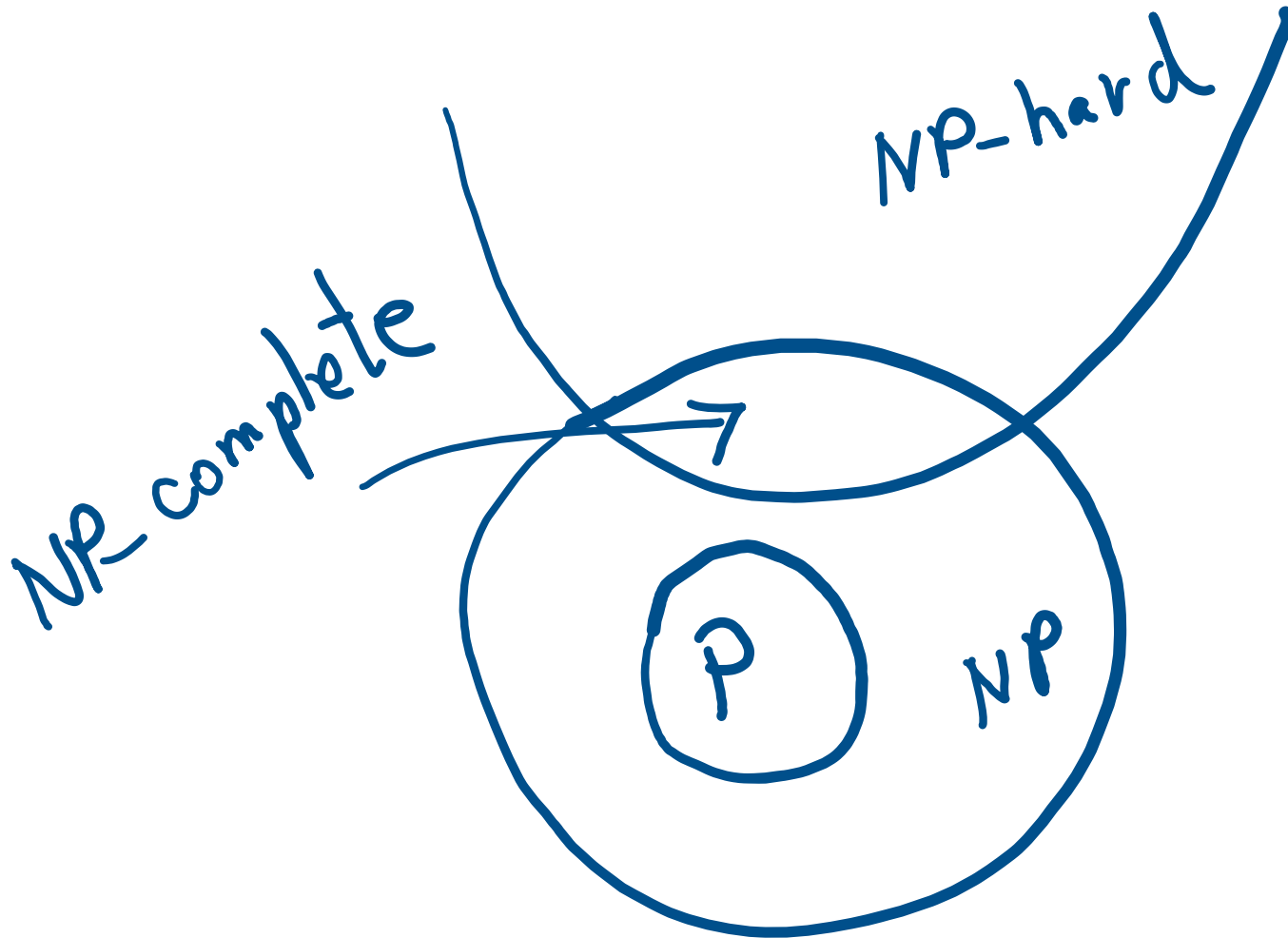
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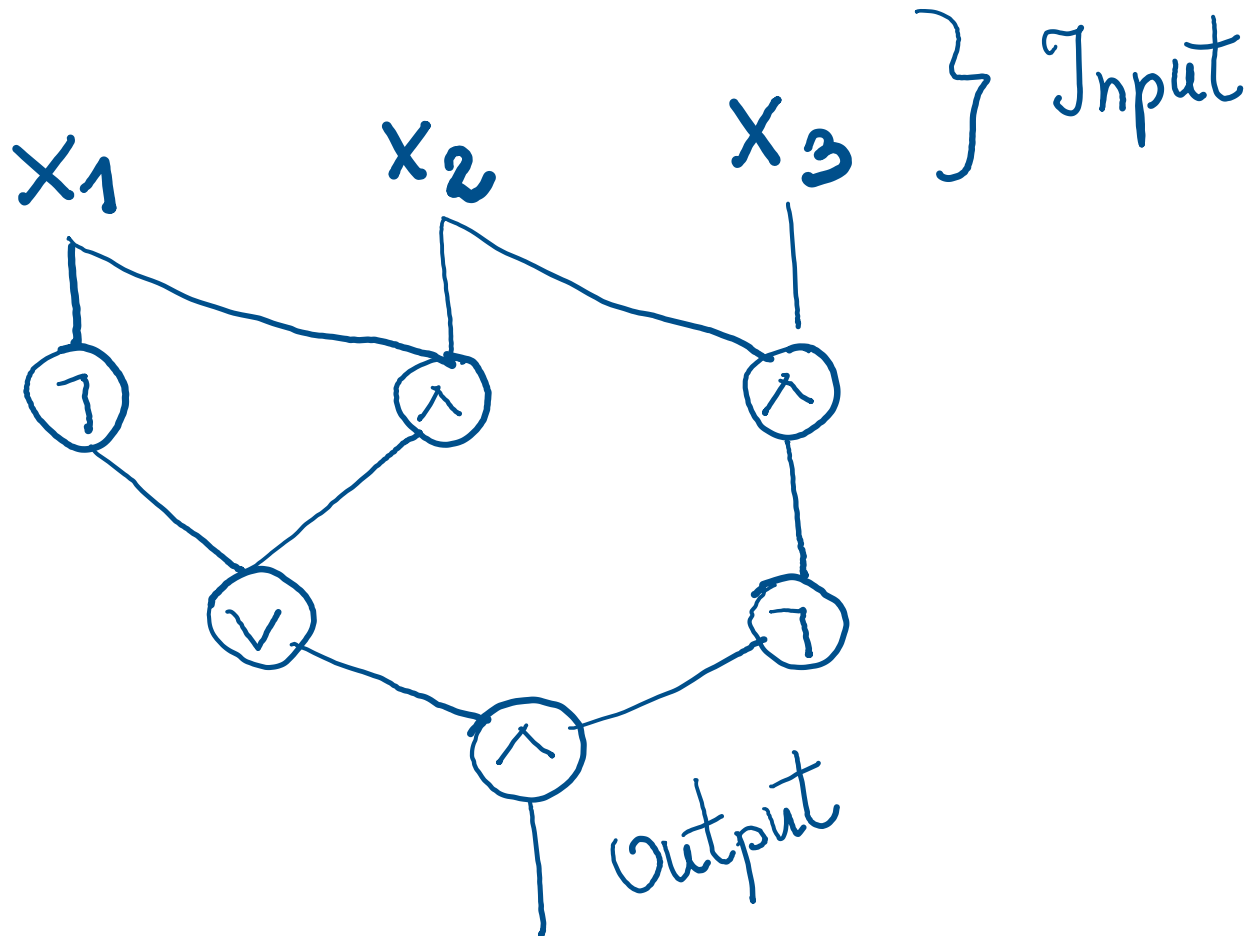
Definition (Boolean circuits): A Boolean circuit is a collection of gates and inputs connected by wires (no cycles). Gates do AND, OR, NOT operations.

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Example:



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Function computed by the circuit: To each circuit C we can associate a function $f_C : \{0,1\}^n \rightarrow \{0,1\}$, where if C outputs b on input (x_1, \dots, x_n) , then $f_C(x_1, \dots, x_n) = b$.

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Theorem: Let $t : N \rightarrow N$ be a function, where $t(n) \geq n$ [i.e. the TM reads the input]. If $A \in DTIME(t(n))$, then for each $n \in N$ there is a circuit C_n of size $O(t(n)^2)$ such that for every $x = x_1 \dots x_n \in \{0,1\}^n$, we have $x \in A$ if and only if $C_n(x) = 1$.

Proof: We modify every TM considered so that when it's about to accept, it cleans the tape.

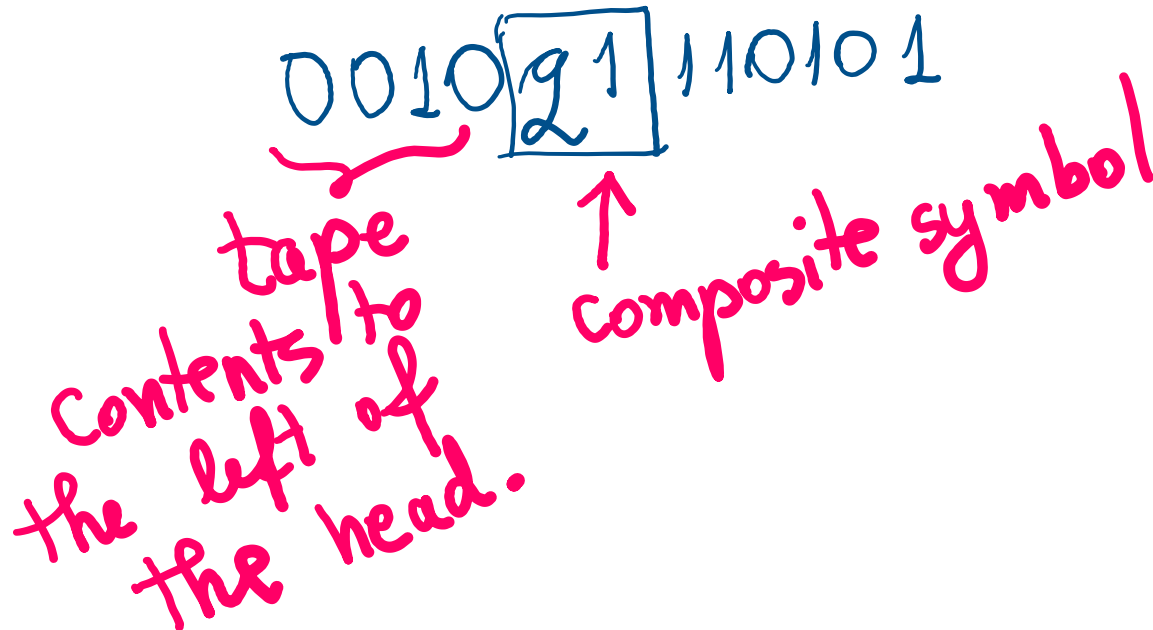
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We will make some composite symbols to capture the state and symbol under the tape head together:



Let $A \in DTIME(t(n))$. Then there is a TM M that runs in $O(t(n))$ that decides A ; suppose it is $C * t(n)$ for a constant C .

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We draw a table to visualize the execution of the TM M .

$\text{cell}(1,1)$	q ₀	0	1	0	w	w			w	w	w
$\text{cell}(\text{c.t}(n), 2)$											

↑ accept/reject

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Each row represents a **configuration** of the TM.

q_0	0	1	0	□	□				□	□	□

$C \cdot t(n)$ rows

$C \cdot t(n)$ columns

$cell(c \cdot t(n), 2)$ \nearrow accept/reject

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[illegible]

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Observation: if we know $\text{cell}(i-1, j-1)$, $\text{cell}(i-1, j)$, and $\text{cell}(i-1, j+1)$, we also know $\text{cell}(i, j)$ by the way the transition function δ of the machine works.

a	b	a
	c	

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We can begin constructing the circuit C_n .

Let k = number of elements in $\Gamma \cup (Q \times \Gamma)$ [i.e. one for each tape symbol, and also one for each combined symbol (state, symbol under tape head)].

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- E.g., if $\text{light}[i,j,s]$ is on, then $\text{cell}[i,j]$ contains the symbol s .

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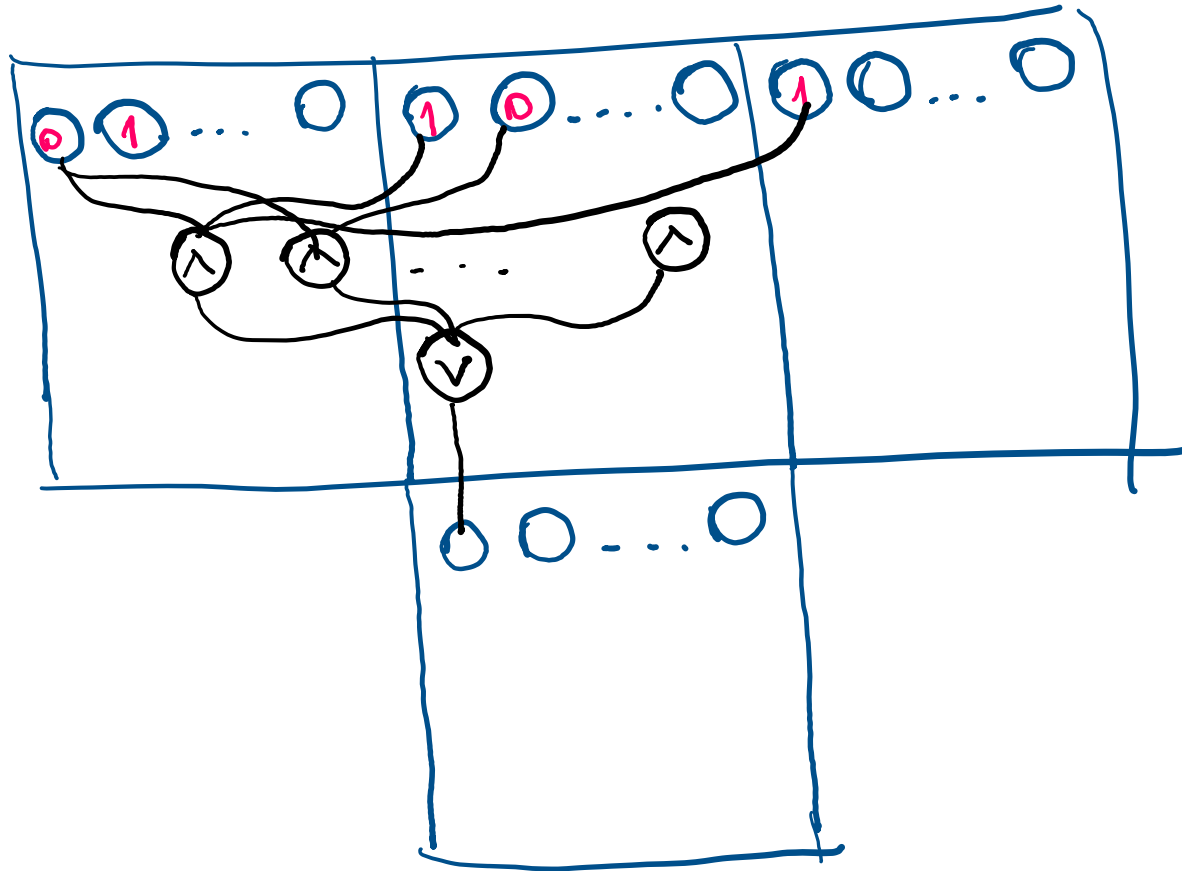
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Then we can wire the circuit so that if $\text{light}[i-1,j-1,a]$, $\text{light}[i-1,j,b]$, and $\text{light}[i-1,j+1,c]$ are on, then $\text{light}[i,j,s]$ is on.

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How should we wire the cells in row 1, e.g. $\text{light}[1, 1, q_0 1]$?

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The cells in row 1 have no predecessors and are handled in a special way:

- $\text{light}[1, 1, q_0 1]$ is connected to input x_1 . Since the start state is q_0 and the head starts at x_1 .
- $\text{light}[1, 1, q_0 0]$?

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- Similarly, the next ones.
- In particular $\text{light}[1, n + 1, \sqcup], \dots, \text{light}[1, C * t(n), \sqcup]$ are on.

What is the output gate?

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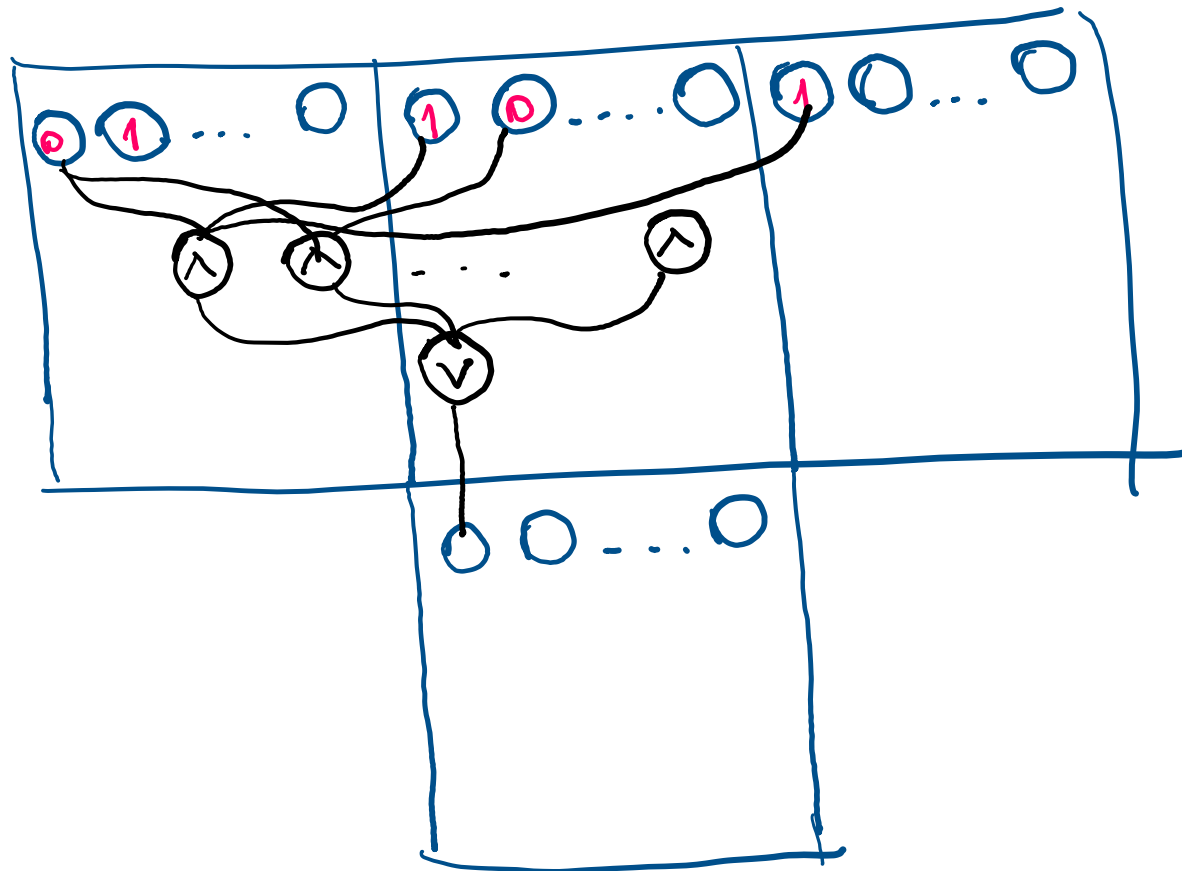
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The output gate is the one attached to $\text{light}[C * t(n), 1, q_{\text{accept}}]$.

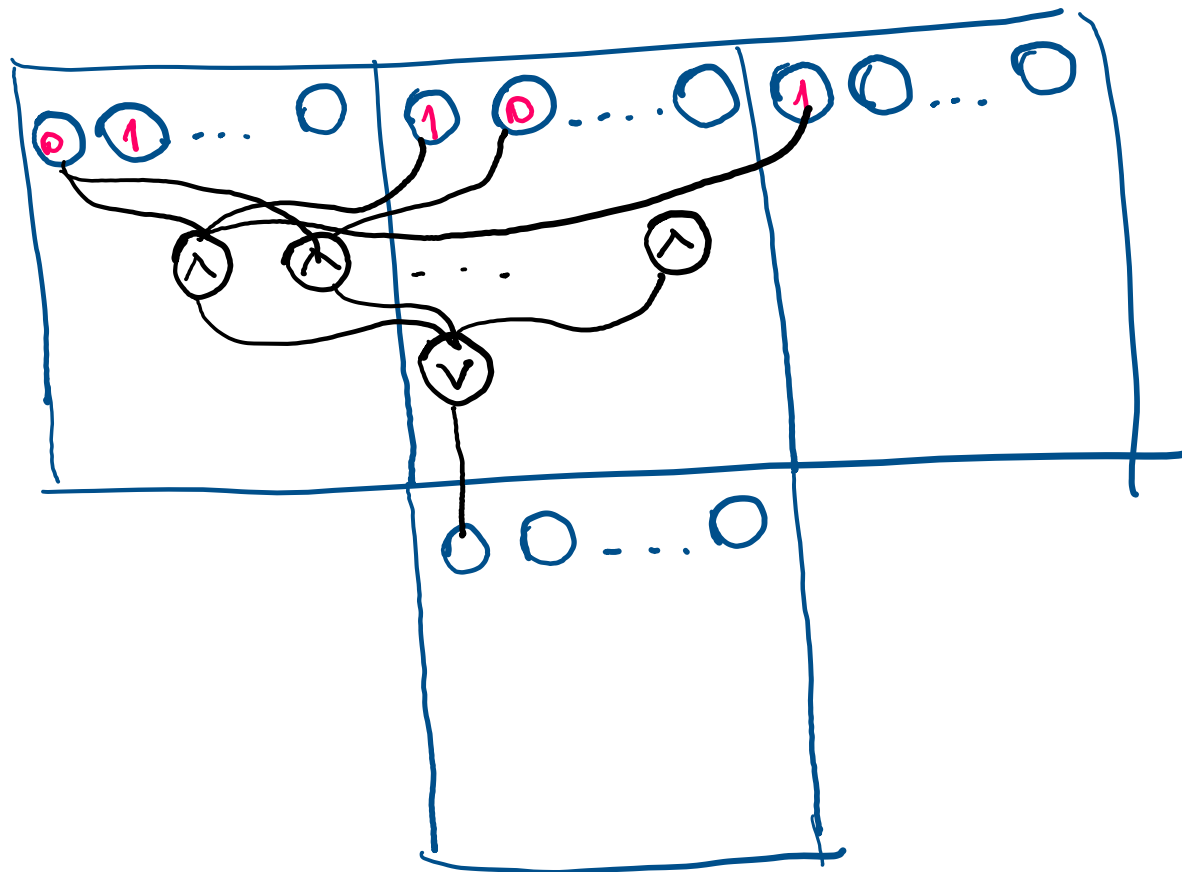
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How many gates do we use for each cell?



Proof (cont): Let $A \in DTIME(t(n))$. Then there is a TM M that runs in $O(t(n))$ that decides A ; suppose it is $C * t(n)$ for a constant C .

For each cell we use $O(f(k))$ gates to obtain its input from the three cells above. There are $C * t(n)^2$ cells, so overall the circuit has $O(t(n)^2)$ gates.



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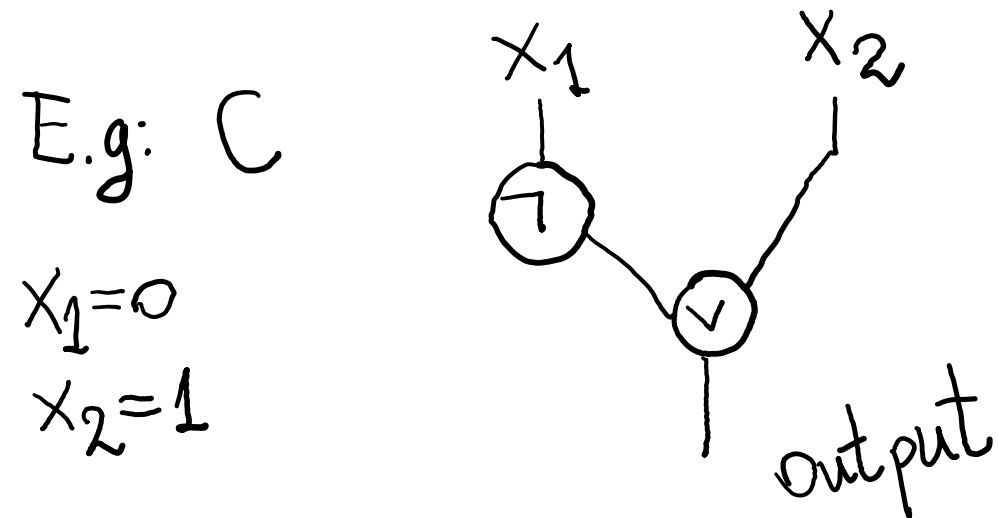
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Proof: Design a verifier for CircuitSAT. Given as input (C, x) , where C is a circuit and $x = x_1, \dots, x_n$ is an input for C , check if $C(x) = 1$.



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Can we show that CircuitSAT is NP-complete? **Hint:** Use the previous theorem.

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