

EDRD 4420: Teaching Adolescents with Disabilities in Literacy

3 Credit Hours

Prerequisite: EDRD 3320

This course prepares prospective content teachers to increase the literacy of students with disabilities in inclusive classrooms. Teacher candidates will learn to: (a) recognize various types of reading and writing disabilities; (b) effectively implement Response to Instruction; (c) develop inclusive, multi-level lesson plans embed accommodations and modifications; (d) identify appropriate roles for parents in fostering literacy in students with disabilities; and (e) work collaboratively with special education teachers.

ECET 1001L: Introduction to Electrical Engineering Technology Lab

1 Credit Hours

Prerequisite: ENGR 1000

A continuation of ENGR 1000, this course actively introduces the student to an exciting career of applied engineering within the context of Electrical Engineering Technology. The class/lab blends real-world workplace content, engineering tools/processes, critical engineering skills/competencies, and hands-on input-culminating in an exciting class competition that demonstrates an engineered solution to a defined problem.

ECET 1101: Circuits I

3 Credit Hours

Prerequisite: ECET 1001L

Concurrent: MATH 1190

This course introduces electrical quantities, element configurations, and circuit analysis tools. DC circuit analysis is emphasized, including circuits containing dependent sources and techniques such as mesh analysis, nodal analysis, superposition, and Thevenin equivalence. Reactive elements are examined, as are the transient responses of circuits that contain a single reactive element. Sinusoidal AC circuit analysis is introduced through the application of basic circuit laws to series and parallel configurations.

ECET 1200L: Digital I Lab

1 Credit Hours

Concurrent: ECET 1200

The laboratory component of ECET 1200 is designed to provide the student with hands-on experience in the fundamental concepts and techniques of discrete logic design and implementation using Boolean logic. The concepts are extended into programmable logic design (PLD) using a VHDL programming language. Lab exercises are based on modern digital design principles and practices.