

Homework 4 Answer Sheet

Please state the name, SID and email of each member of your group.

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Logisim Version: 2.7.1

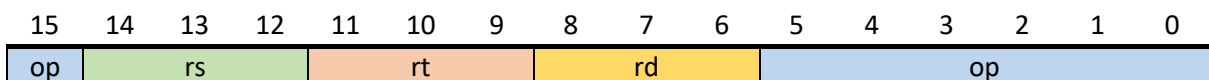
OS (Window/MAC/Linux): Windows 11 Pro 25H2

- A. Do all members make significant contributions to this homework? If not, please specify the details.

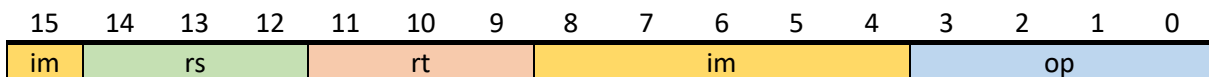
Yes

- B. Please explain how many types of instructions are supported in your processor, and explain the format of each type of instructions (e.g., which bits are used as the operation or function code, which bits are used to index the 1st, 2nd or 3rd operand, and which bits are used to store the immediate number). You can draw figures to better explain your answer.

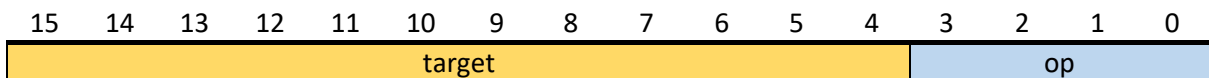
R-Type:



I-Type:



J-Type:



- C. Please explain the format of each instruction (including the format of this instruction and its operation codes, and other information if needed).

li	<div>Type: I-Type Opcode: 0000 Format:</div> <table><tr><td>Immediate 15</td><td>R2 14-12</td><td>R1 11-9</td><td>Immediate 8-4</td><td>Opcode 3-0</td></tr></table> <div>Example: li \$r1, 1 Binary: 0 001 000 00001 0000 Hex: 1010 li \$r3, 10</div>	Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0
Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0		

	Binary: 0 011 000 01010 0000 Hex: 30A0					
add	Type: R-Type Opcode: 0001 Format: <table><tr><td>Opcode 15</td><td>R2 14-12</td><td>R1 11-9</td><td>Register Destination 8-6</td><td>Opcode 5-0</td></tr></table> Example: add \$r2, \$r1, \$r2 Binary: 0 010 001 010 000001 Hex: 2281	Opcode 15	R2 14-12	R1 11-9	Register Destination 8-6	Opcode 5-0
Opcode 15	R2 14-12	R1 11-9	Register Destination 8-6	Opcode 5-0		
and	Type: R-Type Opcode: 0010 Format: <table><tr><td>Opcode 15</td><td>R2 14-12</td><td>R1 11-9</td><td>Register Destination 8-6</td><td>Opcode 5-0</td></tr></table> Example: and \$r3, \$r1, \$r2 Binary: 0 010 001 011 000010 Hex: 22C2	Opcode 15	R2 14-12	R1 11-9	Register Destination 8-6	Opcode 5-0
Opcode 15	R2 14-12	R1 11-9	Register Destination 8-6	Opcode 5-0		
or	Type: R-Type Opcode: 0011 Format: <table><tr><td>Opcode 15</td><td>R2 14-12</td><td>R1 11-9</td><td>Register Destination 8-6</td><td>Opcode 5-0</td></tr></table> Example: or \$r4, \$r1, \$r2 Binary: 0 010 001 100 000011 Hex: 2303	Opcode 15	R2 14-12	R1 11-9	Register Destination 8-6	Opcode 5-0
Opcode 15	R2 14-12	R1 11-9	Register Destination 8-6	Opcode 5-0		
load	Type: I-Type Opcode: 0110 Format: <table><tr><td>Immediate 15</td><td>R2 14-12</td><td>R1 11-9</td><td>Immediate 8-4</td><td>Opcode 3-0</td></tr></table> Example: load \$r6, \$r7 Binary: 0 110 111 00000 0100 Hex: 6E04	Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0
Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0		
store	Type: I-Type Opcode:0101 Format:					

	<table><tr><td>Immediate 15</td><td>R2 14-12</td><td>R1 11-9</td><td>Immediate 8-4</td><td>Opcode 3-0</td></tr></table> Example: store \$r3, \$r7 Binary: 0 011 111 00000 0101 Hex: 3E05	Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0
Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0		
move	Type: I-Type Opcode:0110 Format: <table><tr><td>Immediate 15</td><td>R2 14-12</td><td>R1 11-9</td><td>Immediate 8-4</td><td>Opcode 3-0</td></tr></table> Example: move \$r4, \$r3 Binary: 0 100 011 00000 0110 Hex:4606	Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0
Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0		
addi	Type: I-Type Opcode: 0111 Format: <table><tr><td>Immediate 15</td><td>R2 14-12</td><td>R1 11-9</td><td>Immediate 8-4</td><td>Opcode 3-0</td></tr></table> Example: Addi \$r2 \$r1, 1 Binary: 0 010 001 00001 0111 Hex: 2217	Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0
Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0		
andi	Type: I-Type Opcode:1000 Format: <table><tr><td>Immediate 15</td><td>R2 14-12</td><td>R1 11-9</td><td>Immediate 8-4</td><td>Opcode 3-0</td></tr></table> Example: andi \$r3, \$r1, 3 Binary: 0 011 001 00011 1000 Hex: 3238	Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0
Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0		

ori	<p>Type: I-Type Opcode:1001 Format:</p> <table><tr><td>Immediate 15</td><td>R2 14-12</td><td>R1 11-9</td><td>Immediate 8-4</td><td>Opcode 3-0</td></tr></table> <p>Example: ori \$r4, \$r3, 8 Binary: 0 100 011 01000 1001 Hex: 4689</p>	Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0
Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0		
ble	<p>Type: I-Type Opcode:1010 Format:</p> <table><tr><td>Immediate 15</td><td>R2 14-12</td><td>R1 11-9</td><td>Immediate 8-4</td><td>Opcode 3-0</td></tr></table> <p>Example: ble \$r2, \$r3, -1 Binary: 1 011 010 11111 1010 Hex: B5FA</p>	Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0
Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0		
bne	<p>Type: I-Type Opcode:1011 Format:</p> <table><tr><td>Immediate 15</td><td>R2 14-12</td><td>R1 11-9</td><td>Immediate 8-4</td><td>Opcode 3-0</td></tr></table> <p>Example: bne \$r2, \$r3, -1 Binary: 1 011 010 11111 1011 Hex: B5FB</p>	Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0
Immediate 15	R2 14-12	R1 11-9	Immediate 8-4	Opcode 3-0		
jump	<p>Type: J-Type Opcode: 1100 Format:</p> <table><tr><td>Immediate 15-4</td><td>Opcode 3-0</td></tr></table> <p>Example: jump 3 Binary: 000000000011 1100 Hex: 003C</p>	Immediate 15-4	Opcode 3-0			
Immediate 15-4	Opcode 3-0					
call	<p>Type: J-Type Opcode: 1101</p>					

	Format: <div> <div>Immediate 15-4</div> <div>Opcode 3-0</div> </div> Example: call 3 Binary: 000000000011 1101 Hex: 003D
rtn	Type: J-Type Opcode: 1101 Format: <div> <div>Immediate 15-4</div> <div>Opcode 3-0</div> </div> Example: rtn Binary: 000000000000 1110 Hex: 003E
halt	Type: - Opcode: 1111 Format: <div> <div>Immediate 15-4</div> <div>Opcode 3-0</div> </div> Example: halt Binary: 000000000000 1111 Hex: 000F

D. Fill the following tables with the machine codes of each instruction of the testing programs:

Test program 1:

instruction	machine code (binary)	machine code (hex)
li \$r1, 1	00010000000010000	1010
li \$r2, 2	00100000000100000	2020
li \$r3, 10	0011000010100000	30A0
add \$r2, \$r1, \$r2	0010001010000001	2281
ble \$r2, \$r3, -1	1011010111111010	B5FA
halt	0000000000001111	000f

Test program 2:

instruction	machine code (binary)	machine code (hex)
li \$r1, 6	0001000001100000	1060
li \$r2, 5	0010000001010000	2050

andi \$r3, \$r1, 3	0011001000111000	3238
ori \$r4, \$r3, 8	0100011010001001	4689
halt	0000000000001111	000F

Test program 3:

instruction	machine code (binary)	machine code (hex)
li \$r1, 6	0001000001100000	1060
li \$r2, 5	0010000001010000	2050
and \$r3, \$r1, \$r2	0010001011000010	22C2
li \$r8, 0	0111000000000000	7000
store \$r3, \$r8	0011111000000101	3E05
or \$r4, \$r1, \$r2	0010001100000011	2303
li \$r8, 1	0111000000010000	7010
store \$r4, \$r8	0100111000000101	4E05
li \$r8, 1	0111000000010000	7010
load \$r7, \$r8	0110111000000100	6E04
halt	0000000000001111	000F

Test program 4:

instruction	machine code (binary)	machine code (hex)
li \$r1, 6	0001000001100000	1060
li \$r2, 4	0010000001000000	2040
call 7	0000000001111101	007D
move \$r4, \$r3	0100011000000110	4606
li \$r1, 7	0001000001110000	1070
li \$r2, 8	0010000010000000	2080
call 3	000000000111101	003D
move \$r5, \$r3	0101011000000110	5606
jump 3	000000000111100	003C
add \$r3, \$r1, \$r2	0010001011000001	22C1
rtn	0000000000001110	000E
halt	0000000000001111	000F

Test program 5:

instruction	machine code (binary)	machine code (hex)
li \$r1, 2	0001000000100000	1020
addi \$r2 \$r1, 1	0010001000010111	2217
halt	0000000000001111	000F