# AZORIX VLSI

## Design Verification Course

Industry Ready

Diploma Programs

## COURSE OVERVIEW

Duration: 8 Months | Base Fee: [1]XXXX | Level: Beginner to

Transform your career with our comprehensive VLSI Design Verification program. Master the complete digital verification flow from RTL design to advanced verification methodologies.

## CORE MODULES (INCLUDED)

- 1. Verilog Module (6 weeks) - RTL design fundamentals and digital circuit implementation

- 2. SystemVerilog Module (8 weeks) - Advanced verification techniques and OOP concepts

- 3. UVM Module (10 weeks) - Industry-standard verification methodology

## ADD-ON SPECIALIZATIONS (OPTIONAL)

Ø=Þ„ PCIe Verification (4 weeks) - +[1]25,000

Ø=Ü» SoC Integration & Verification (6 weeks) - +[1]30,000

Ø<ß×þ IP Block Verification & VIP Development (3 weeks) - +[1]20,000

## PRICING OPTIONS

- Core DV Program: [1]1,20,000 (6 months)
- DV + One Specialization: [1]1,40,000-[1]1,50,000 (Most Popular)
- Complete Master Program: [1]1,95,000 - Save [1]25,000! (11 months)

## WHY CHOOSE AZORIX VLSI?

• 95% Placement Success Rate

• 2 Years Comprehensive Placement Support