“Don’t Just Learn VLSI
Build the Future with it.”

Mentors & Partners

OUR MENTORS
Dhanush Babu Pasapuleti
Director & Principal
5 Years in Semiconductor Design
& VLSI Architecture

SENIOR MENTOR
Saketh
Silicon Advisory
10 Years experience in
Digital Electronics & Verification

SENIOR MENTOR
Tajashwini Karli
Trainer
10 Years experience

AZORIX
VLSI
TRAINING INSTITUTE

“Adaptive, Industry-Focused Training for Future Chip Designers”

Courses

- **Verilog**
- **System Verilog**
- **UVM**

Azorix VLSI Training Institute
Plot No. 72, Beside Spring Board
Jubilee Enclave, HITEC City
Hyderabad, Telangana 500081
www.azorixvlsi.com
9052653636/9052633636
info@azorixvlsi.com

Companies we work with

Capgemini · HCLTech · Truechip (The Verification IP Specialist) · AEVA · smartsoc solutions · AMD · LEADSOC (partnering your success) · ANALOG DEVICES · Qualcomm · L&T · AXIADO

Limited SEATS
Open Enroll Now

About us

At Azorix, we believe in adaptive learning-training that matches your strengths, pace, and career goals.

- Hands-on projects (FIFO's, Memory Systems, AHB, AXI)
- Industry Mentors with 15+ Years' Experience
- Job-Ready Skills & 2-Year Career Support

Why Choose Us

Dedicated Mentor - Mentee Guidance

Personalized 1-on-1 Training

Buddy
Buddy Collaborative Learning

Industry
Expert Trainers (15+ years)

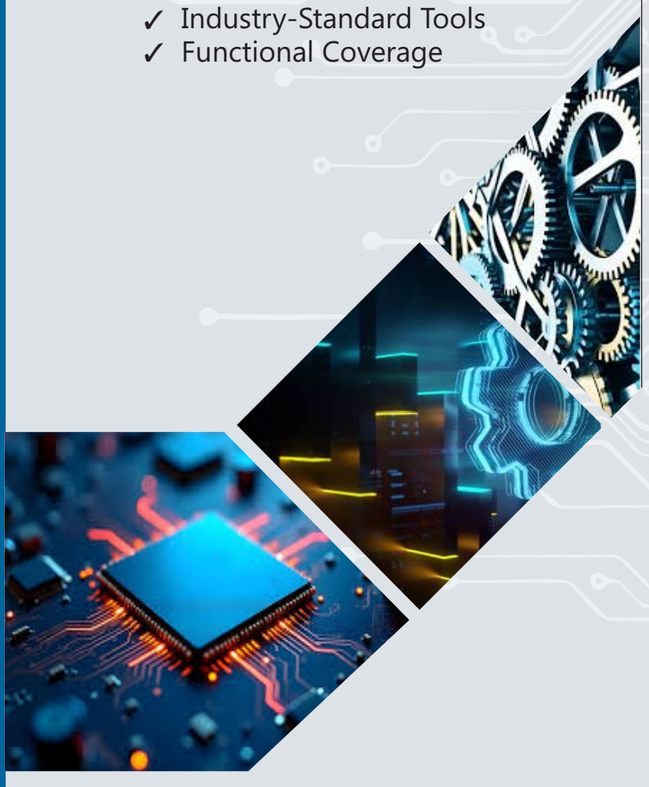
Hands
On Projects Employers Value

CAREER SUCCESS SUPPORT

(2 Years Placement Help)

Verilog
Digital Design Fundamentals

System Verilog
Advanced Verification Mastery

UVM
Test bench Architecture
Coverage
Assertions & Projects

Includes
- ✓ Assertion-Based Verification
- ✓ Industry-Standard Tools
- ✓ Functional Coverage