Metric Accuracy Testing 2

Processes

PID	Arrival	Burst	Priority
P1	0	5	3
P2	2	6	4
P3	3	4	2

Turnaround =
   complete - arrival

waiting =
   turnaround - burst

respone =
   start time - Arrival

FCFS

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
P1	P2	P3

Expected

PID	Turnaround	waiting	response
P1	5	0	0
P2	9	3	3
P3	12	8	8

Sim

PID	Turnaround	waiting	response
P1	5	0	0
P2	9	3	3
P3	12	8	8

✓

SJF

0 P1 1 2 P2 3 P3 4 5 6 7 8 9 10 11 12 13 14 15
P1	P3	P2

Expected

PID	Turnaround	waiting	response
P1	5	0	0
P2	13	7	7
P3	6	2	2

Sim

PID	Turnaround	waiting	response
P1	5	0	0
P2	13	7	7
P3	6	2	2

✓

SRTF

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
P1	P3	P2

Expected

PID	Turnaround	waiting	response
P1	5	0	0
P2	13	7	7
P3	6	2	2

Sim

PID	Turnaround	waiting	response
P1	5	0	0
P2	13	7	7
P3	6	2	2

✓

Priority

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
P1	P3	P2

Expected

PID	Turnaround	waiting	response
P1	5	0	0
P2	13	7	7
P3	6	2	2

Sim

PID	Turnaround	waiting	response
P1	5	0	0
P2	13	7	7
P3	6	2	2

✓

Preemptive priority

Timeline: 0 P1, 1, 2 P2, 3 P3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15

| P1 | P3 | P1 | P2 |

2 sec left

Expected

PID	Turnaround	waiting	response
P1	9	4	0
P2	13	7	7
P3	4	0	0

Sim

PID	Turnaround	waiting	response
P1	9	4	0
P2	13	7	7
P3	4	0	0

✓

Round robin time quantum = 3

Timeline: 0 P1, 1, 2 P2, 3 P3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15

| P1 | P2 | P3 | P1 | P2 | P3 |

1 sec left   3 sec left   1 sec left

Expected

PID	Turnaround	waiting	response
P1	11	6	0
P2	12	6	1
P3	12	8	3

Sim

PID	Turnaround	waiting	response
P1	11	6	0
P2	12	6	1
P3	12	8	3

✓