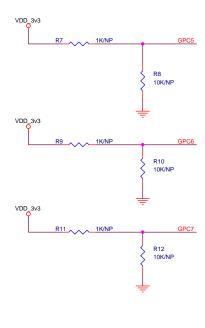
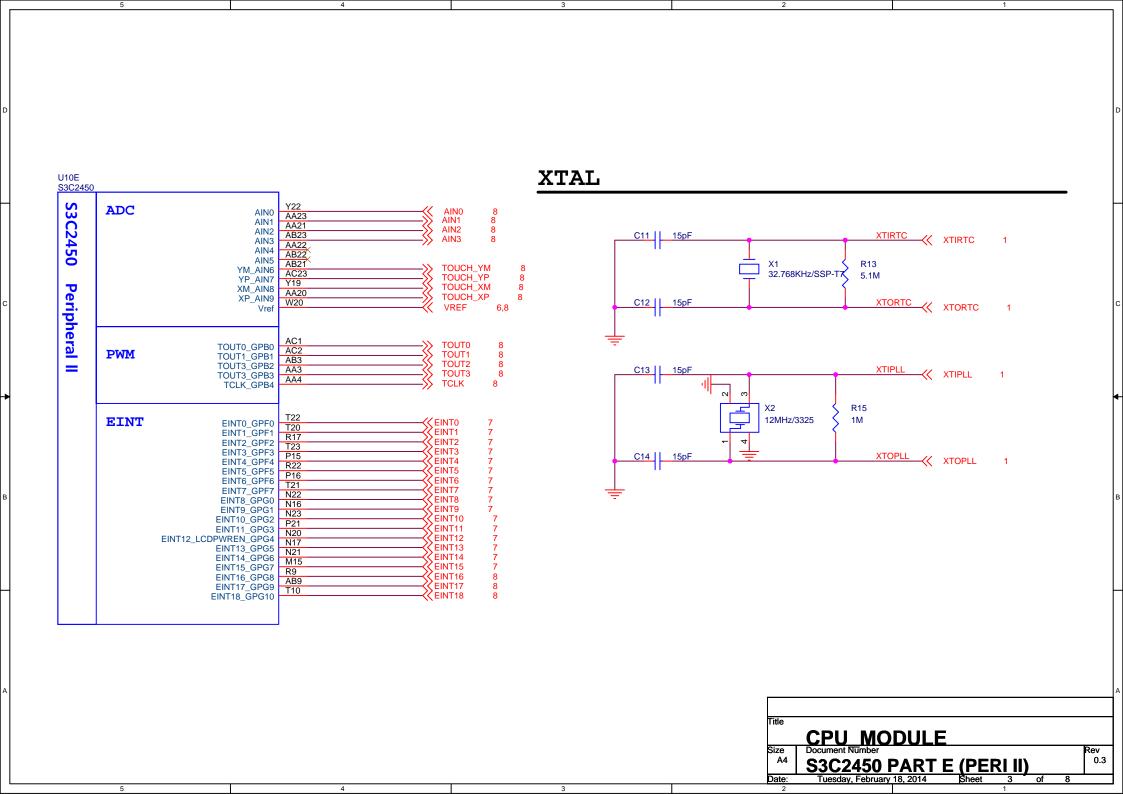


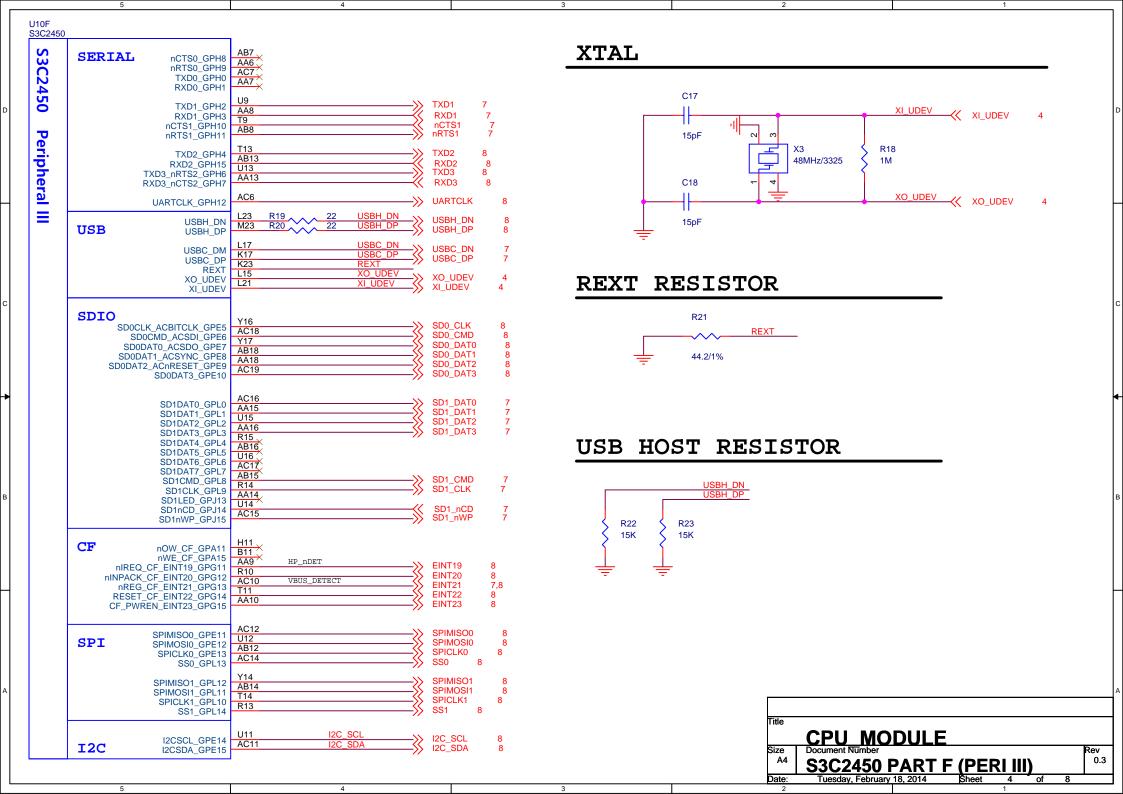
U10D S3C2450 VD0_GPC8 VD1_GPC9 VD2_GPC10 VD3_GPC11 VD4_GPC12 VD5_GPC13 VD6_GPC14 VD7_GPC13 VD6_GPC14 VD7_GPC13 VD6_GPC14 VD7_GPC14 VD7_GPC14 VD7_GPC14 VD1_GPD2 VD11_GPD2 VD11_GPD3 VD13_GPD3 VD18_GPD10 VD18_G S3C2450 LCD LCD_D2 LCD_D[7:2] RED Peripheral I >> LCD_D[15:10] GREEN VD18_GPD10 VD19_GPCD11 VD20_GPD12 VD21_GPD13 VD22_GPD14 VD23_GPD15 >> LCD_D[23:18] **BLUE** LEND_GPC0 M9 VCLK_GPC1 R4 VLINE_GPC2 VFRAME_GPC3 VM_GPC4 N1 M9 P3 N7 LCD_LEND LCD_VCLK LCD_HSYNC LCD_VSYNC LCD_DE LCDVF0_GPC5 LCDVF1_GPC6 LCDVF2_GPC7 nXDACK1-GPB7 nXCREQ1_GPB8 nXDACK0_GPB9 nXDREQ0_GPB10 AC5 GPI04 AA5 GPI05 AB6 GPI06 U8 GPI07 CAMRESET_GPJ12 M7 CAMCLKOUT_GPJ11 CAMPCLK_GPJ8 K2 CAM CAM_RESET CAM_CLKOUT CAM_PCLK CAMHREF_GPJ10 K7 CAMVSYNC_GPJ9 CAM_HSYNC CAM_VSYNC CAMDATAO GPJO L8 CAMDATA1 GPJ1 L2 CAMDATA2 GPJ2 T7 CAMDATA3 GPJ3 M2 CAMDATA4 GPJ4 L9 CAMDATA6 GPJ6 CAMDATA6 GPJ6 CAMDATA7 GPJ7 M8 CAM_DATO CAM_DAT1 CAM_DAT2 CAM_DAT3 CAM_DAT4 CAM_DAT5 CAM_DAT6 CAM_DAT7 nXBACK_GPB5 AC4 Y5 CPU_RTCK TP4 DMA Audio I2S_LRCK I2S_SCLK I2S_CDCLK I2S_DIN I2S_DOUT

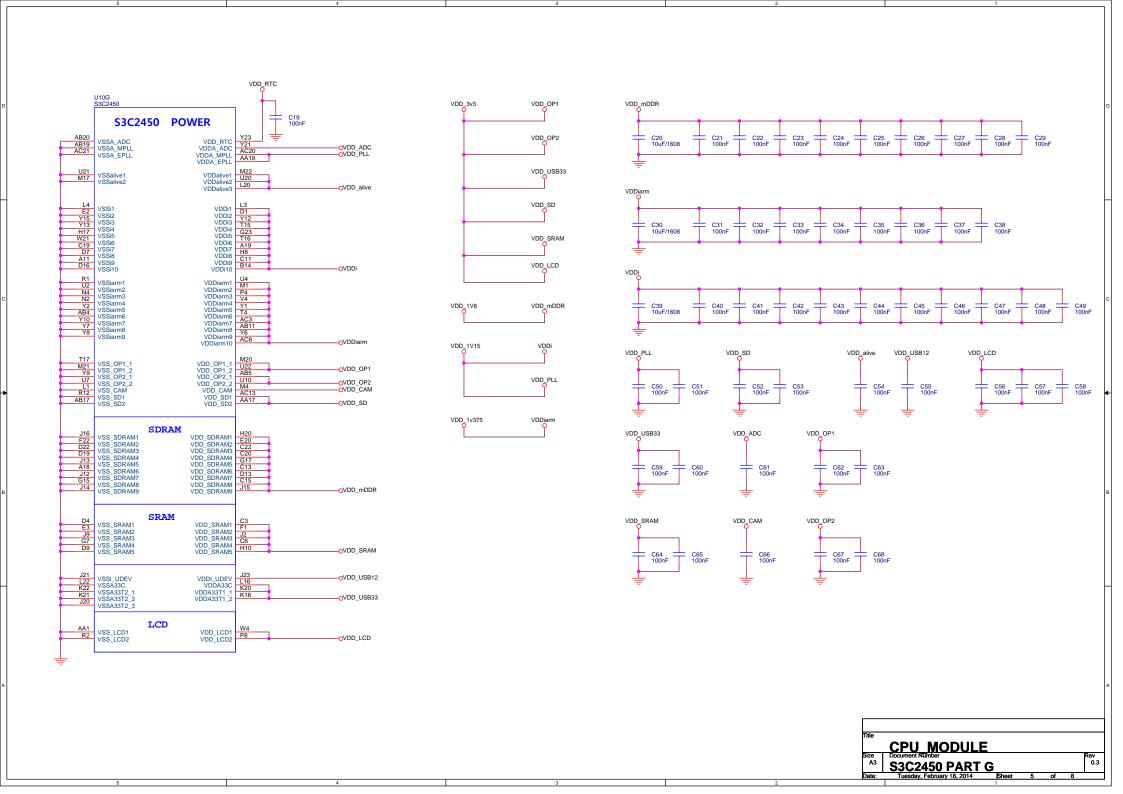
5 GPC5/6/7 PIN CONFIGURATION TABLE IN IROM BOOT MODE

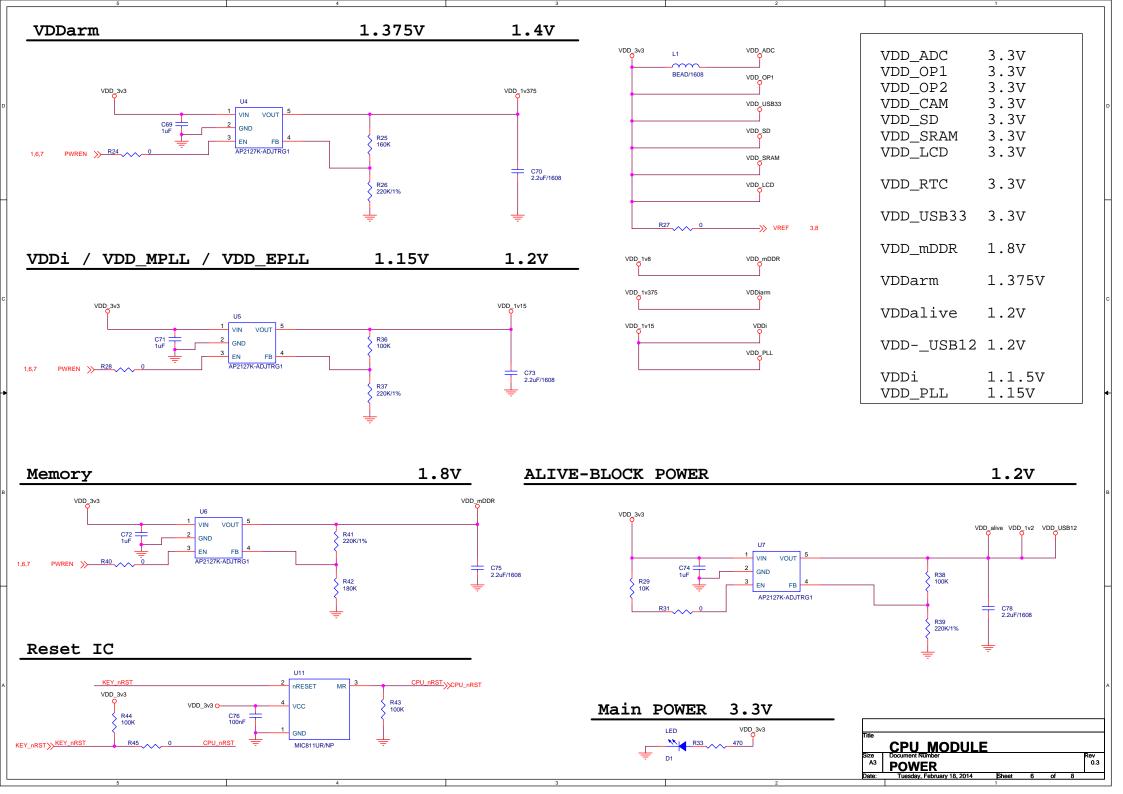
	Page	Address Cycle	GPC7 [2]	GPC6 [1]	GPC5 [0]
MMC(MoviNAND/iNand)	-	-	0	0	0
Reserved	-	-	0	0	1
Nand	512	3	0	1	0
		4	0	1	1
	2048	4	1	0	0
		5	1	0	1
	4096	5	1	1	0



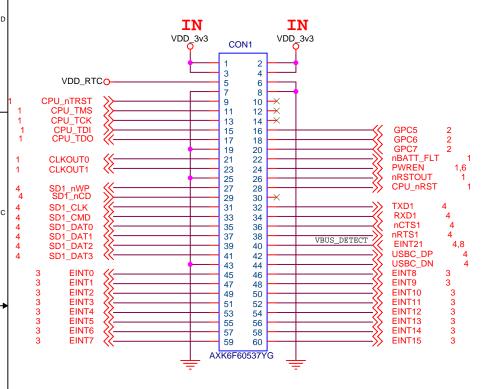


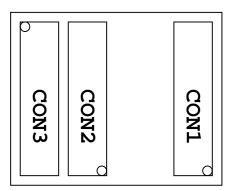






EXPANSION CONNECTOR 1





Title

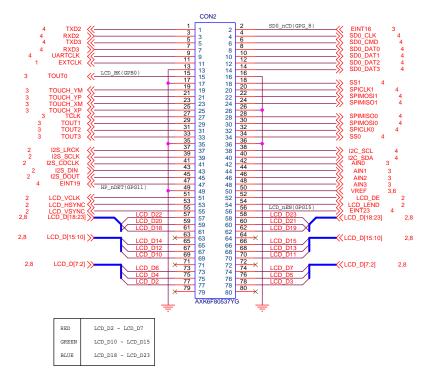
CPU MODULE

Size A4 EXPANSION CONNECTOR

Date: Tuesday, February 18, 2014 Sheet 7 of 8

2

EXPANSION CONNECTOR 3



EXPANSION CONNECTOR 2

