

Game Technology

Lecture 6 – 21.11.2014

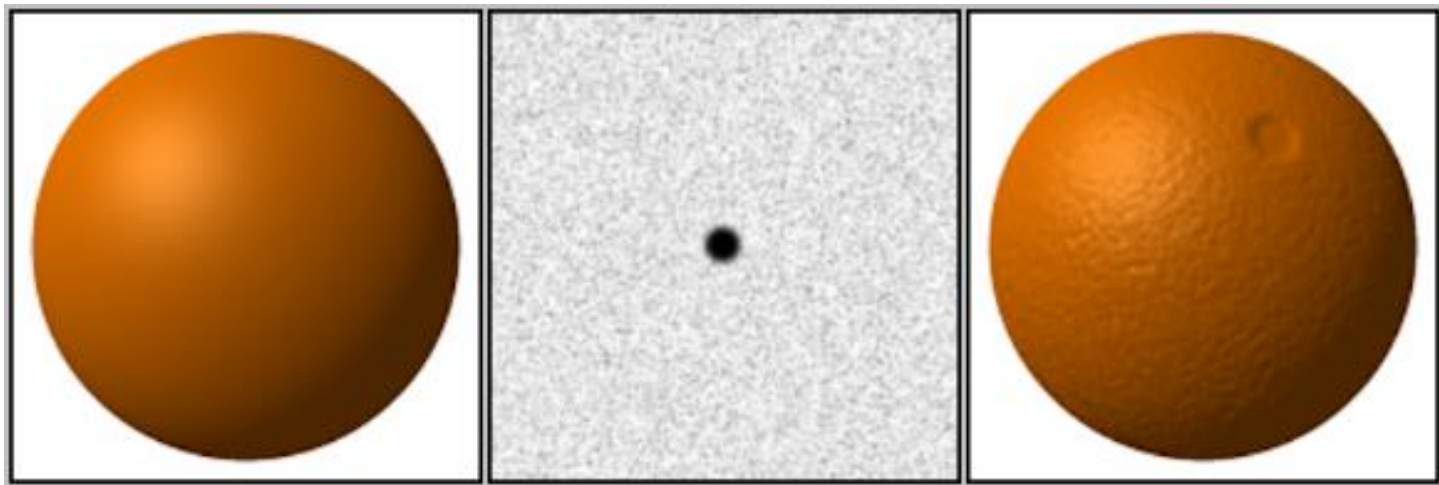


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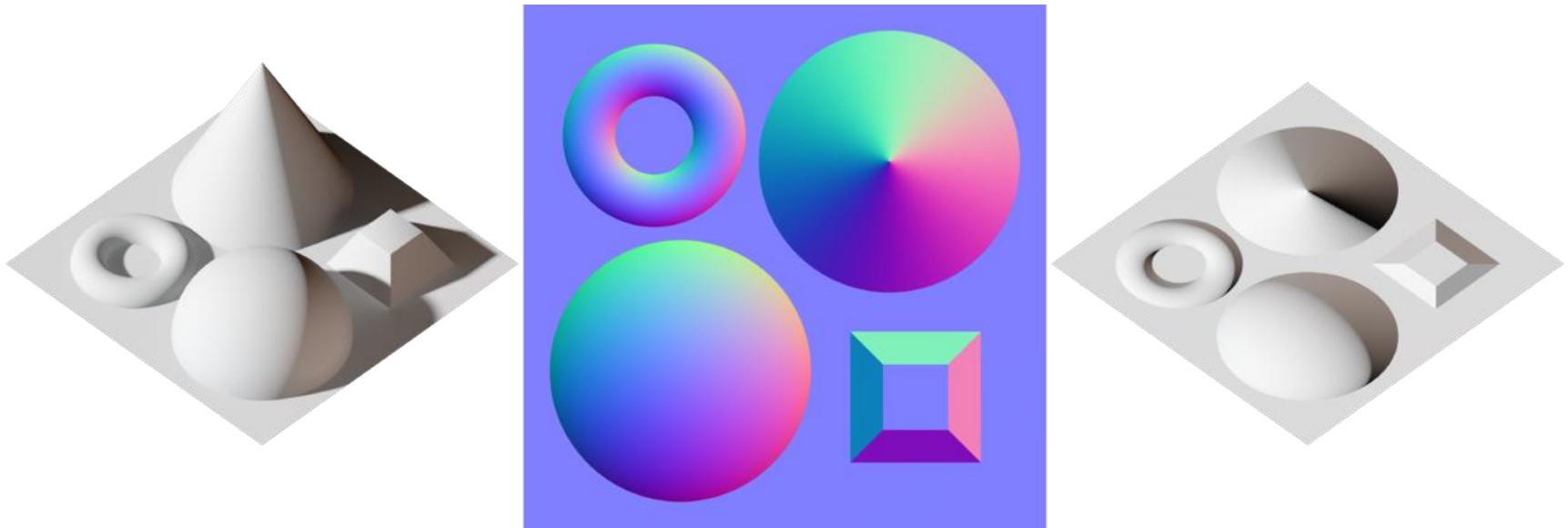
Preliminary timetable

Lecture No.	Date	Topic
1	17.10.2014	Basic Input & Output
2	24.10.2014	Timing & Basic Game Mechanics
3	31.10.2014	Software Rendering 1
4	07.11.2014	Software Rendering 2
5	14.11.2014	Basic Hardware Rendering
6	21.11.2014	Animations
7	28.11.2014	Physically-based Rendering
8	05.12.2014	Physics 1
9	12.12.2014	Physics 2
10	19.12.2014	Scripting
11	16.01.2015	Compression & Streaming
12	23.01.2015	Multiplayer
13	30.01.2015	Audio
14	06.02.2015	Procedural Content Generation
15	13.02.2015	AI

Bump Mapping

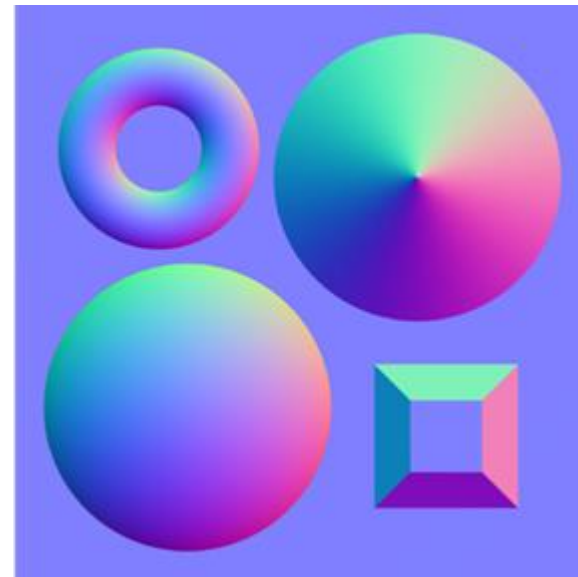


Normal Maps



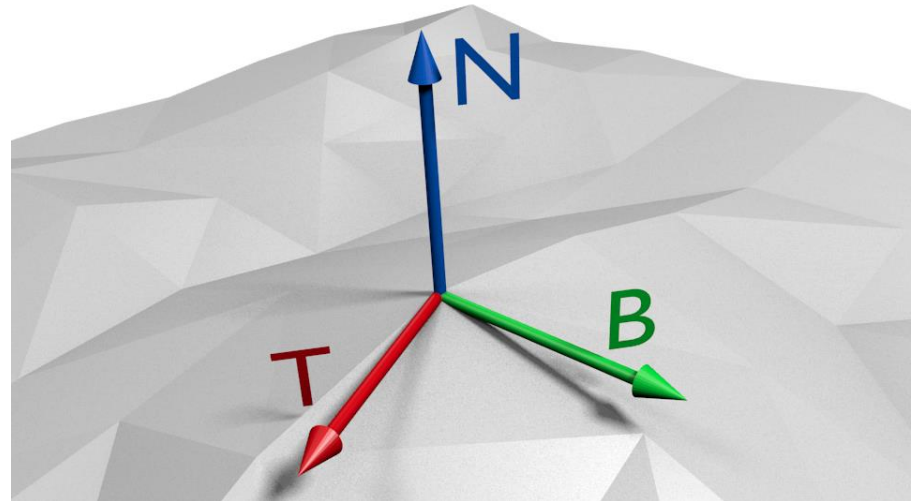
Normal Maps

- $\text{normal} = 2 * \text{color} - 1;$



Tangent Space

- Defines coordinate systems orthogonal to the surface



- Reuse texture coordinates:
 $\text{deltaPos1} = \text{deltaU1} * T + \text{deltaV1} * B$
 $\text{deltaPos2} = \text{deltaU2} * T + \text{deltaV2} * B$

- $$\begin{bmatrix} T & B & N \\ T & B & N \\ T & B & N \end{bmatrix}$$

Normal Mapping



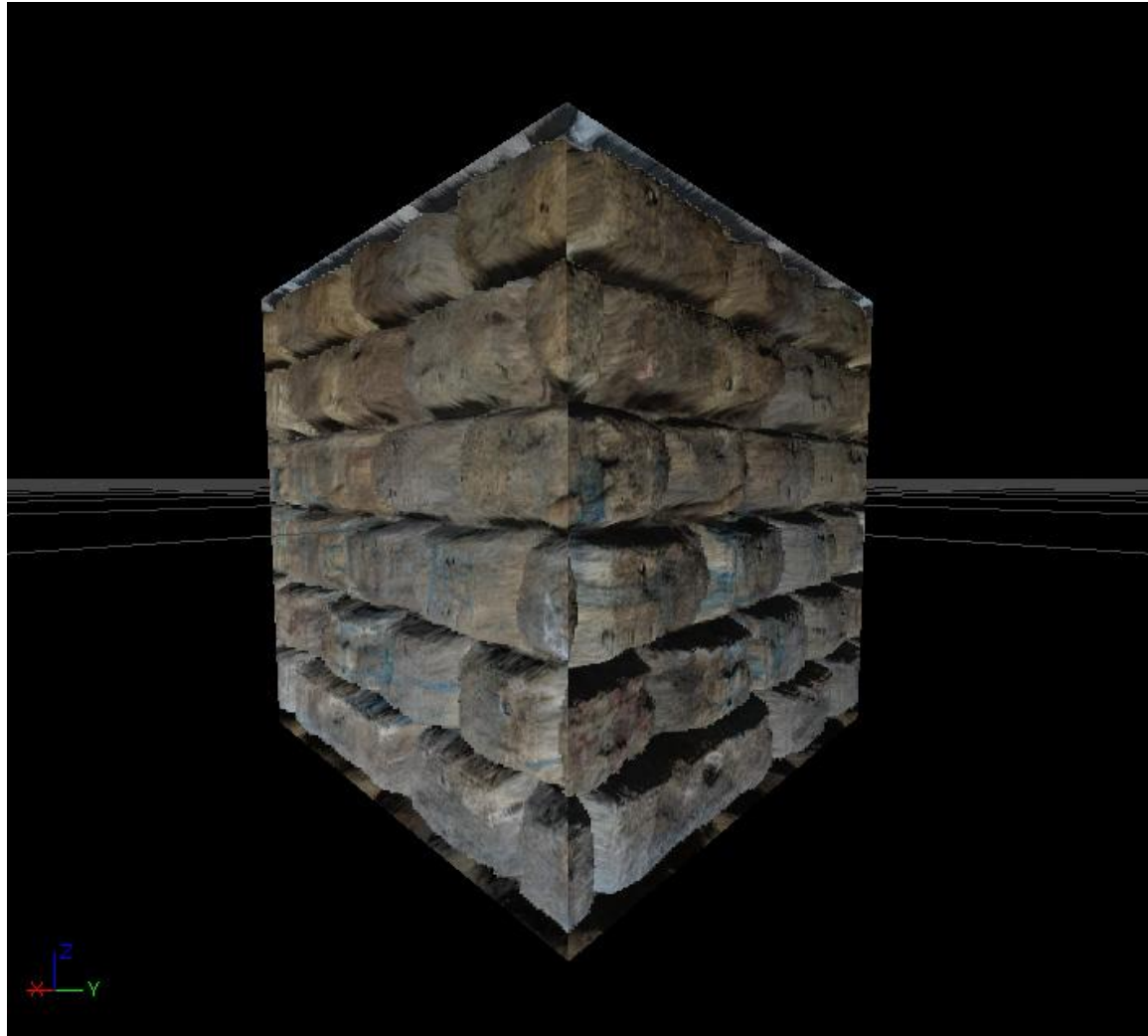
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Parallax Occlusion Mapping



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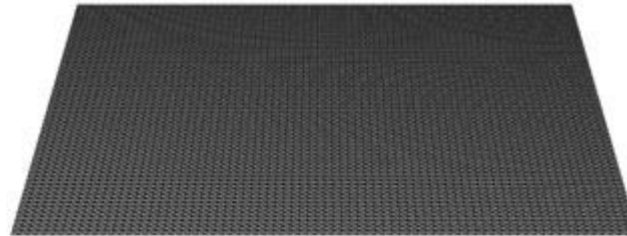




Displacement Mapping



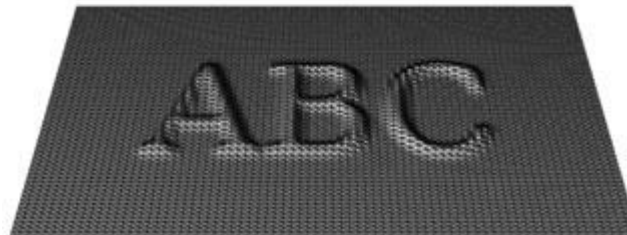
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ORIGINAL MESH



DISPLACEMENT MAP

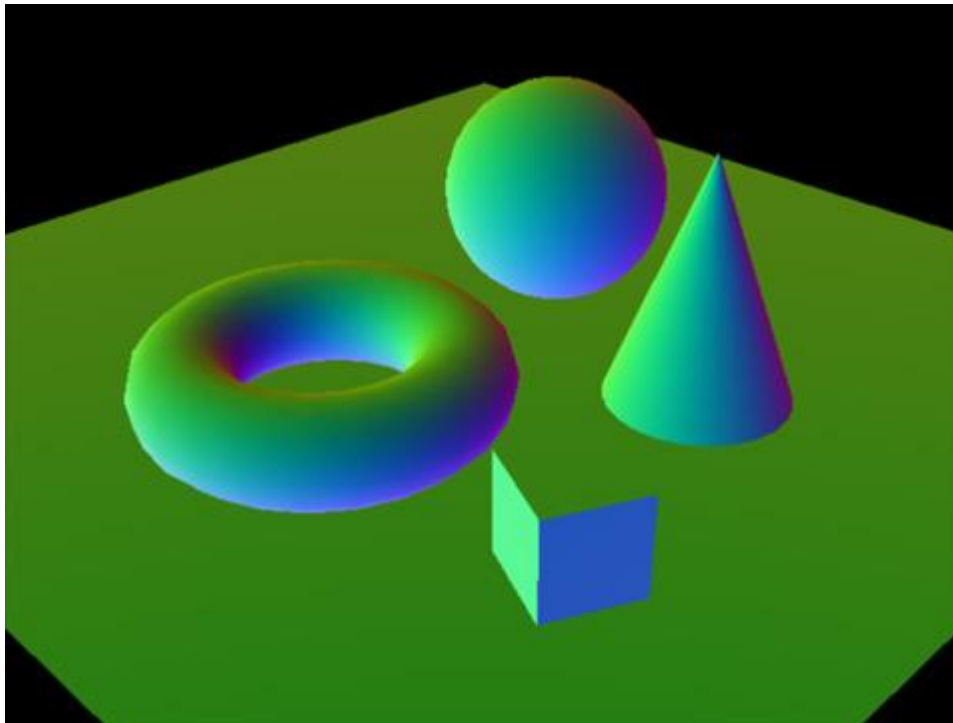


MESH WITH DISPLACEMENT

Deferred Shading



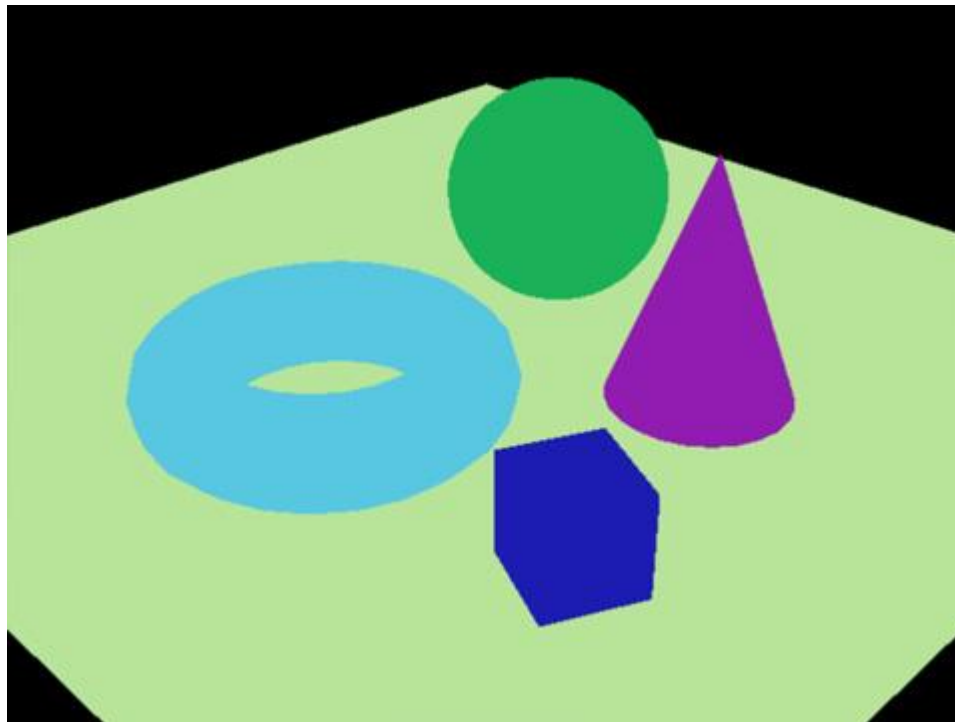
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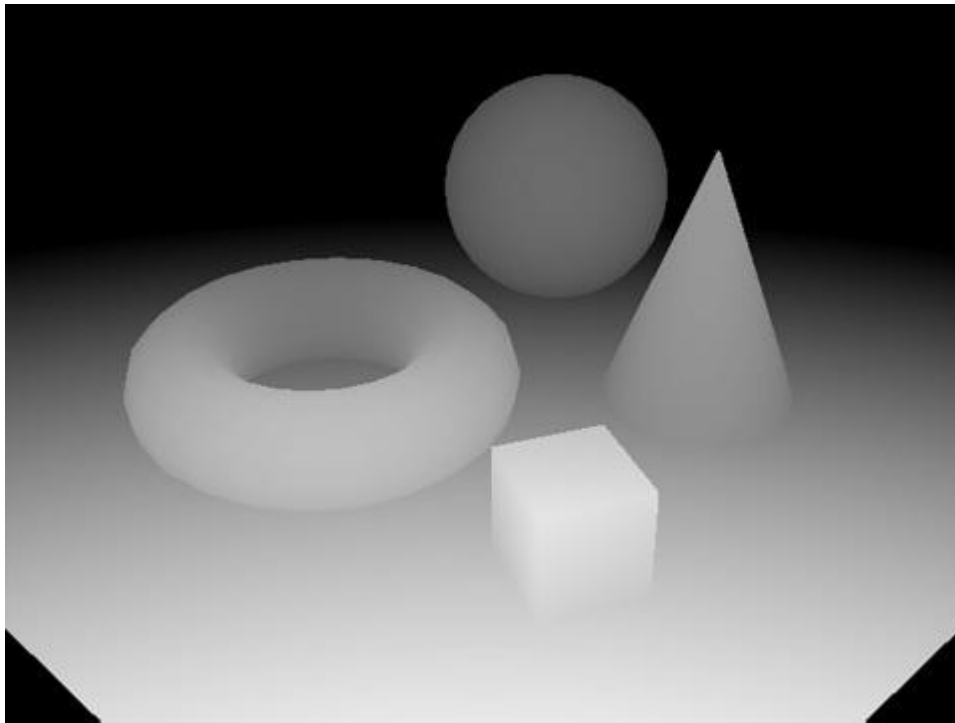
Deferred Shading



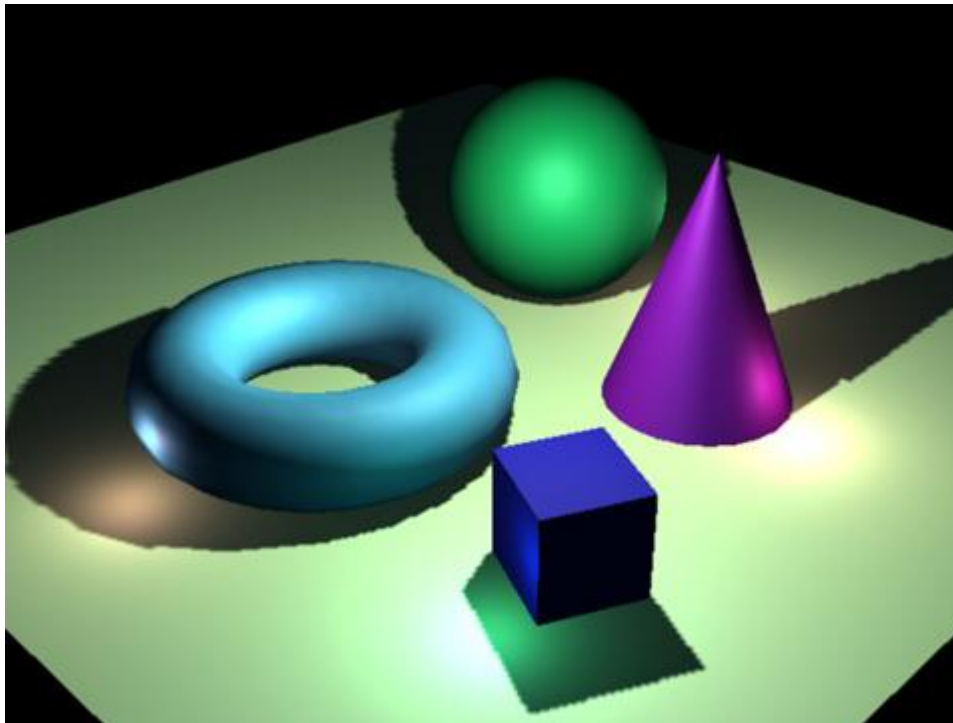
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Deferred Shading



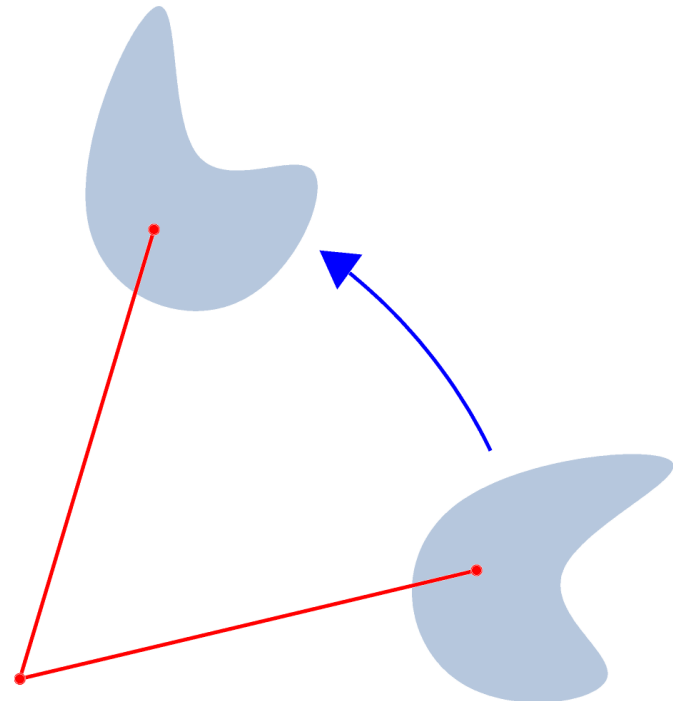
Deferred Shading



- **projection * view * model**
- **Animate the model matrix to animate an object**

Movement and Rotation

- **model = (translate to end position) * rotation
* (translate rotation center to 0)**



Scale



Shear



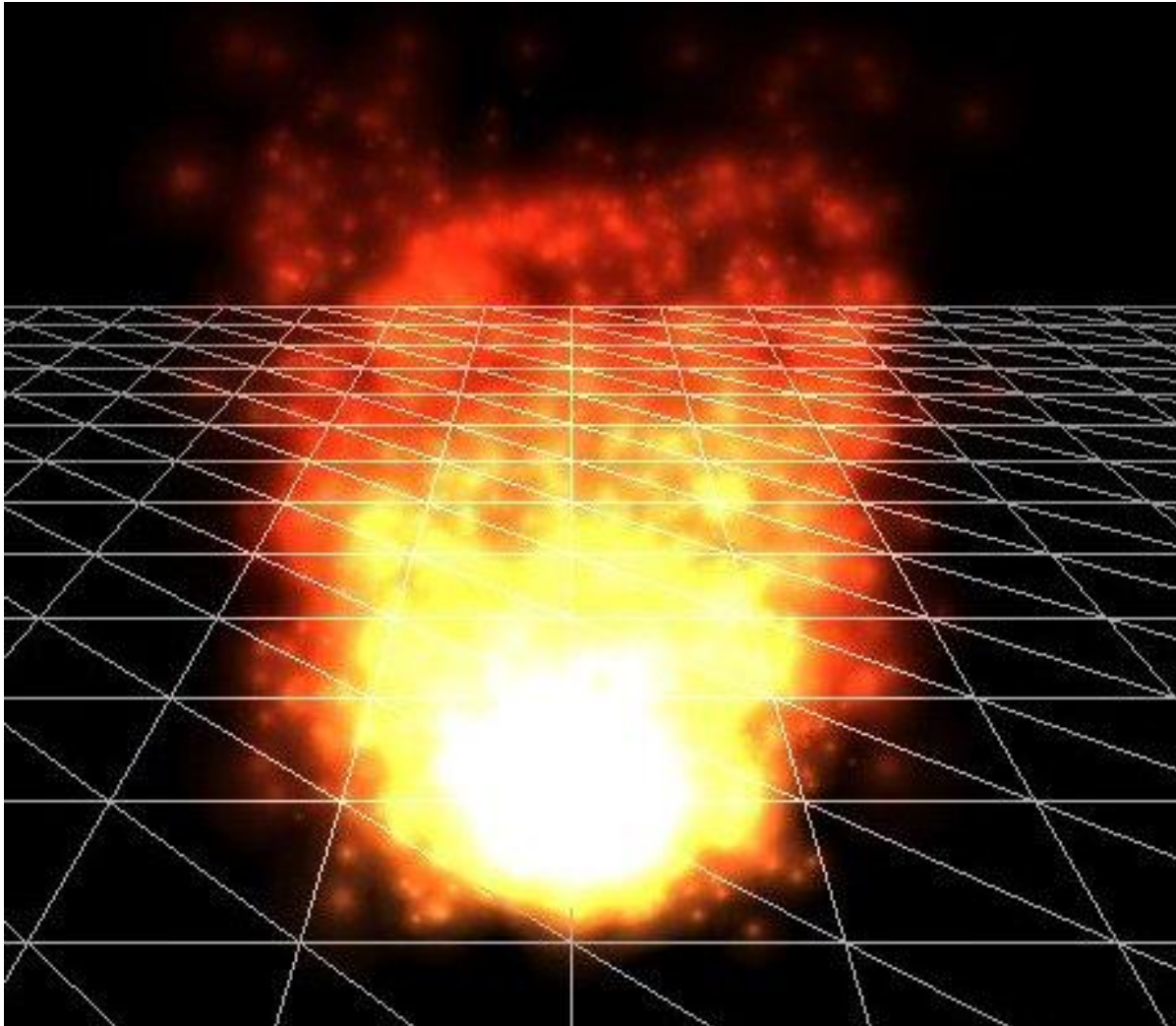
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Particle Systems



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- **A particle is (mostly)**
 - Just an image
 - „Billboards“
 - One texture, two triangles
 - Always rotated to camera
 - Use inverse of view matrix

- **Emitter**
 - Source of particles
- **Animation**
 - Use super simple physics
 - Constant speed or constant speed & gravity
 - Fade out
 - $\alpha = 1 - \text{time} / \text{max_time}$

Particle Systems



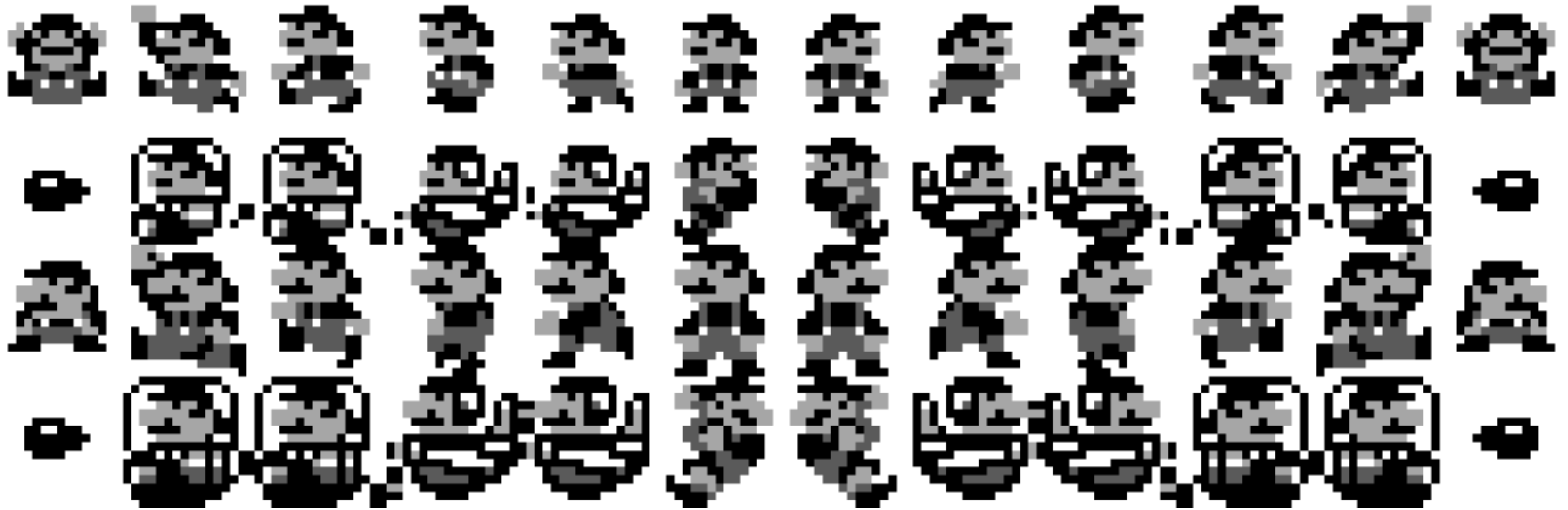
Fluids



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Sprite Sheets



Vertex Animations



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Vertex Animations

- **100 frames * 100000 vertices = lots of data**

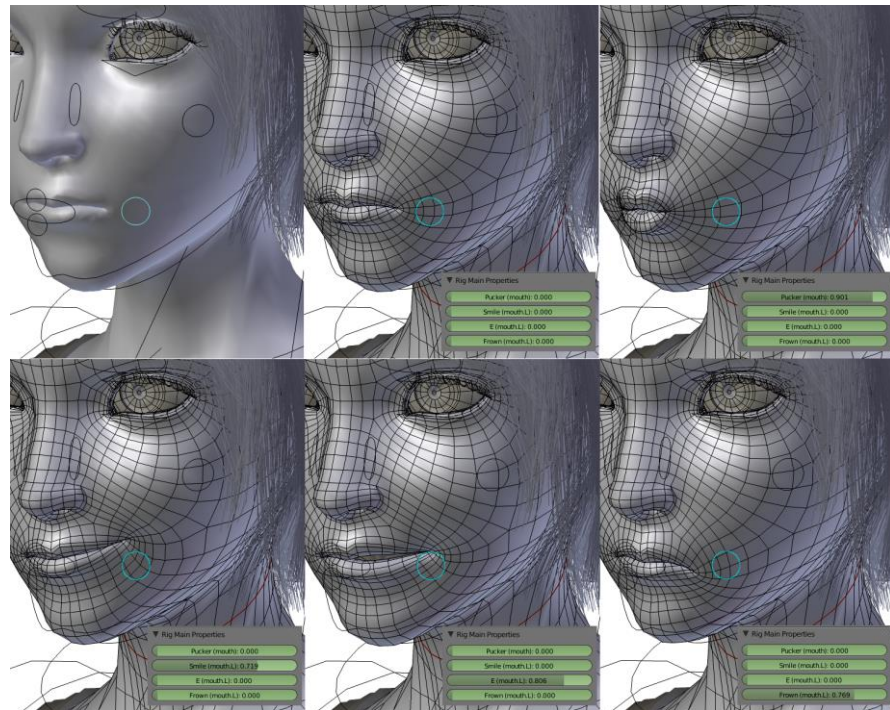
Blend Vertex Positions



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- Morph target animation



Performance Capture



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Skeletal Animation



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Skeletal Animation



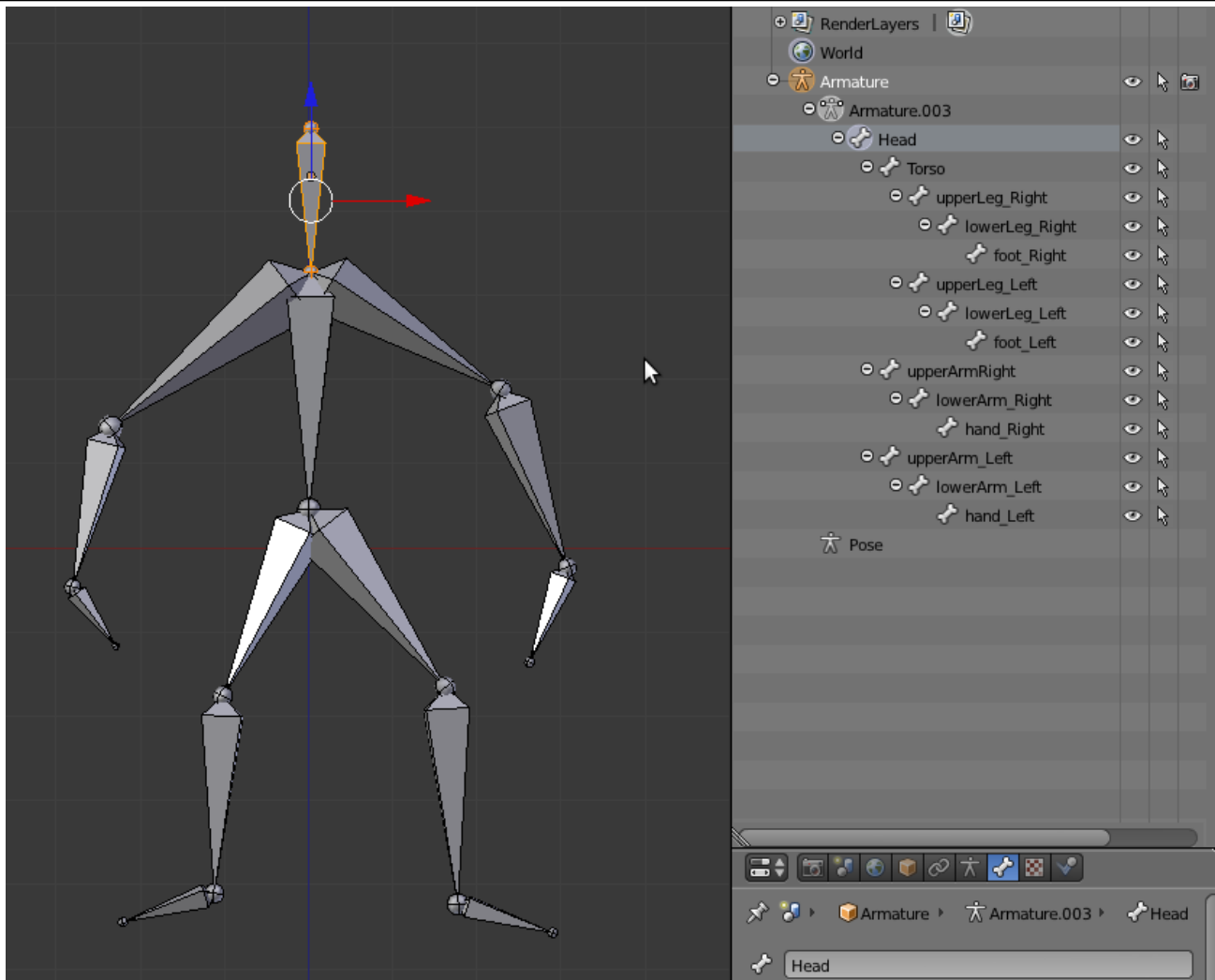
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Skeletal Animation



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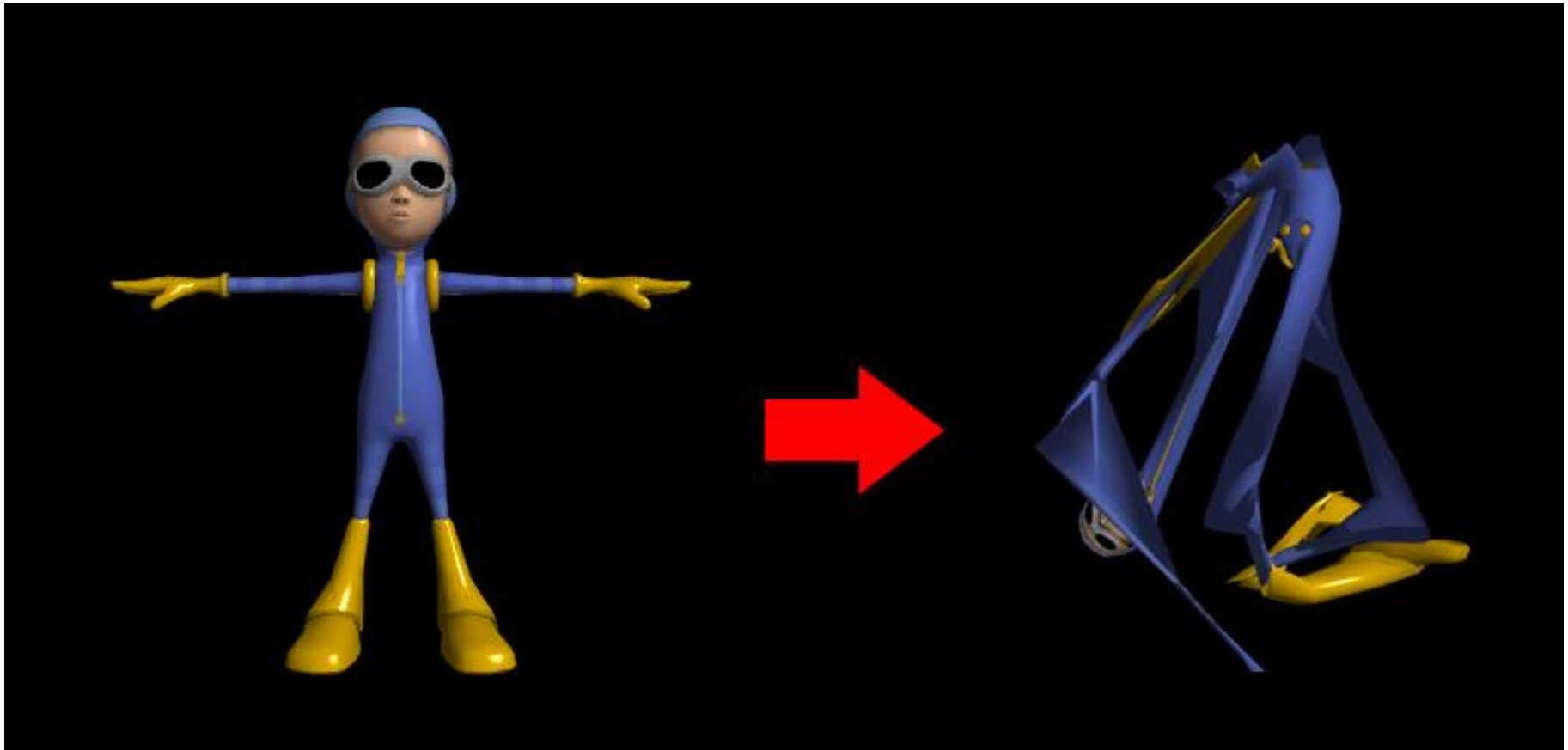


- **One bone – One Transformation matrix**
 - Or just a rotation
 - Depends on your gfx tool
- **Animation**
 - Just an array of small transformation matrix arrays
 - Framerate can be low
 - Interpolation works fine

Skinning



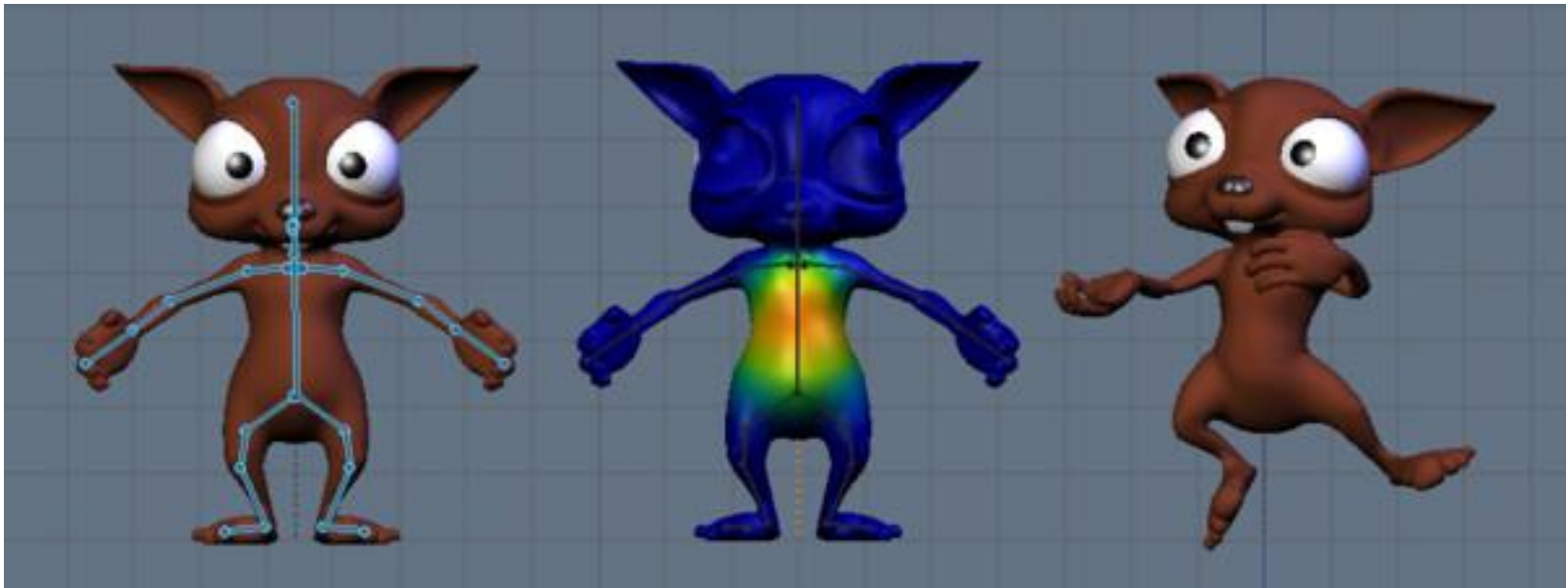
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Skinning



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- **For each vertex**
 - Array of (weight, index)
- **At start**
 - Compute inverse of every bone transform matrix
- **For animation step**
 - Compute new transform matrices
 - For each bone compute new transform * old inverse
- **For each vertex**
 - For each weight
 - Compute (new transform * old inverse * vertex) * weight
 - Sum it up

Motion Capturing



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- **Forward Kinematics**
 - Input: Bone rotations
 - Output: Final positions
- **Inverse Kinematics:**
 - Input: Final positions
 - Output: Bone rotations

Inverse Kinematics

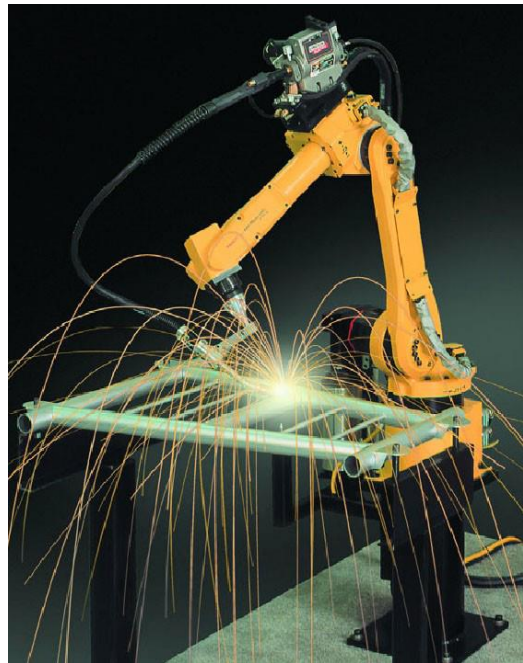


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Inverse Kinematics

- **Numerical, iterative solution using Jacobi Matrix**
 - See Robotics Lecture



Unexpected Deformations

- „Achselhöhle“



- **Spherical Skinning**
 - http://www.crytek.com/download/izfrey_siggraph2011.pdf

Muscles



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Muscles



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Physical Animations



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Hair, Cloth,...



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Rag Dolls



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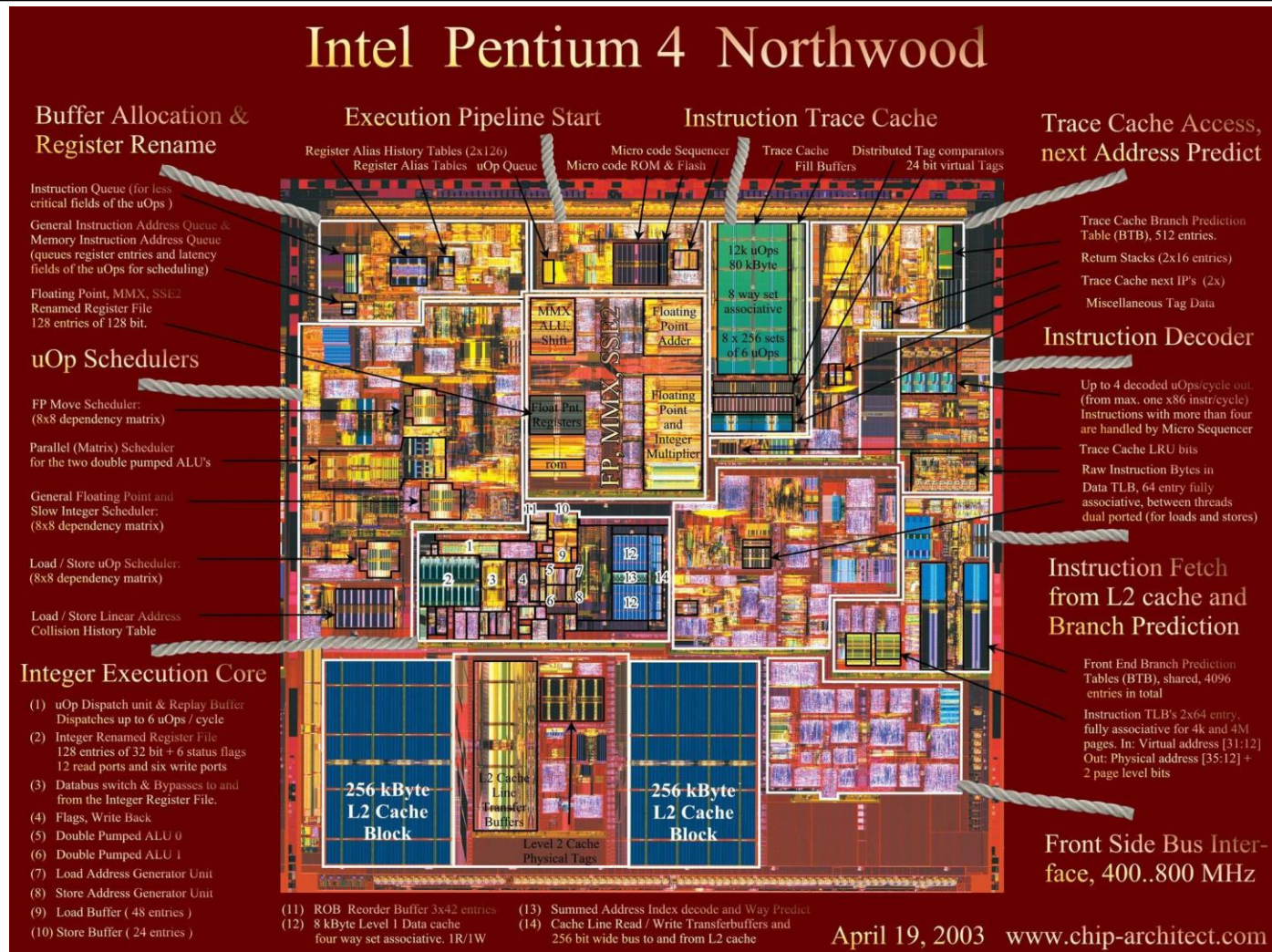
Rag Doll <-> Skeletal Animation

- **Player hit -> rag doll simulation**
- **Wait**
- **Blend from current positions to nearest known animation state**
- **Play animation**

CPU internals

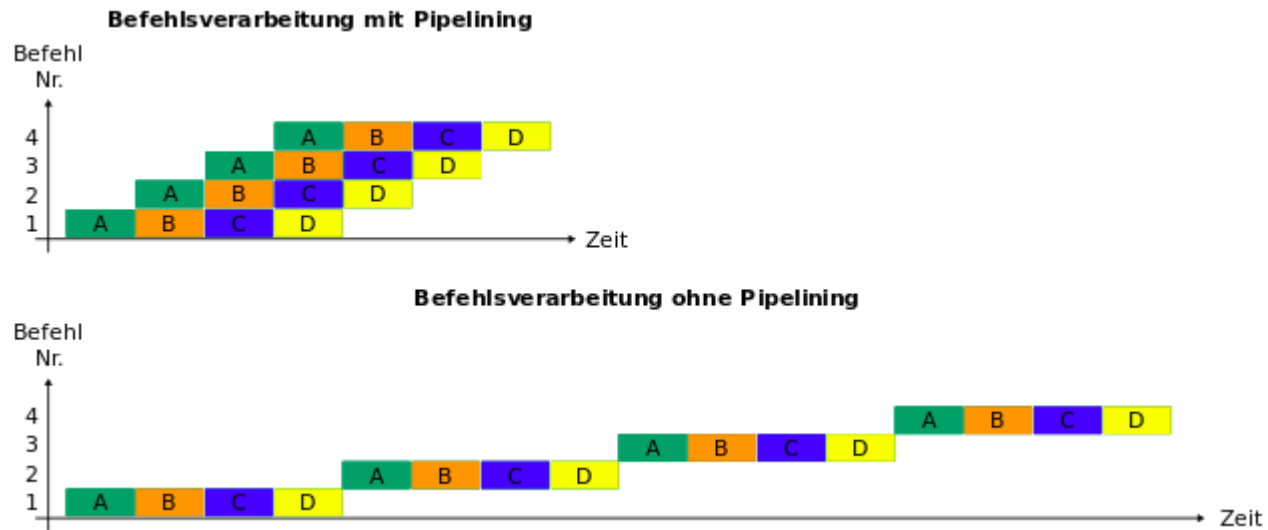


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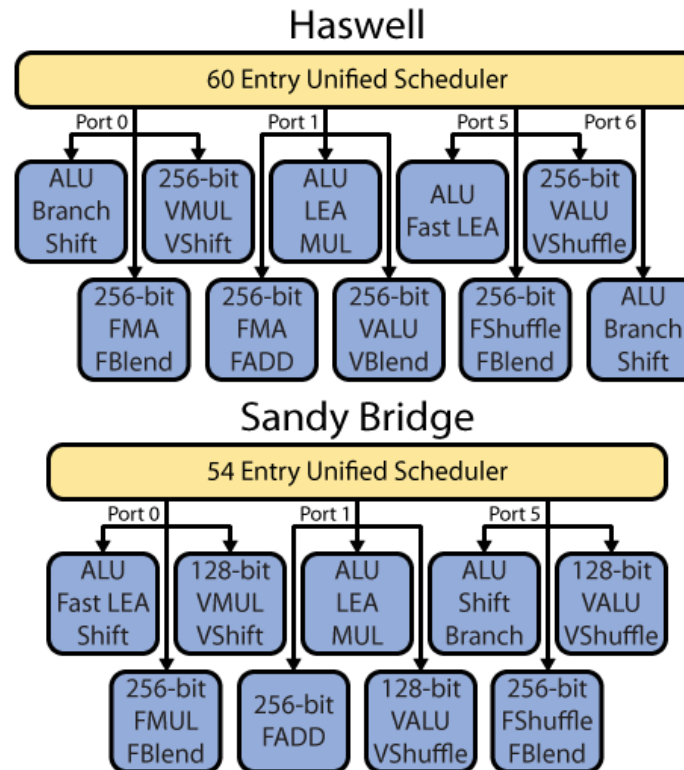
April 19, 2003 www.chip-architect.com

Pipelining



Multiple Execution Units

- „Note that Figure 3 does not show every execution unit, due to space limitations.“
(from <http://www.realworldtech.com/haswell-cpu/4/>)



- **Structural Hazards**
 - Out of hardware
- **Data Hazards**
 - Data dependencies
- **Control Hazards**
 - Dynamic branching

Structural Hazards

- **Modern CPUs add more ALUs**
- **Already at a very high level**

Data Hazards

- Sometimes just register uses,
not real data dependencies
- `subl rax, rbx`
`addl rcx, rax`
- `subl rcx, rax`
`addl rbx, rax`
- -> Register renaming
 - CPU uses more registers internally
than can be directly addressed

- **Compiler can help**
 - Reorder instructions
 - Depends highly on CPU
- **Out-of-Order CPUs**
 - Can reorder instructions themselves
 - Can incorporate current situation in decisions
 - All current x86 CPUs are out-of-order
 - More and more ARM CPUs are out-of-order
 - PS360 are in-order

- **Speculative execution**
 - Branch Prediction more and more sophisticated



```
int main()
{
    // generate data
    const unsigned arraySize = 32768;
    int data[arraySize];

    for (unsigned c = 0; c < arraySize; ++c)
        data[c] = std::rand() % 256;

    // !!! with this, the next loop runs faster
    std::sort(data, data + arraySize);

    // test
    clock_t start = clock();
    long long sum = 0;

    for (unsigned i = 0; i < 100000; ++i)
    {
        // primary loop
        for (unsigned c = 0; c < arraySize; ++c)
        {
            if (data[c] >= 128)
                sum += data[c];
        }
    }

    double elapsedTime = static_cast<double>(clock() - start) / CLOCKS_PER_SEC;

    std::cout << elapsedTime << std::endl;
    std::cout << "sum = " << sum << std::endl;
}
```

- Cache Hierarchy critical for performance
- L1 cache ~ KiloBytes
- L2 cache ~ MegaBytes
- Main memory ~ GigaBytes
- L1 cache ~ 0.5 ns
- L2 cache ~ 7 ns
- Main memory ~ 100 ns

- **Access pattern prediction**
 - Works best when data is reused or for sequential data reads
- **Cache Lines**
 - Memory read in blocks
 - ~ 64 Bytes
 - Proper data alignment can help

- „Plain old data“
- ```
struct Data {
 int a;
 float b;
};
```
- Predictable data structures
- No constructor calls during array allocation
- No additional data for virtual function pointers
- ```
Data data[64];
```
- Linear data of $64 * \text{sizeof}(\text{Data})$ bytes

Memory alignment

- **Add unused data**
- **Use system specific things**
 - `posix_memalign(..)`
- **Use alignas in C++ 11**
- **struct alignas(16) Data {**
 int a;
 float b;
};
- **alignas(128) char cacheline[128];**

Premultiplied Alpha

- **White Pixel, transparent pixel**
 - $\text{source alpha} * \text{new pixel} + (1 - \text{source alpha}) * \text{old pixel}$
 - $\text{new pixel} + (1 - \text{source alpha}) * \text{old pixel}$
- **$(1, 1) (1, 0) \rightarrow (1, 0.5) \rightarrow 0.5$ (source alpha * new pixel)**
- **$(1, 1) (0, 0) \rightarrow (0.5, 0.5) \rightarrow 0.25$ (source alpha * new pixel)**
- **$(1, 1) (0, 0) \rightarrow (0.5, 0.5) \rightarrow 0.5$ (new pixel)**

Premultiplied Alpha

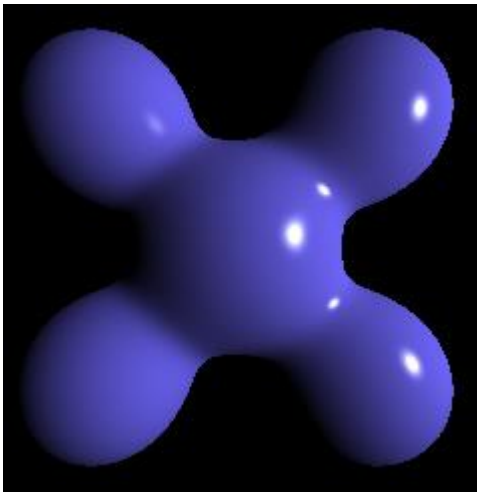
- **Grey Pixel, transparent pixel**
 - $\text{source alpha} * \text{new pixel} + (1 - \text{source alpha}) * \text{old pixel}$
 - $\text{new pixel} + (1 - \text{source alpha}) * \text{old pixel}$
- **$(0.5, 1) (1, 0) \rightarrow (0.75, 0.5) \rightarrow 0.375$ (source alpha * new pixel)**
- **$(0.5, 1) (0, 0) \rightarrow (0.25, 0.5) \rightarrow 0.125$ (source alpha * new pixel)**
- **$(0.5, 1) (0, 0) \rightarrow (0.25, 0.5) \rightarrow 0.25$ (new pixel)**
- **$(0.5, 1) (0.5, 0) \rightarrow (0.5, 0.5) \rightarrow 0.25$ (source alpha * new pixel)**

Blending Order

- **source alpha * new pixel + (1 - source alpha) * old pixel**
 - **$a * n + (1 - a) * o$**
 - **$a2 * n2 + (1 - a2) * (a1 * n1 + (1 - a1) * o)$**
 - **$a2*n2 + a1*n1 + o - a1*o - a1*a2*n1 - a2*o + a1*a2*o$**
-
- **source alpha * new pixel + old pixel**
 - **$a * n + o$**
 - **$a2 * n2 + (a1 * n1 + o)$**
 - **$= a1 * n1 + a2 * n2 + o$**

Phong Lighting

- **Reflects only light sources**
- **Expects light sources to be round**



Roughness

- **Use mip mapped cube map**
- **Choose mip map level based on roughness**
- **Higher mip level -> blurrier -> increased roughness**