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BVRIT HYDERABAD
College of Engineering for Women
Nizampet Road, Bachupally, Hyderabad-500 090
II B.Tech.ISemester (R18) – II- Midterm Examinations, March- 2021
Computer Organization& Microprocessor
Department of IT

Date: 03-03-2021 FN

Duration : 1 hour

Max Marks: 10

Answer any two Questions. Each question carries equal marks.

Q.No.	Question	Course outcomes	Blooms level	Marks
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Unit-III

1	a)	Explain Interrupt Cycle with suitable diagram	C214.3	Apply	2
	b)	Explain about different types of assembler directives and operators	C214.3	Apply	3

Unit-IV

2	a)	Explain Booth's Multiplications algorithm with example	C214.4	Analyze	3
	b)	List the steps in CPU-IoP Communication	C214.4	Apply	2

Unit- IV & V

3	a)	Explain about different modes of data transfer.	C214.4	Analyze	3
	b)	Explain about memory management in cache memory with diagram	C214.5	Apply	2

Unit-V

4	a)	Explain about Associative memory with diagram	C214.5	Analyze	3
	b)	Discuss about instruction pipeline with example	C214.6	Analyze	2