Code No:C214 **Set No. 1**

BVRIT HYDERABAD COLLEGE OF ENGINEERING FOR WOMEN
II B.Tech. I Sem., I Mid-Term Examinations, September-2019
Computer Organization and Architecture
Objective Exam

Name: _____ Hall Ticket No. | | | | | | | | | | |

Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

I. Choose the correct alternative:

1. Cumulative addition of four bits (1 + 1 + 1 + 1) gives [ ]
   a. 1111 b. 110 c. 100 d. 101
2. The 2' s complement representation of the decimal value 15 is [ ]
   a. 1111 b. 11111 c. 1000 d. 0001
3. ---------- is used to set a bit [ ]
   a. AND b. XOR c. OR d. NOT
4. A Control variable which can be represented as string of 0's & 1's is [ ]
   a.control variable b. control memory c. control word d. Both a & c
5. Program counter is used to [ ]
   a. next instruction address to be executed b. last instruction address in program
   c. hold current instruction address d. None
6. RTL Stands for [ ]
   a. Rotate transfer language b. Register transfer language
   c. Random transfer language d. Read transfer language
7. Decoding instruction involves ---- and ---- [ ]
   a. what operands b. how operands are specified
   c. what is the operation d. All of the above
8. LDA refers to [ ]
   a.load AC contents to memory b. load memory contents to AC
   c. Both a & b d. none of the above
9. OUTR is used for [ ]
   a.printing output b. getting output c.loading data d. All
10. Mode bit is ____ for Direct address and ____ for indirect address [ ]
    a.0,0 b. 1,0 c. 0,1 d.1,1

Cont…..2

II. Fill in the Blanks:

11. Return address of subroutine call is stored in _____
12. Characteristic equation of XOR is _____
13. Computer design is concerned with _____
14. $(101101)_2$ = (_____)$_{10}$
15. Next address generator is also called as _____
16. Microoperation refers to _____
17. Stack organization uses _____ address instruction format
18. Arithmetic shift right _____ the number by 2 power no. of shifts
19. External interrupts are generated by _____
20. $(124)_{10}$ = (_____)$_8$

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Code No:C214 **Set No. 2**

BVRIT HYDERABAD COLLEGE OF ENGINEERING FOR WOMEN
II B.Tech. I Sem., I Mid-Term Examinations, September-2019
Computer Organization and Architecture
Objective Exam

Name: _____ Hall Ticket No. | | | | | | | | | | |

Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

I. Choose the correct alternative:

1. ---------- is used to set a bit [ ]
   a. AND b. XOR c. OR d. NOT
2. A Control variable which can be represented as string of 0's & 1's is [ ]
   a. a.control variable b. control memory c. control word d. Both a & c
3. Program counter is used to [ ]
   a. a. hold current instruction address b. last instruction address in program
   b. next instruction address to be executed d. None
4. RTL Stands for [ ]
   a. a. Rotate transfer language b. Register transfer language
   b. Random transfer language d. Read transfer language
5. Decoding instruction involves ---- and ---- [ ]
   a. a. what operands b. how operands are specified
   b. c. what is the operation d. All of the above
6. LDA refers to [ ]
   a. a.load AC contents to memory b. load memory contents to AC
   b. c. Both a & b d. none of the above
7. OUTR is used for [ ]
   a. a.printing output b. getting output c.loading data d. All
8. Mode bit is ____ for Direct address and ____ for indirect address [ ]
   a. a.0,0 b. 1,0 c. 0,0 d.1,1
9. Cumulative addition of four bits (1 + 1 + 1 + 1) gives [ ]
   a.1111 b. 110 c. 100 d. 101
10. The 2' s complement representation of the decimal value 15 is [ ]
    a.1111 b. 11111 c. 1000 d. 0001

Cont…..2

II Fill in the Blanks:

11. Computer design is concerned with _____
12. $(101101)_2$ = (_____)$_{10}$
13. Next address generator is also called as _____
14. Microoperation refers to _____
15. Stack organization uses _____ address instruction format
16. Arithmetic shift right _____ the number by 2 power no. of shifts
17. External interrupts are generated by _____
18. $(124)_{10}$ = (_____)$_8$
19. Return address of subroutine call is stored in _____
20. Characteristic equation of XOR is _____

Code No:C214 **Set No. 3**

BVRIT HYDERABAD COLLEGE OF ENGINEERING FOR WOMEN
II B.Tech. I Sem., I Mid-Term Examinations, September-2019
Computer Organization and Architecture
Objective Exam

Name: _____ **Hall Ticket No.** | | | | | | | | | | |

Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

I. Choose the correct alternative:

1. Program counter is used to [ ]
   a. a. hold current instruction address b. last instruction address in program
   b. next instruction address to be executed d. None
2. RTL Stands for [ ]
   a. a. Rotate transfer language b. Register transfer language
   b. Random transfer language d. Read transfer language
3. Decoding instruction involves ---- and ---- [ ]
   a. a. what operands b. how operands are specified
   b. c. what is the operation d. All of the above
4. LDA refers to [ ]
   a. a.load AC contents to memory b. load memory contents to AC
   b. c. Both a & b d. none of the above
5. OUTR is used for [ ]
   a. a.printing output b. getting output c.loading data d. All
6. Mode bit is ____ for Direct address and ____ for indirect address [ ]
   a. a.0,0 b. 1,0 c. 0,0 d.1,1
7. Cumulative addition of four bits (1 + 1 + 1 + 1) gives [ ]
   a.1111 b. 110 c. 100 d. 101
8. The 2' s complement representation of the decimal value 15 is [ ]
   a.1111 b. 11111 c. 1000 d. 0001
9. ---------- is used to set a bit [ ]
   a. AND b. XOR c. OR d. NOT
10. A Control variable which can be represented as string of 0's & 1's is [ ]
    a. a.control variable b. control memory c. control word d. Both a & c

Cont…..2

II. Fill in the Blanks:

11. Next address generator is also called as _____
12. Microoperation refers to _____
13. Stack organization uses _____ address instruction format
14. Arithmetic shift right _____ the number by 2 power no. of shifts
15. External interrupts are generated by _____
16. $(124)_{10}$ = (_____)$_8$
17. Return address of subroutine call is stored in _____
18. Characteristic equation of XOR is _____
19. Computer design is concerned with _____
20. $(101101)_2$ = (_____)$_{10}$

Code No:C214 **Set No. 4**

BVRIT HYDERABAD COLLEGE OF ENGINEERING FOR WOMEN
II B.Tech. I Sem., I Mid-Term Examinations, September-2019
Computer Organization and Architecture
Objective Exam

Name: _____ Hall Ticket No. | | | | | | | | | | |

Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

I. Choose the correct alternative:

1. Decoding instruction involves ---- and ---- [ ]
   a. a. what operands b. how operands are specified
   b. c. what is the operation d. All of the above
2. LDA refers to [ ]
   a. a.load AC contents to memory b. load memory contents to AC
   b. c. Both a & b d. none of the above
3. OUTR is used for [ ]
   a. a.printing output b. getting output c.loading data d. All
4. Mode bit is ____ for Direct address and ____ for indirect address [ ]
   a. a.0,0 b. 1,0 c. 0,0 d.1,1
5. Cumulative addition of four bits (1 + 1 + 1 + 1) gives [ ]
   a. 1111 b. 110 c. 100 d. 101
6. The 2' s complement representation of the decimal value 15 is [ ]
   a. 1111 b. 11111 c. 1000 d. 0001
7. ---------- is used to set a bit [ ]
   a. AND b. XOR c. OR d. NOT
8. A Control variable which can be represented as string of 0's & 1's is [ ]
   a. a.control variable b. control memory c. control word d. Both a & c
9. Program counter is used to [ ]
   a. a. hold current instruction address b. last instruction address in program
   b. next instruction address to be executed d. None
10. RTL Stands for [ ]
    a. a. Rotate transfer language b. Register transfer language
    b. Random transfer language d. Read transfer language

Cont…..2

II Fill in the Blanks:

11.Stack organization uses _____ address instruction format
12.Arithmetic shift right _____ the number by 2 power no. of shifts
13.External interrupts are generated by _____
14.$(124)_{10}$ = (_____)$_8$
15.Return address of subroutine call is stored in _____
16.Characteristic equation of XOR is _____
17.Computer design is concerned with _____
18. $(101101)_2$ = (_____)$_{10}$
19.Next address generator is also called as _____
20.Microoperation refers to _____