Code No: IT304PC

Set No. 1

Obj	ective	Exam
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ame: Hall Ticket No.	
Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks:	10.
<ul> <li>1. Hardware of the computer usually divided into</li> <li>a)CPU b) Memory</li> <li>c) Input and output devices d) All of the above</li> </ul>	[ ]
<ul><li>2. Computers that have a single-processor register usually called</li><li>a) AC</li><li>b) IR</li><li>c)PC</li><li>d) TR</li></ul>	[ ]
<ul> <li>3. Program counter (PC) is used to</li> <li>a) Stores current instruction</li> <li>b) Stores address of the next instruction</li> <li>c) Used to store temporary data</li> <li>d) None</li> </ul>	[ ] ruction
<ul> <li>4. How the control unit determines the io instructions</li> <li>a) When opcode bits are 0000 to 0110</li> <li>b) When opcode bits are 012</li> <li>c) When opcode bits are 1111</li> <li>d) all of the above</li> </ul>	[ ] 11
<ul> <li>5. Which of the following is correct</li> <li>a) If the address of the operand is specified in the address part of the instruction it is called indirect address</li> <li>b) If the address of the operand is implied in the instruction then it is called physical address</li> <li>c) Address of the operand in a computation type-instruction is called effect address</li> <li>d) None</li> </ul>	ed
6. The size of the instruction queue in 8086 is bytes a) 2 b) 6 c) 8 d) 12	[ ]
7. BHE & A0 pin signals are used for  a) Queue status b) Minimum mode and Maximum mode statu c) Interrupt status d) Memory bank access	[ ] us
8. How many latches are used in 8086 minimum and maximum mode a) 3 b) 2 c) 6 d) None of the above	[ ]
10. ENDP stands for	[ ] Vone [ ]
a) End of the program b) End of the Procedure c) End of the process d) All of the above	count2

Code No: IT304PC :2: Set No. 1

II Fill in the Blanks:	
11. If the data and programs are stored in same memory the called	en its architecture is
12. The control unit controls other units by generating	_
13. Control memory in micro-programmed control unit is	
14. When an instruction is executing,flip flop is che	cked for interrupt
15. The size of the flag register in 8086 processor is bits	
16. The default offset register for code segment register is	
17. The size of register instruction format is	
18. Data from the multiplexed address bus is serpareted by	
19. MOV instruction belongs to thecategory of 8086 ins	structions.
20Pin is used to select maximum & minimum mode	of operation in 8086

microprocessor

Objective Exam	
Name: Hall Ticket No.	
Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 2	10.
<ul> <li>1. How the control unit determines the io instructions</li> <li>a) When opcode bits are 0000 to 0110</li> <li>b) When opcode bits are 011</li> <li>c) When opcode bits are 1111</li> <li>d) all of the above</li> </ul>	[ ] .1
<ul> <li>2. Which of the following is correct</li> <li>a) If the address of the operand is specified in the address part of the instruction it is called indirect address</li> <li>b) If the address of the operand is implied in the instruction then it is called physical address</li> <li>c) Address of the operand in a computation type-instruction is called effect address</li> <li>d) None</li> </ul>	d
3. The size of the instruction queue in 8086 is bytes a) 2 b) 6 c) 8 d) 12	]
4. BHE & A0 pin signals are used for [ a) Queue status b) Minimum mode and Maximum mode status c) Interrupt status d) Memory bank access	] s
5. How many latches are used in 8086 minimum and maximum mode  a) 3 b) 2 c) 6 d) None of the above	]
6. MOV BL, CL performs  a) 8 bit operation  b) 16 bit operation  c) Both  d) N  7. ENDP stands for  a) End of the program  b) End of the Procedure  c) End of the process  d) All of the above	[ ] one ]
8. Hardware of the computer usually divided into a)CPU b) Memory c) Input and output devices d) All of the above	[ ]
<ul><li>9. Computers that have a single-processor register usually called</li><li>a) AC</li><li>b) IR</li><li>c)PC</li><li>d) TR</li></ul>	[ ]
10. Program counter (PC) is used to  a) Stores current instruction b) Stores address of the next instruction c) Used to store temporary data d) None	[ ] ruction

Code No: IT304PC :2: Set No. 2

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	<b>F:11</b>	:	the	Ю		
-	FIII	ın	INE	ы	ıan	KS:

11.	When an instruction is executing,flip flop is checked for interrupt
12.	The size of the flag register in 8086 processor is bits
13.	The default offset register for code segment register is
14.	The size of register instruction format is
15.	Data from the multiplexed address bus is serpareted by
16.	MOV instruction belongs to thecategory of 8086 instructions.
	Pin is used to select maximum & minimum mode of operation in 8086 microprocessor
18.	If the data and programs are stored in same memory then its architecture is called
19.	The control unit controls other units by generating
20.	Control memory in micro-programmed control unit is

Code No: IT304PC

Set No. 3

	<b>Objective Exa</b>	ı <b>m</b>		
Name:	Hall	Ticket No.		
Answer All Questions. All Quest				larks: 10.
	, ,			
1. The size of the instruction	gueue in 8086 is	hytos		[ ]
	c) 8 d) 1			[ ]
2. BHE & A0 pin signals are				[ ]
a) Queue status		node and Max	kimum mode	status
c) Interrupt status	d) Memory b	ank access		
3. How many latches are use	ed in 8086 minim	um and max	imum mode	[ ]
a) 3 b) 2	c) 6	d) None of th	e above	
4. MOV BL, CL performs				[ ]
a) 8 bit operation	b) 16 bit (	operation	c) Both	d) None
<ol><li>ENDP stands for</li></ol>				[ ]
a) End of the program			re	
c) End of the process	· ·			
6. Hardware of the computer	•			[ ]
<ul><li>a)CPU</li><li>c) Input and output device</li></ul>		) Memory ) All of the al	nove	
c) input and output device	25 u	) An or the at	JOVC	
7. Computers that have a sing	gle-processor reg	ister usually	called	[ ]
a) AC b) IR	c)PC	d) TR		
8. Program counter (PC) is us	and to			г 1
a) Stores current instruction		) Stores addre	ess of the nex	t instruction
c) Used to store temporar		) None	Jos of the nex	it instruction
,	•	,		
<ol><li>How the control unit deter</li></ol>	mines the io inst	ructions		[ ]
a) When opcode bits are	0000 to 0110	b) When c	pcode bits a	re 0111
c) When opcode bits are	1111	d) all of th	ie above	
10. Which of the following is		1	, C.1	[ . ]
a) If the address of the op then it is called indirect	-	in the addres	ss part of the	instruction
b) If the address of the or		in the instruc	ction then it is	s called
physical address	<b>1</b>			
c) Address of the operand	in a computation	ı type-instruc	tion is called	effective
address				
d) None				

### II Fill in the Blanks:

11. The default offset register for code segment register is
12. The size of register instruction format is
13. Data from the multiplexed address bus is serpareted by
14. MOV instruction belongs to thecategory of 8086 instructions.
15Pin is used to select maximum & minimum mode of operation in 8086 microprocessor
16. If the data and programs are stored in same memory then its architecture is called
17. The control unit controls other units by generating
18. Control memory in micro-programmed control unit is
19. When an instruction is executing,flip flop is checked for interrupt
20. The size of the flag register in 8086 processor is bits

Code No: IT304PC

Set No. 4

count...2

Objective Exam	ı
Name: Hall T	icket No.
Answer All Questions. All Questions Carry Equal Ma	·
1. How many latches are used in 8086 minimum a) 3 b) 2 c) 6 d)	m and maximum mode [ ]  None of the above
<ul> <li>2. MOV BL, CL performs</li> <li>a) 8 bit operation</li> <li>b) 16 bit operation</li> <li>3. ENDP stands for</li> </ul>	[ ] eration c) Both d) None [ ]
a) End of the program b) End of th c) End of the process d) All of th	
	Memory All of the above
5. Computers that have a single-processor registral AC b) IR c)PC	er usually called [ ] d) TR
· ·	[ ] Stores address of the next instruction None
7. How the control unit determines the io instruction a) When opcode bits are 0000 to 0110 c) When opcode bits are 1111	b) When opcode bits are 0111
<ul> <li>8. Which of the following is correct</li> <li>a) If the address of the operand is specified in then it is called indirect address</li> <li>b) If the address of the operand is implied in physical address</li> <li>c) Address of the operand in a computation ty address</li> </ul>	the instruction then it is called
d) None $$ 9. The size of the instruction queue in 8086 is $\_$	hytes [ ]
a) 2 b) 6 c) 8 d) 12	bytes [ ]
<ul> <li>10. BHE &amp; A0 pin signals are used for</li> <li>a) Queue status</li> <li>b) Minimum mod</li> <li>c) Interrupt status</li> <li>d) Memory bank</li> </ul>	[ ] de and Maximum mode status k access

Code No: IT304PC :2: Set No. 4

#### II Fill in the Blanks:

11. Control memory in micro-programmed control unit is
12. When an instruction is executing,flip flop is checked for interrupt
13. The size of the flag register in 8086 processor is bits
14. The default offset register for code segment register is
15. The size of register instruction format is
16. Data from the multiplexed address bus is serpareted by
17. MOV instruction belongs to thecategory of 8086 instructions.
18Pin is used to select maximum & minimum mode of operation in 8086 microprocessor
19. If the data and programs are stored in same memory then its architecture is called
20. The control unit controls other units by generating