

Set No. 1

Objective Exam

Hall Ticket No.[illegible]

Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

- count...2**

II Fill in the Blanks:

11. If the data and programs are stored in same memory then its architecture is called_____
12. The control unit controls other units by generating_____
13. Control memory in micro-programmed control unit is _____
14. When an instruction is executing, _____flip flop is checked for interrupt
15. The size of the flag register in 8086 processor is _____ bits
16. The default offset register for code segment register is_____
17. The size of register instruction format is _____
18. Data from the multiplexed address bus is serpareted by _____
19. MOV instruction belongs to the _____category of 8086 instructions.
20. _____Pin is used to select maximum & minimum mode of operation in 8086 microprocessor

Code No: IT304PC

Set No. 2

BVRIT HYDERABAD COLLEGE OF ENGINEERING FOR WOMEN

II B.Tech. I Sem., I Mid-Term Examinations, December-2020

Computer Organization and Microprocessor

Objective Exam

Name: _____ **Hall Ticket No.**

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Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

1. How the control unit determines the io instructions []
a) When opcode bits are 0000 to 0110 b) When opcode bits are 0111
c) When opcode bits are 1111 d) all of the above
2. Which of the following is correct []
a) If the address of the operand is specified in the address part of the instruction then it is called indirect address
b) If the address of the operand is implied in the instruction then it is called physical address
c) Address of the operand in a computation type-instruction is called effective address
d) None
3. The size of the instruction queue in 8086 is _____ bytes []
a) 2 b) 6 c) 8 d) 12
4. BHE & A0 pin signals are used for _____ []
a) Queue status b) Minimum mode and Maximum mode status
c) Interrupt status d) Memory bank access
5. How many latches are used in 8086 minimum and maximum mode []
a) 3 b) 2 c) 6 d) None of the above
6. MOV BL, CL performs []
a) 8 bit operation b) 16 bit operation c) Both d) None
7. ENDP stands for _____ []
a) End of the program b) End of the Procedure
c) End of the process d) All of the above
8. Hardware of the computer usually divided into _____ []
a) CPU b) Memory
c) Input and output devices d) All of the above
9. Computers that have a single-processor register usually called _____ []
a) AC b) IR c) PC d) TR
10. Program counter (PC) is used to _____ []
a) Stores current instruction b) Stores address of the next instruction
c) Used to store temporary data d) None

count...2

II Fill in the Blanks:

11. When an instruction is executing, _____ flip flop is checked for interrupt
12. The size of the flag register in 8086 processor is _____ bits
13. The default offset register for code segment register is _____
14. The size of register instruction format is _____
15. Data from the multiplexed address bus is serpareted by _____
16. MOV instruction belongs to the _____ category of 8086 instructions.
17. _____ Pin is used to select maximum & minimum mode of operation in 8086 microprocessor
18. If the data and programs are stored in same memory then its architecture is called _____
19. The control unit controls other units by generating _____
20. Control memory in micro-programmed control unit is _____

Code No: IT304PC

Set No. 3

BVRIT HYDERABAD COLLEGE OF ENGINEERING FOR WOMEN

II B.Tech. I Sem., I Mid-Term Examinations, December-2020

Computer Organization and Microprocessor

Objective Exam

Name: _____ **Hall Ticket No.**

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Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

1. The size of the instruction queue in 8086 is _____ bytes []
a) 2 b) 6 c) 8 d) 12
2. BHE & A0 pin signals are used for _____ []
a) Queue status b) Minimum mode and Maximum mode status
c) Interrupt status d) Memory bank access
3. How many latches are used in 8086 minimum and maximum mode []
a) 3 b) 2 c) 6 d) None of the above
4. MOV BL, CL performs []
a) 8 bit operation b) 16 bit operation c) Both d) None
5. ENDP stands for _____ []
a) End of the program b) End of the Procedure
c) End of the process d) All of the above
6. Hardware of the computer usually divided into _____ []
a) CPU b) Memory
c) Input and output devices d) All of the above
7. Computers that have a single-processor register usually called _____ []
a) AC b) IR c) PC d) TR
8. Program counter (PC) is used to _____ []
a) Stores current instruction b) Stores address of the next instruction
c) Used to store temporary data d) None
9. How the control unit determines the io instructions []
a) When opcode bits are 0000 to 0110 b) When opcode bits are 0111
c) When opcode bits are 1111 d) all of the above
10. Which of the following is correct []
a) If the address of the operand is specified in the address part of the instruction then it is called indirect address
b) If the address of the operand is implied in the instruction then it is called physical address
c) Address of the operand in a computation type-instruction is called effective address
d) None

count...2

II Fill in the Blanks:

11. The default offset register for code segment register is _____
12. The size of register instruction format is _____
13. Data from the multiplexed address bus is serpareted by _____
14. MOV instruction belongs to the _____category of 8086 instructions.
15. _____Pin is used to select maximum & minimum mode of operation in 8086 microprocessor
16. If the data and programs are stored in same memory then its architecture is called_____
17. The control unit controls other units by generating_____
18. Control memory in micro-programmed control unit is _____
19. When an instruction is executing, _____flip flop is checked for interrupt
20. The size of the flag register in 8086 processor is _____ bits

Code No: IT304PC

Set No. 4

BVRIT HYDERABAD COLLEGE OF ENGINEERING FOR WOMEN

II B.Tech. I Sem., I Mid-Term Examinations, December-2020

Computer Organization and Microprocessor

Objective Exam

Name: _____ Hall Ticket No.

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Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

1. How many latches are used in 8086 minimum and maximum mode []
a) 3 b) 2 c) 6 d) None of the above
2. MOV BL, CL performs []
a) 8 bit operation b) 16 bit operation c) Both d) None
3. ENDP stands for _____ []
a) End of the program b) End of the Procedure
c) End of the process d) All of the above
4. Hardware of the computer usually divided into _____ []
a) CPU b) Memory
c) Input and output devices d) All of the above
5. Computers that have a single-processor register usually called _____ []
a) AC b) IR c) PC d) TR
6. Program counter (PC) is used to _____ []
a) Stores current instruction b) Stores address of the next instruction
c) Used to store temporary data d) None
7. How the control unit determines the io instructions []
a) When opcode bits are 0000 to 0110 b) When opcode bits are 0111
c) When opcode bits are 1111 d) all of the above
8. Which of the following is correct []
a) If the address of the operand is specified in the address part of the instruction then it is called indirect address
b) If the address of the operand is implied in the instruction then it is called physical address
c) Address of the operand in a computation type-instruction is called effective address
d) None
9. The size of the instruction queue in 8086 is _____ bytes []
a) 2 b) 6 c) 8 d) 12
10. BHE & A0 pin signals are used for _____ []
a) Queue status b) Minimum mode and Maximum mode status
c) Interrupt status d) Memory bank access

count...2

II Fill in the Blanks:

11. Control memory in micro-programmed control unit is _____
12. When an instruction is executing, _____ flip flop is checked for interrupt
13. The size of the flag register in 8086 processor is _____ bits
14. The default offset register for code segment register is _____
15. The size of register instruction format is _____
16. Data from the multiplexed address bus is separated by _____
17. MOV instruction belongs to the _____ category of 8086 instructions.
18. _____ Pin is used to select maximum & minimum mode of operation in 8086 microprocessor
19. If the data and programs are stored in same memory then its architecture is called _____
20. The control unit controls other units by generating _____