

TL084I, TL084AI, TL084BI, TL084C, TL084AC, TL084BC

General purpose JFET quad operational amplifiers

Datasheet — production data

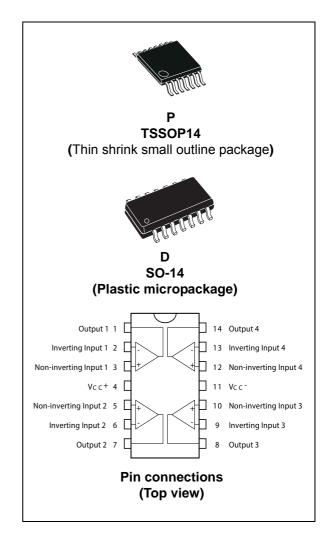
Features

- Wide common-mode (up to V_{CC}⁺) and differential voltage range
- · Low input bias and offset current
- Output short-circuit protection
- High input impedance JFET input stage
- · Internal frequency compensation
- · Latch up free operation
- High slew rate: 16 V/μs (typical)

Description

The TL084I, TL084AI, TL084BI, TL084C, TL084AC and TL084BC are high-speed, JFET input, quad operational amplifiers incorporating well matched, high voltage JFET and bipolar transistors in a monolithic integrated circuit.

The devices feature high slew rates, low input bias and offset currents, and low offset voltage temperature coefficient.



Contents

1	Schematic diagram 3
2	Absolute maximum ratings and operating conditions
3	Electrical characteristics
4	Parameter measurement information10
5	Typical applications11
6	Package information
	6.1 TSSOP14 package information
	6.2 SO-14 package information
7	Ordering information15
8	Revision history

1 Schematic diagram

Non-inverting Inverting In

Figure 1. Circuit schematics (for each amplifier)

2 Absolute maximum ratings and operating conditions

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CC}	Supply voltage ⁽¹⁾	±18	
V _{in}	Input voltage ⁽²⁾	±15	V
V _{id}	Differential input voltage ⁽³⁾	±30	
R _{thja}	Thermal resistance junction to ambient ⁽⁴⁾⁽⁵⁾ TSSOP14 SO-14	100 105	°C/W
P _{tot}	Power dissipation	680	mW
	Output short-circuit duration ⁽⁶⁾	Infinite	
т	Operating free-air temperature range: for TL084I/TL084AI/TL084BI	-40 to +125	
T _{oper}	Operating free-air temperature range: for TL084C/TL084AC/TL084BC	0 to +70	°C
T _{stg}	Storage temperature range	-65 to +150	
	HBM: human body model ⁽⁷⁾	1000	
ESD	MM: machine model ⁽⁸⁾	150	V
	CDM: charged device model ⁽⁹⁾	1500	

- All voltage values, except differential voltage, are with respect to the zero reference level (ground) of the supply voltages where the zero reference level is the midpoint between V_{CC}⁺ and V_{CC}⁻.
- 2. The magnitude of the input voltage must never exceed the magnitude of the supply voltage or 15 volts, whichever is less.
- 3. Differential voltages are the non-inverting input terminal with respect to the inverting input terminal.
- 4. Short-circuits can cause excessive heating and destructive dissipation.
- R_{th} are typical values.
- 6. The output may be shorted to ground or to either supply. Temperature and/or supply voltages must be limited to ensure that the dissipation rating is not exceeded.
- 7. Human body model: 100 pF discharged through a 1.5 $k\Omega$ resistor between two pins of the device, done for all couples of pin combinations with other pins floating.
- Machine model: a 200 pF cap is charged to the specified voltage, then discharged directly between two
 pins of the device with no external series resistor (internal resistor < 5 Ω), done for all couples of pin
 combinations with other pins floating.
- 9. Charged device model: all pins plus package are charged together to the specified voltage and then discharged directly to the ground.

Table 2. Operating conditions

Symbol	Parameter	TL084I/AI/BI	TL084C/AC/BC	Unit
V_{CC}	Supply voltage range	6	V	
T _{oper}	Operating free-air temperature range	-40 to +125	0 to +70	°C



3 Electrical characteristics

Table 3. $V_{CC} = \pm 15 \text{ V}$, $T_{amb} = +25 \text{ °C}$ (unless otherwise specified)

	Parameter	TL084I/AI/AC/BI/BC			TL084C			
Symbol	Parameter		Тур.	Max.	Min.	Тур.	Max.	Unit
V _{io}	Input offset voltage ($R_s = 50 \Omega$) $T_{amb} = +25 ^{\circ}\text{C} \text{TL084}$ $T_{amb} = +25 ^{\circ}\text{C} \text{TL084A}$ $T_{amb} = +25 ^{\circ}\text{C} \text{TL084B}$ $T_{min} \leq T_{amb} \leq T_{max} \text{TL084}$ $T_{min} \leq T_{amb} \leq T_{max} \text{TL084A}$ $T_{min} \leq T_{amb} \leq T_{max} \text{TL084B}$		3 3 1	10 6 3 13 7 5		3	10	mV
$\Delta V_{io}/\Delta T$	Input offset voltage drift		10			10		μV/°C
l _{io}	Input offset current $T_{amb} = +25 ^{\circ}C$ $T_{min} \leq T_{amb} \leq T_{max}$		5	100 4		5	100 4	pA nA
l _{ib}	Input bias current ⁽¹⁾ $T_{amb} = +25 \text{ °C}$ $T_{min} \leq T_{amb} \leq T_{max}$		20	200 20		30	200 20	pA nA
A _{vd}	Large signal voltage gain (R _L = 2 k Ω , V _o = ±10 V) T_{amb} = +25 °C $T_{min} \le T_{amb} \le T_{max}$	50 25	200		25 15	200		V/mV
SVR	Supply voltage rejection ratio ($R_S = 50 \Omega$) $T_{amb} = +25 \text{ °C}$ $T_{min} \leq T_{amb} \leq T_{max}$	80 80	86		70 70	86		dB
I _{CC}	Supply current, no load $T_{amb} = +25 ^{\circ}\text{C}$ $T_{min} \leq T_{amb} \leq T_{max}$		1.4	2.5 2.5		1.4	2.5 2.5	mA
V _{icm}	Input common mode voltage range	±11	+15 -12		±11	+15 -12		٧
CMR	Common mode rejection ratio ($R_S = 50 \Omega$) $T_{amb} = +25 ^{\circ}C$ $T_{min} \leq T_{amb} \leq T_{max}$	80 80	86		70 70	86		dB
I _{os}	Output short-circuit current $T_{amb} = +25 ^{\circ}C$ $T_{min} \leq T_{amb} \leq T_{max}$	10 10	40	60 60	10 10	40	60 60	mA
±V _{opp}	Output voltage swing $ T_{amb} = +25 ^{\circ}C \ R_L = 2 k\Omega $ $ R_L = 10 k\Omega $ $ T_{min} \leq T_{amb} \leq T_{max} \ R_L = 2 k\Omega $ $ R_L = 10 k\Omega $	10 12 10 12	12 13.5		10 12 10 12	12 13.5		V
SR	Slew rate V_{in} = 10 V, R_L = 2 k Ω , C_L = 100 pF, unity gain	8	16		8	16		V/μs



Table 3. V_{CC} = ±15 V, T_{amb} = +25 °C (unless otherwise specified) (continued)

Symbol	Parameter	TL084I/AI/AC/BI/BC			TL084C			Unit
Symbol	Farameter		Тур.	Max.	Min.	Тур.	Max.	Onit
t _r	Rise time V_{in} = 20 mV, R_L = 2 k Ω , C_L = 100 pF, unity gain		0.1			0.1		μs
K _{ov}	Overshoot V_{in} = 20 mV, R_L = 2 k Ω , C_L = 100 pF, unity gain		10			10		%
GBP	Gain bandwidth product V_{in} = 10 mV, R_L = 2 k Ω , C_L = 100 pF, F= 100 kHz	2.5	4		2.5	4		MHz
R _i	Input resistance		10 ¹²			10 ¹²		Ω
THD	Total harmonic distortion F= 1 kHz, R_L = 2 k Ω , C_L = 100 pF, A_v = 20 dB, V_o = 2 V_{pp})		0.01			0.01		%
e _n	Equivalent input noise voltage $R_S = 100 \Omega$, F= 1 kHz		15			15		$\frac{\text{nV}}{\sqrt{\text{Hz}}}$
Øm	Phase margin		45			45		degrees
V ₀₁ /V ₀₂	Channel separation A _v = 100		120			120		dB

^{1.} The input bias currents are junction leakage currents which approximately double for every 10°C increase in the junction temperature.

47/

Figure 2. Maximum peak-to-peak output voltage Figure 3. Maximum peak-to-peak output voltage vs. frequency ($R_L = 2 \text{ k}\Omega$) vs. frequency ($R_L = 10 \text{ k}\Omega$)

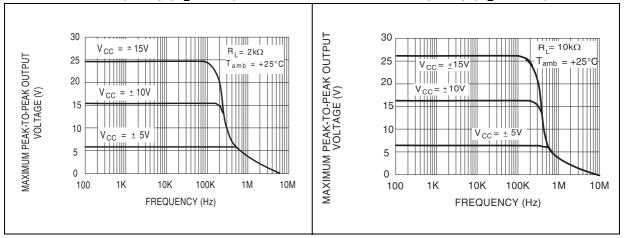


Figure 4. Maximum peak-to-peak output voltage Figure 5. Maximum peak-to-peak output voltage vs. frequency and temp.

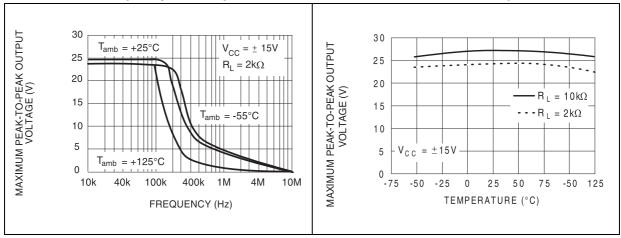


Figure 6. Maximum peak-to-peak output voltage vs. load resistance vs. supply voltage

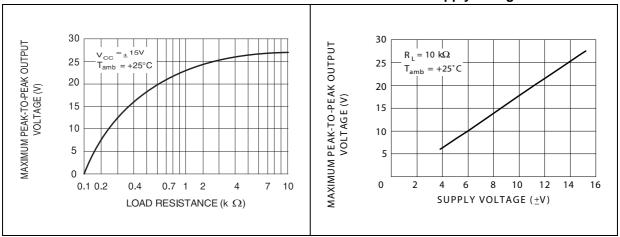


Figure 8. Input bias current vs. free air temp.

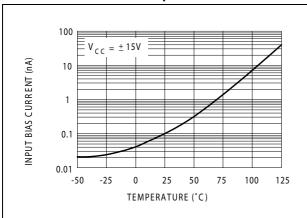


Figure 9. Large signal differential voltage amplification vs. free air temp.

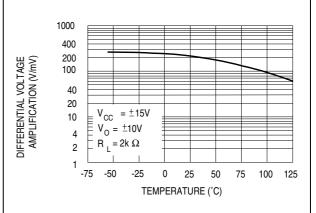


Figure 10. Large signal differential voltage amplification and phase shift vs. frequency

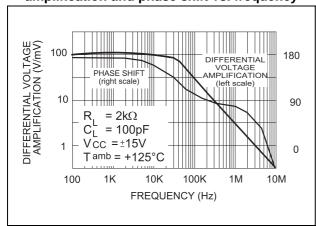


Figure 11. Total power dissipation vs. free air temp.

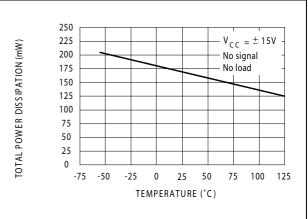


Figure 12. Supply current per amplifier vs. free air temp.

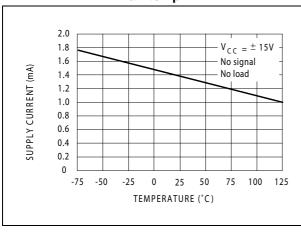
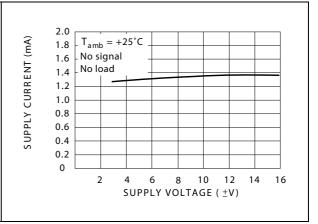


Figure 13. Supply current per amplifier vs. supply voltage



8/17 DocID2301 Rev 7

Figure 14. Common mode rejection ratio vs. free air temp.

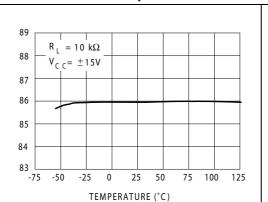


Figure 15. Voltage follower large signal pulse response

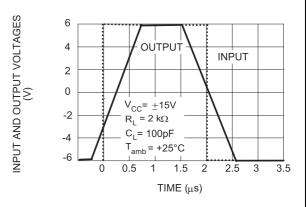
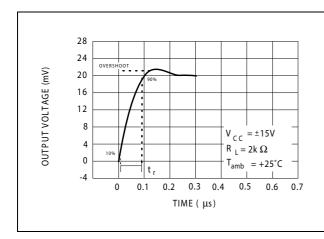


Figure 16. Output voltage vs. elapsed time

Figure 17. Equivalent input noise voltage vs. frequency



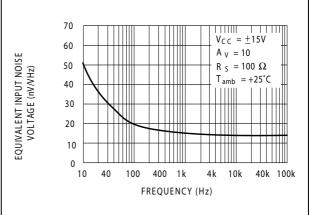
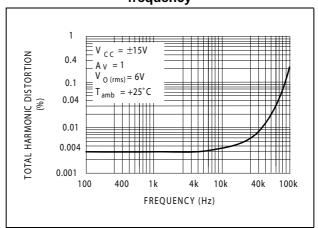


Figure 18. Total harmonic distortion vs. frequency



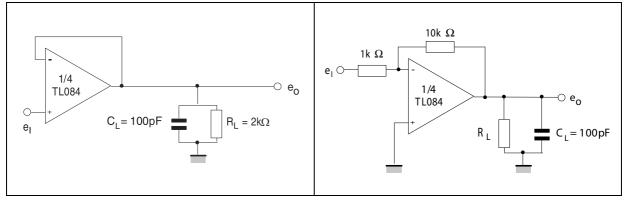
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COMMON MODE MODE REJECTION RATIO (dB)

4 Parameter measurement information

Figure 19. Voltage follower

Figure 20. Gain-of-10 inverting amplifier



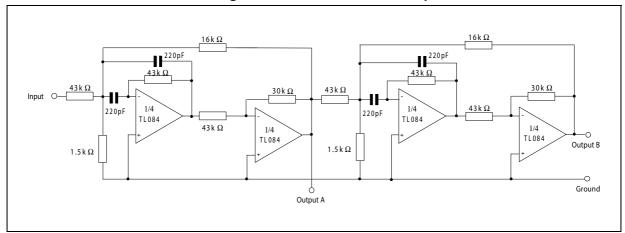
10/17 DocID2301 Rev 7

5 Typical applications

 $f_O = 100 kHz$ 1/4 Output A TL084 1M Ω 1/4 1μF TL084 1/4 Output B Input \bigcirc — TL084 100k Ω 100k Ω 100k Ω ____ V_{CC}+ 100 μF 100k Ω 1/4 Output C TL084

Figure 21. Audio distribution amplifier

Figure 22. Positive feeback bandpass filter



Cascaded bandpass filter fo = 100 kHz; Q = 69; Gain = 16

Figure 23. Output A Figure 24. Output B Second order bandpass filter fo = 100 kHz; Q = 30; Gain = 4

6 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

6.1 TSSOP14 package information

A A2 A1 D C K E

Figure 25. TSSOP14 package mechanical drawing

Table 4. TSSOP14 package mechanical data

Ref.		Millimeters			Inches	
Rei.	Min.	Тур.	Max.	Min.	Тур.	Max.
Α			1.2			0.047
A1	0.05		0.15	0.002	0.004	0.006
A2	0.8	1	1.05	0.031	0.039	0.041
b	0.19		0.30	0.007		0.012
С	0.09		0.20	0.004		0.0089
D	4.9	5	5.1	0.193	0.197	0.201
Е	6.2	6.4	6.6	0.244	0.252	0.260
E1	4.3	4.4	4.48	0.169	0.173	0.176
е		0.65 BSC			0.0256 BSC	
K	0°		8°	0°		8°
L1	0.45	0.60	0.75	0.018	0.024	0.030

6.2 SO-14 package information

Figure 26. SO-14 package mechanical drawing

Table 5. SO-14 package mechanical data

	Dimensions						
D-f		Millimeters			Inches		
Ref.	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α	1.35		1.75	0.05		0.068	
A1	0.10		0.25	0.004		0.009	
A2	1.10		1.65	0.04		0.06	
В	0.33		0.51	0.01		0.02	
С	0.19		0.25	0.007		0.009	
D	8.55		8.75	0.33		0.34	
E	3.80		4.0	0.15		0.15	
е		1.27			0.05		
Н	5.80		6.20	0.22		0.24	
h	0.25		0.50	0.009		0.02	
L	0.40		1.27	0.015		0.05	
k	8° (max.)						
ddd			0.10			0.004	

14/17 DocID2301 Rev 7

7 Ordering information

Table 6. Order codes

Order code	Temperature range	Package	Packing	Marking
TL084IDT				0841
TL084AIDT	-40°, +125°	SO-14	Tape & reel	084AI
TL084BIDT				084BI
TL084IYDT ⁽¹⁾	-40°, +125°	SO-14		084IY
TL084BIYDT(1)	Automotive grade	30-14		084BIY
TL084IPT	-40°, +125°	TSSOP14		0841
TL084CDT				084C
TL084ACDT	0°, +70°	SO-14		084AC
TL084BCDT	U , +/U			084BC
TL084CPT		TSSOP14		084C
TL084CD	0°, +70°	SO-14	Tube	084C

Qualified and characterized according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 & Q002 or equivalent.



8 Revision history

Table 7. Document revision history

Date	Revision	Changes
28-Mar-2001	1	Initial release.
30-Jul-2007	2	Added values for R_{thja} , R_{thjc} and ESD in Table 1: Absolute maximum ratings. Added Table 2: Operating conditions. Expanded Table 6: Order codes. Template update.
15-Jul-2008	3	Removed information concerning military temperature ranges (TL084Mx, TL084AMx, TL084BMx). Added automotive grade order codes in Table 6: Order codes.
05-Jul-2012	4	Removed commercial types TL084IYD, TL084AIYD and TL084BIYD. Updated Table 6: Order codes.
29-Jan-2013	5	Added part numbers TL084A and TL084B. Added SO-14 package silhouette. Updated layout of Table 1: Absolute maximum ratings. Updated of Table 3: VCC = ±15 V, Tamb = +25 °C (unless otherwise specified). Replaced SO-14 package mechanical drawing (Figure 27: SO-14 package mechanical drawing). Replaced SO-14 package mechanical data (Table 5: SO-14 package mechanical data).
11-Jul-2017	6	Replaced 105 °C by 125 °C in Table 1: Absolute maximum ratings and Table 2: Operating conditions. Removed DIP14 package information. Updated Table 6: Order codes to remove obsolete part numbers.
17-Oct-2017	7	Updated part numbers in the title, and in Table 6: Order codes.



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