

A

B

C

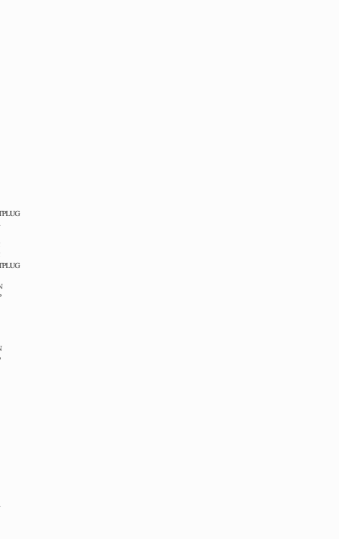
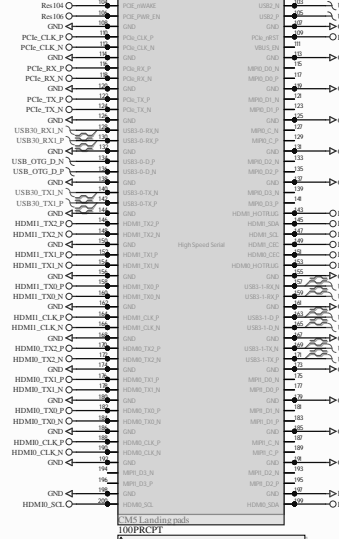
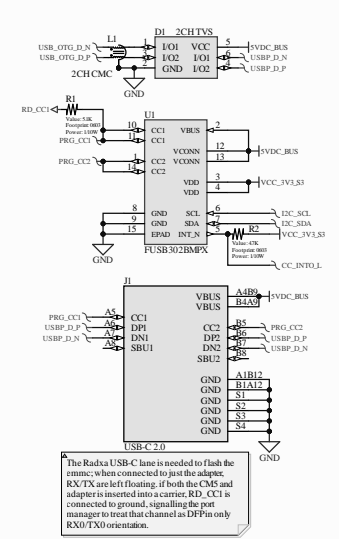
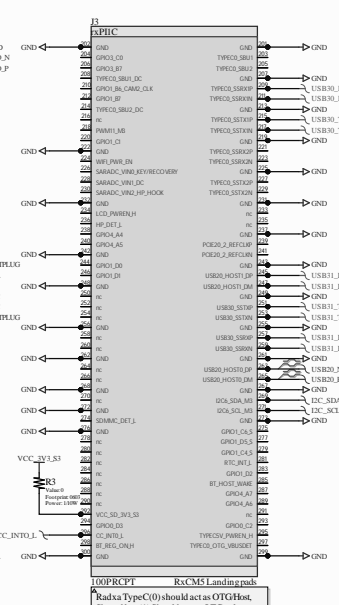
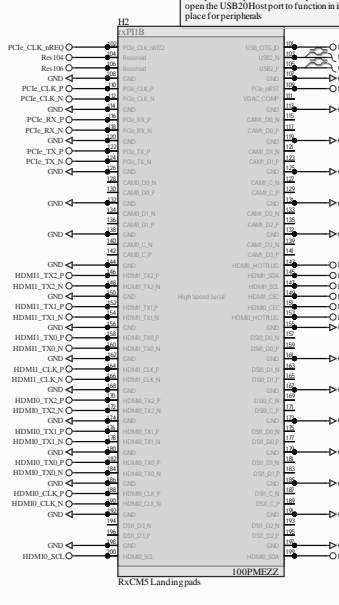
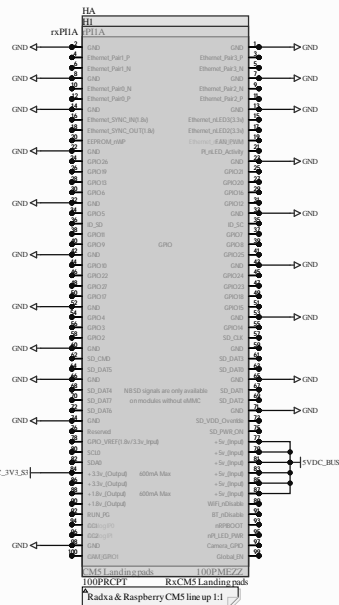
D

A

B

C

D

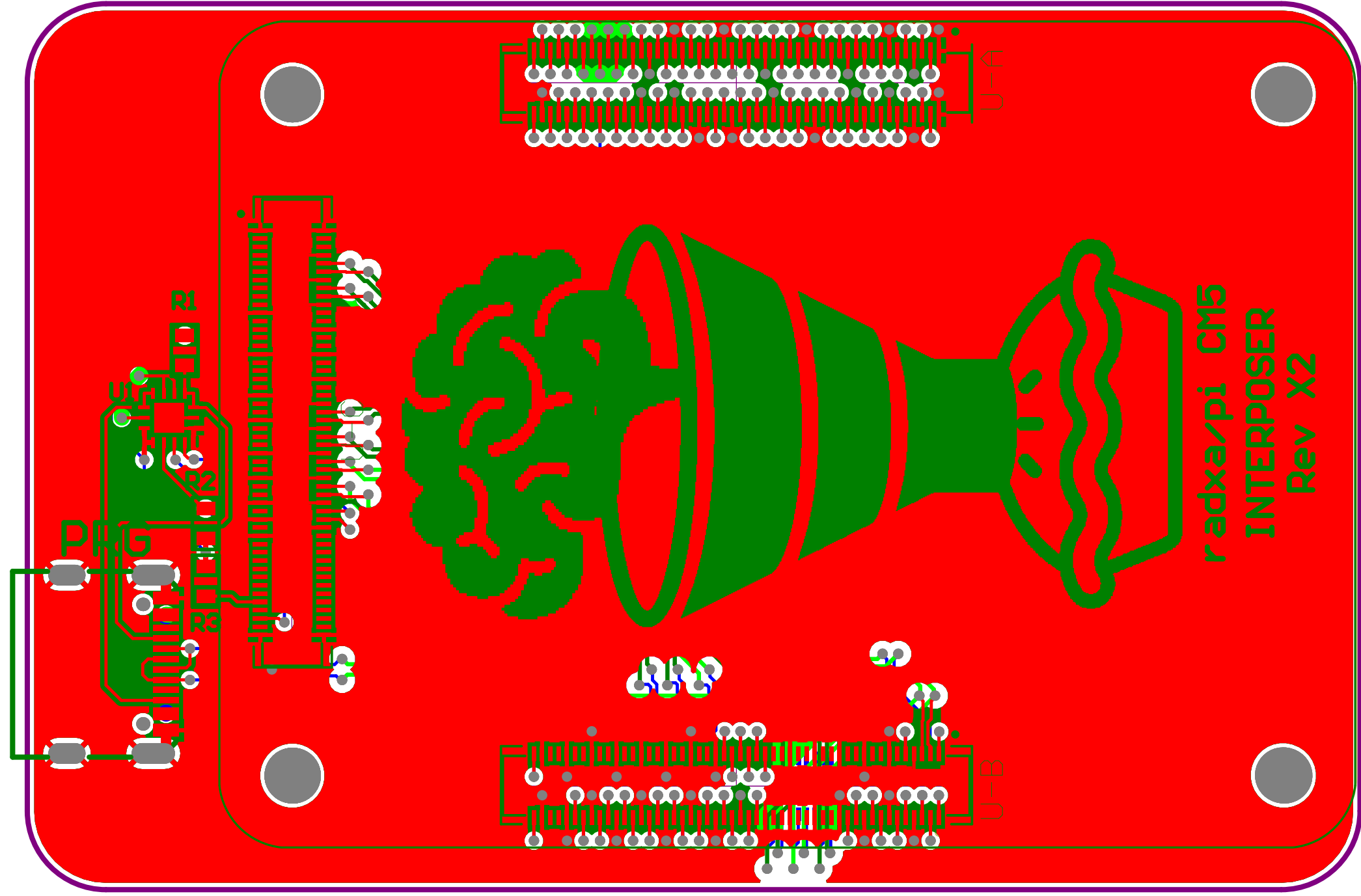


Radxa HDMI & PCIe line up. CAM & DSI do not. USB2 OTG is part of USB-C port, so it is transposed to operate at USB3 speeds and opens the USB20 Host port to function in its place for peripherals

Radxa TypeC0 should act as OTG Host. Shared bus (1) Should act as OTG only.

The Radxa USB-C lane is needed to flash the rom, when connected to just the adapter. RX/TX are left floating, if both the CMS and adapter is inserted into a carrier. RD\_CCI1 is connected to ground, signalling the port manager to treat that channel as DFF pin only RX0/TX0 orientation.

Raspberry USB 3 lanes must come from Radxa J3; USB3\* RX&TX pass directly. \*D\_PD\_N are relocated based on carrier function



Designator	Footprint	Value	LCSC PN
D1	FP-SOT886-MFG	1pF	C478118
H1	DF40G-100DS- 0.4V(51)		C597931
H2	DF40G-100DS- 0.4V(51)		C597931
HA	DF40G-100DP- 0.4V(51)		C19089239
HB	DF40G-100DP- 0.4V(51)		C19089239
J1	GT-USB-7010ASV		C2988369
J3	DF40G-100DS- 0.4V(51)		C597931
L1	SMD-6-2CH		C396460
R1	0603	5.1K	
R2	0603	4.7K	
R3	0603	0	
rPI1	rP-200P-CM-REP (top)		
rxPI1	RX-300P-CM-FTPD		
U1	MLP14_2P5X2P5_ONS- L		C132291