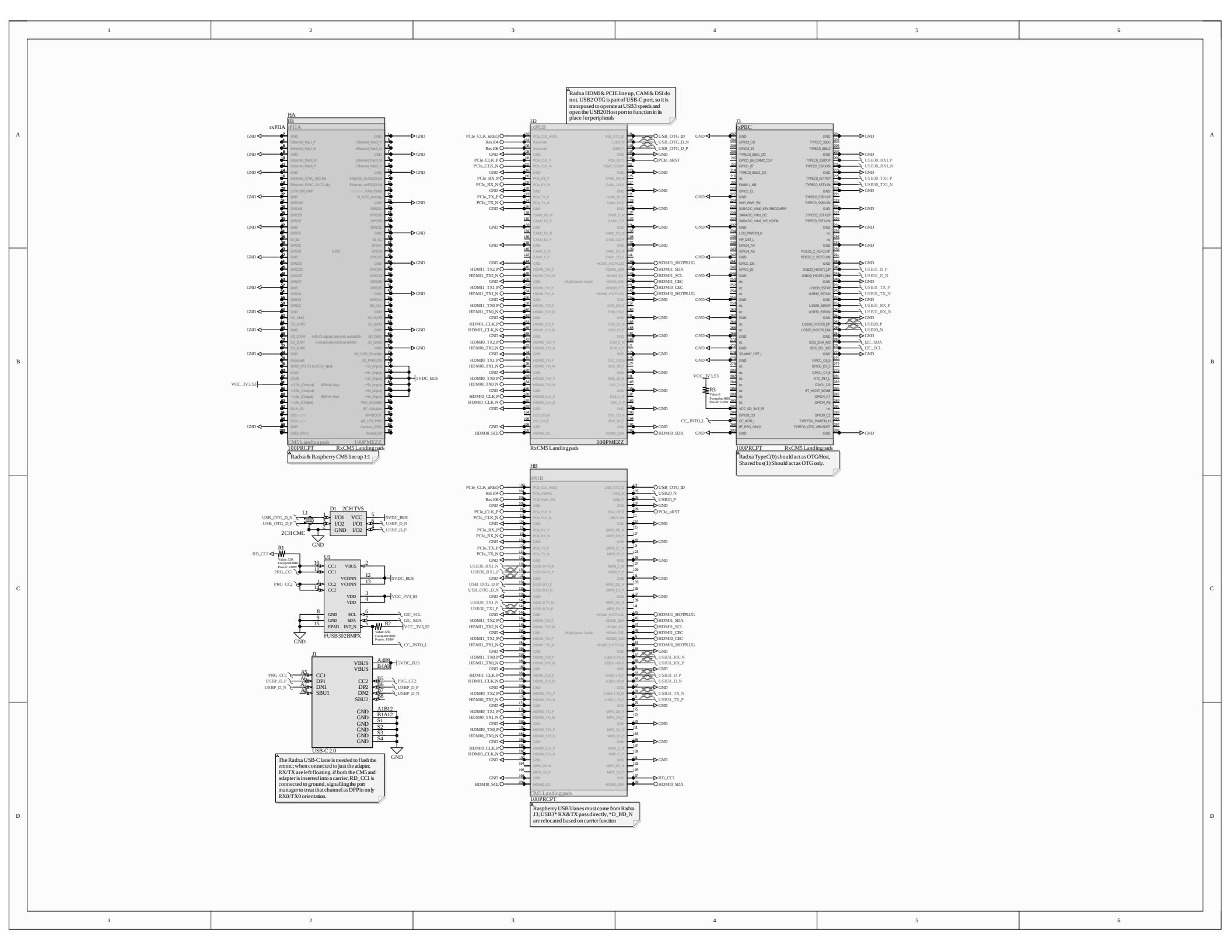
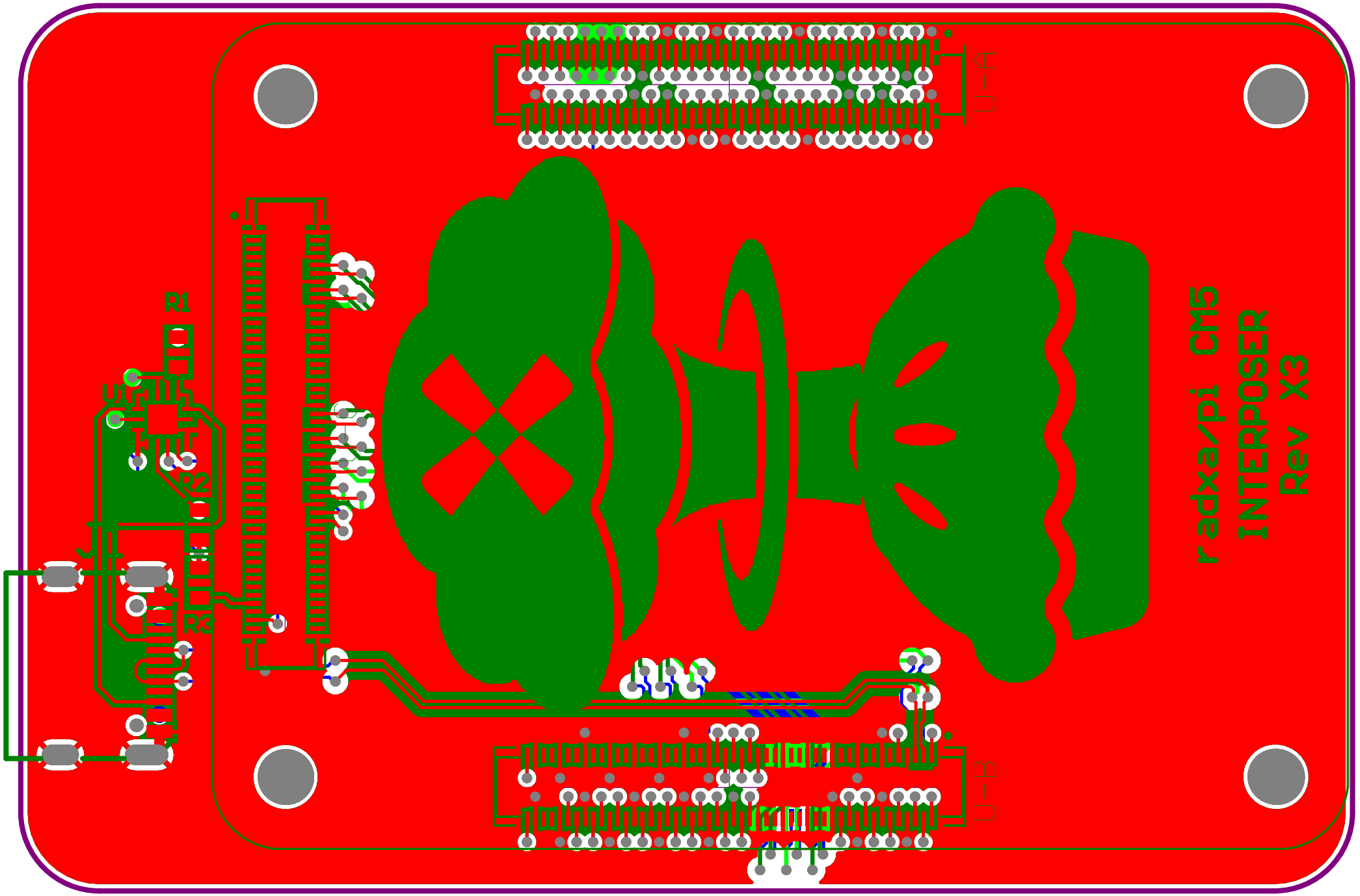

HA
H1
rxPI1A
100PRCPT
RxCM5 Landing pads
Radxa & Raspberry CM5 line up 1:1
Radxa HDMI & PCIE line up, CAM & DSI do not. USB2 OTG is part of USB-C port, so it is transposed to operate at USB3 speeds and open the USB20 Host port to function in its place for peripherals
H2
rxPI1B
100PMEZZ
RxCM5 Landing pads
J3
xPIIC
100PRCPT
RxCM5 Landing pads
Radxa TypeC(0) should act as OTG/Host, Shared bus(1) Should act as OTG only.
HB
xPI1B
CM5 Landing pads
100PRCPT
Raspberry USB3 lanes must come from Radxa J3; USB3* RX&TX pass directly, *_P/D_P/D_N are relocated based on carrier function
D1 2CH TVS
2CH CMC
R1
R2
R3
U1
FUSB302BMPX
J1
USB-C 2.0
The Radxa USB-C lane is needed to flash the emmc; when connected to just the adapter, RX/TX are left floating. if both the CM5 and adapter is inserted into a carrier, RD_CC1 is connected to ground, signalling the port manager to treat that channel as DFPin only RX0/TX0 orientation.


J1
J2
J3
R1
R2
R3
raspi CM5
INTERPOSER
Rev X3

Designator	Footprint	Value	LCSC PN
D1	FP-SOT886-MFG	1pF	C478118
H1	DF40C-100DS-0.4V(51)		C597931
H2	DF40C-100DS-0.4V(51)		C597931
HA	DF40C-100DP-0.4V(51)		C19089239
HB	DF40C-100DP-0.4V(51)		C19089239
J1	GT-USB-7010ASV		C2988369
J3	DF40C-100DS-0.4V(51)		C597931
L1	SMD-6-2CH		C396460
R1	0603	5.1K	
R2	0603	4.7K	
R3	0603	0	
rPI1	rP-200P-CM-REP (top)		
rxPI1	RX-300P-CM-FTPD		
U1	MLP14_2P5X2P5_ONS-L		C132291