

Unit -2

Data link layer

The data link layer is a hardware level. Information at this layer is in the form of frames. It takes the services from physical layer and provides the services to network layer. It is divided into 2 sublayers -

- 1.) media Access control sublayer (MAC)
- 2.) logical link control sublayer (LLC)

imp. design issues of Data link layer

- 1.) Data Transfer
- 2.) Frame synchronization
- 3.) Error control
- 4.) Flow control
- 5.) Addressing
- 6.) link management

1.) Data Transfer-

The data link layer is responsible to provide defined services to the network layer. The principle of this service is to provide the data from the network layer on the source machine to the destination machine there are 2 types of services -

- 1.) Connection less
- 2.) connection oriented

2) Frame synchronization -

The source machine sends data in a block called frames to the destination machine. The starting and ending of each frame must be recognized by the destination machine.



3) Flow control -

The source machine should not send data frame at a fast rate as compared to the destination machine receiving data rate. When a sender sends data in a high speed, the slow receiver will not be able to handle it and thus, there will be a chance of losing the data frame.

4) Error control -

The error made in bits during the transmission from the source to destination machine must be detected and corrected. Thus, this layer ensures error free data transmission.

5) Addressing -

Each frame comprises of a header which contains source and destination addresses.

6) Link management -

The initiation, maintenance and termination of a link between the source and destination is required for effective exchange of data.

Framing

Framing in data link layer is a point-to-point connection between the sender and the receiver. The data link layer receives packets from the network layer and converts them into frames. Small frames are more efficient for flow and error control.



Header -

The source and destination address is placed in header part of the frame.

Payload field -

It contains the actual message that the sender wants to transmit to the destination.

Trailer -

It comprises of error detection and error correction bits.

Flag -

It shows the beginning and end of a frame.

Types of Framing

1) Fixed size framing

2) Variable size framing

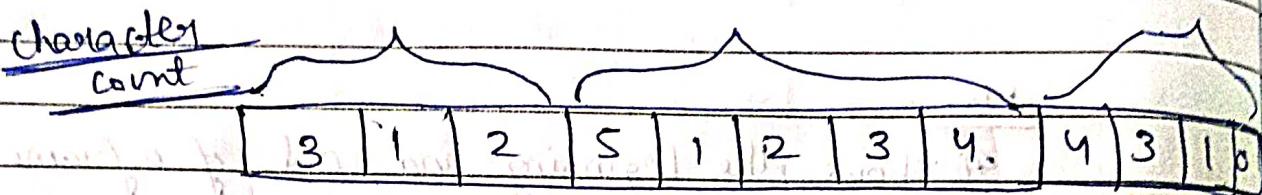
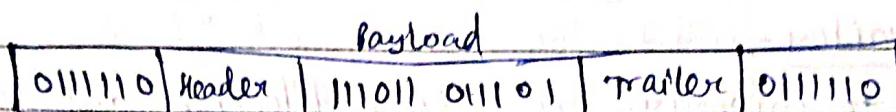
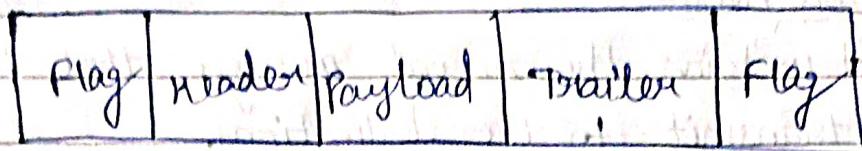
1) Fixed size framing -

The frame has fixed size. There is no need of defining the boundaries of the frame to mark the beginning and end of the frame.

2) Variable size framing -

In variable size, we need a way to define the end and start of a frame.

value of flag = 0111110



bit stuffing
byte stuffing

Error

- 1) single bit error
- 2) multi bit error
- 3) burst error → multibit with consecutive error

error detection

- 1) Parity check → for single bit error

even parity

(even no. of 1's)

→ add 0 bit

(odd no. of 1's)

→ add 1 bit

odd parity

(odd no. of 1's)

→ add 0 bit

(even no. of 1's)

→ add 1 bit

Checksum - 1's complement

10011001	11100010	00100100	10000100
----------	----------	----------	----------

$$\begin{array}{r}
 & & 10011001 \\
 & & 11100010 \\
 & & 00100100 \\
 & & 10000100 \\
 \hline
 10 | & 00100011 & & 00100111 \\
 & + & & + 10 \\
 & & & \hline
 & & 00100101 &
 \end{array}$$

→ 11011010 → 1's complement

→ 11111111
00000000 → 1's complement.

→ This indicates no error.

10011111	11100010	00100101	10000100
----------	----------	----------	----------

$$\begin{array}{r}
 10011111 \\
 11100010 \\
 00100101 \\
 10000100 \\
 \hline
 100100010
 \end{array}$$

00101000

10011111

111

0010111001

010001110

01100100

00100011

11000100101

$$\begin{array}{r} & 1 \\ & + 1 \\ \hline & 10 \\ + 0 & \hline 10 \end{array} \quad \begin{array}{r} & 1 \\ & + 1 \\ \hline & 10 \\ + 1 & \hline 10 \end{array} \quad \begin{array}{r} & 1 \\ & + 1 \\ \hline & 10 \\ + 1 & \hline 10 \end{array}$$

100

~~0010111001~~~~010001110~~~~01100100~~~~00100011~~

00100011010000 | 01000111 | 1101001 | 00000000

0100010000

11111000

01000111

01100100

00100000

0100010001

working of CRC

Q. In a CRC (cyclic redundancy check), error detecting scheme the generator polynomial is $x^4 + x^3 + x + 1$ and the 11 bit code 10010011011.

Sol:-

$$x^4 + x^3 + 1 = 10011$$

$$x^4 + x^3 + x + 1$$

$$10011$$

sender :-

10011	100100110110000	10001010100
-------	-----------------	-------------

10011

00000

X 00101

00000

X 01011

00000

X 10110

10011

X 01011

00000

X 10111

10011

X 01000

00000

X 10000

10011

X 00110

00000

X 01100

00000

X 1100

CRC bit

An 100100110111100 An

Repetition -

100100110111100
100100110111100

0000000000000000

100100110111100

0000000000000000

100100110111100

0000000000000000

100100110111100
100100110111100

110110

0000000000000000

100100110111100

0000000000000000

100100110111100

0000000000000000

110110

0000000000000000

100100110111100

0000000000000000

100100110111100

0000000000000000

100100110111100

Hamming Code (error detection & correction)

$$2^n \Rightarrow 2^0 = 1 \quad P_1$$

$$2^1 = 2 \quad P_2$$

$$2^2 = 4 \quad P_3$$

$$2^3 = 8 \quad P_4$$

Parity bit

Data bit

msg \rightarrow $D_3 D_2 D_1$
 $1 \ 1 \ 0 \ 0$

4 bit.

110001

3 bit parity to be added.

8	7	6	5	4	3	2	1
P ₄	D ₄	D ₃	D ₂	P ₃	D ₁	P ₂	P ₁

P ₄	1	1	0	P ₃	0	P ₂	P ₁
----------------	---	---	---	----------------	---	----------------	----------------

111	110	101	100	011	010	001
-----	-----	-----	-----	-----	-----	-----

$$P_1 = 1 \ 3 \ 5 \ 7 = 1 \quad (\text{least sign. bit में } 1)$$

Even parity

odd -

(वियमें 1)

$$P_2 = 2 \ 3 \ 6 \ 7 = 0$$

already even

$$P_3 = \begin{matrix} 4 & 5 & 6 & 7 \\ \times & 0 & 1 & 0 \end{matrix} = \boxed{0}$$

P_4 = not required.

Q. 100011

100011 → 0 2 3 4 5 6 7 8
 $P_1, P_2, D_1, P_3, D_2, D_3, D_4$

$$P_1 = 2^0 = 1$$

$$P_2 = 2^1 = 2$$

$$P_3 = 2^2 = 4$$

$$P_4 = 2^3 = 8$$

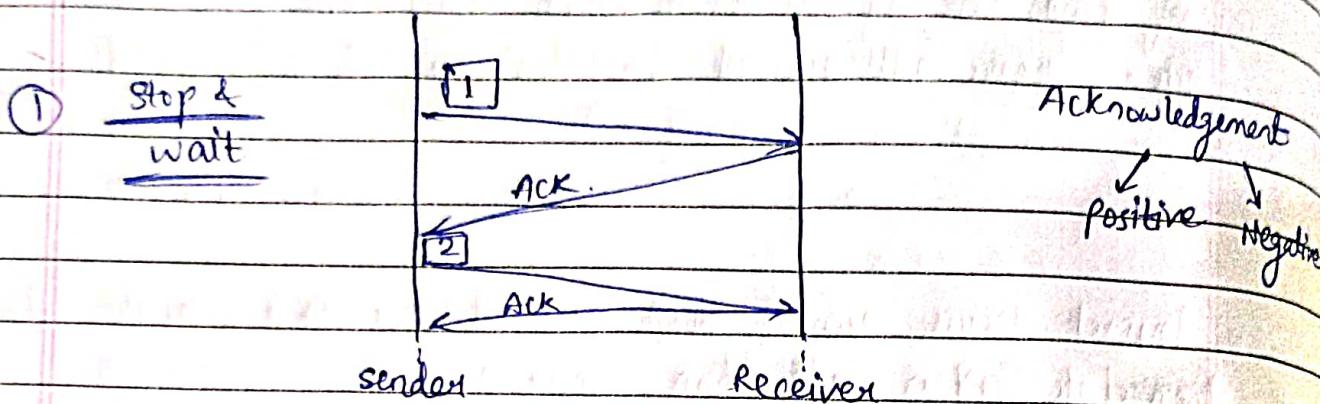
$$P_5 = 2^4 = 16$$

16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1
P_5						D_6	P_5	P_4	D_4	D_3	P_2	P_3	D_1	P_2	P_1

16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	
P_5							0	P_4	0	0	0	1	P_3	1	P_2	P_1

(1 to 10 bits)

Flow control



Disadvantages -

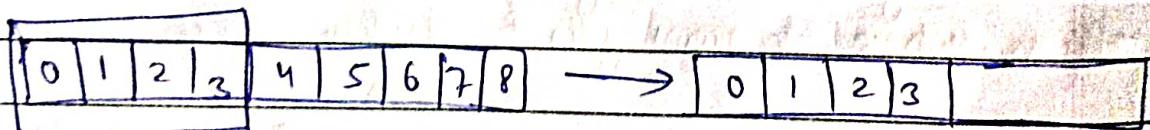
- 1.) wastage of Bandwidth
- 2.) wait for ack. of one

② Automatic Repeat Request

timer

③ sliding window-

$K=4$ (window size)



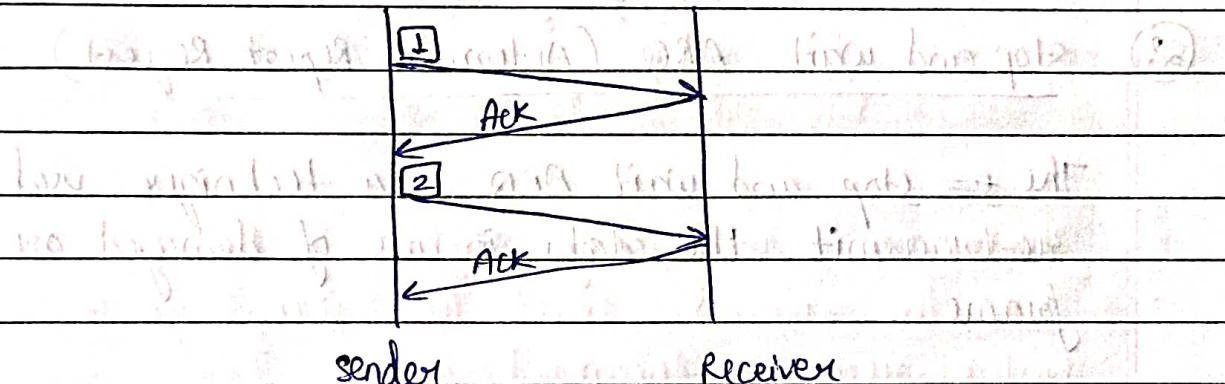
Flow control

① Stop and wait Protocol -

It is a set of procedure that tells the sender how much data it can transmit to the receiver.

The Receiving device has limited speed and memory to store the data! Therefore, the receiving device must be able to inform the sending device to stop the transmission temporarily before the limits have reached.

① Stop and Wait Protocol -



The stop and wait protocol is the most simplest protocol used in flow control.

In this method, the sender waits for an acknowledgement after every frame it sends.

When the acknowledgement is received, then only the next frame is sent.

Advantages -

The stop and wait method is simple as each frame is checked and acknowledged before the next frame is sent.

Disadvantages -

It is inefficient as each frame must travel ~~all~~ way to the receiver and the acknowledgement travels all the way before the next frame is sent. Thus, there is a wastage of bandwidth.

② stop and wait ARQ (Automatic Repeat Request) -

The ~~stop~~ stop and wait ARQ is a technique used to re-transmit the data in case of damaged or lost frame.

This technique works on a principle that the sender will not transmit the next frame until it receives the acknowledgement of the last frame. There is a timer after which the frame is re-transmitted.

③ Sliding Window -

The sliding window method in ~~flow~~ flow control is a technique in which the sender can transmit several frames by getting an acknowledgement. Thus, the utilization of

communication channel is very high.

Sliding window refers to imaginary boxes at both the sender and receiver. The frames can be acknowledged even when the window is not completely filled.

When a new frame arrives, the size of the window shrink once the acknowledgement has been sent, the receiver window expands.

Go Back N -

If one frame is lost or damaged then it re-transmits all the frames after which it does not receive the positive acknowledgement.

Selective Reject -

This technique is more efficient than Go Back N.

In this technique only those frames are re-transmitted for which negative acknowledgement has received.

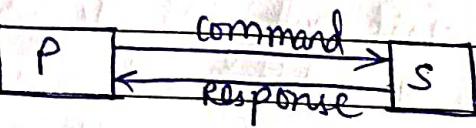
The receiver stores buffer which keeps all the damaged frame on hold until the frame is correctly received.

imp.

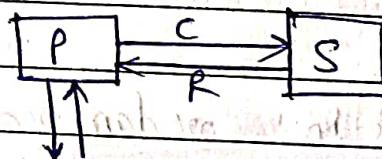
HDLC (High level datalink control)

HDLC is a group of communication protocols of data link layer for transmitting data. It is applicable for point-to-point and point-to-multipoint communication.

NRM (Normal Response Mode)



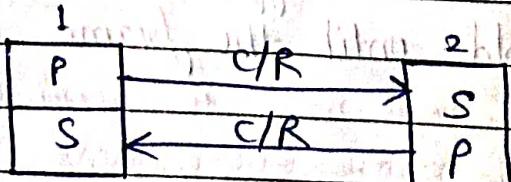
point-to-point



There are two types of stations, the primary station sends the command and the secondary station responds to the received command.

ARM (Asynchronous Response mode)

Each stations can send the command and respond to the commands.



HDLC Frame -

beginning sequence	Header	Payload	CRC	Ending sequence
--------------------	--------	---------	-----	-----------------

The Beginning and the ending sequence is a 8-bit sequence that marks the beginning and ending of the frame. Its pattern is 0111110.

Header - It is of 16 bits.

The header is divided into two - address and control.

•

1) Address - It contains the addresses of primary and secondary stations.

2) Control - It contains the control frames.

Payload -

It carries the data from the network layer. Its length may be vary.

CRC -

It is a 16 bit sequence for error detection.

Types of frame -

There are 3 types of frame. The type of frame is determined by the control field of frame.

i) unnumbered frame -

The frame or the information frame carries the user data from the network layer. The value of the control field is 0.

ii) numbered frame -

The frame or the supervisory frame does not contain the information. The 1st two bits of control field is 01.

iii) numbered frame -

It is framing or unnumbered frame. It is used for link management. The 1st two bits of control field is 11.

Multiple Access Control Protocol (MAC)

The data link layer is separated into two sub-layers. The upper sublayer is responsible for flow control and error control. It is called logical link control layer. The lower sublayer is responsible for multiple access resolution i.e. called media access control.

The media access control protocol is divided into three protocols -

- 1.) The Random Access
- 2.) The controlled Access
- 3.) The channelization protocol.

1.) Random Access Protocol -

In random access protocol all the system have equal priority. No one depends and controls the another system.

If more than one system attempts to transmit the data, there may be a chance of collision, due to which the frames may either be lost or damaged.

Random Access protocol is divided into -

- (i) ALOHA - It is used for random access in the network.
It was designed for wireless local area network.
User transmits the data any time.

Types of ALOHA -

- 1.) Pure ALOHA
- 2.) slotted ALOHA

1) Pure ALOHA - It is the simplest protocol in which whenever the station has data it transmits its frame continuously, due to which there is high level of risk of collision.

- Each station transmits the data whenever it has a data without checking the system/channel is idle or not.
- Whenever the data frame is sent successfully, the receiver waits for an acknowledgement. If the receiver does not receive an acknowledgement within a specified time, the station waits for a random time called the back off time.

2) slotted ALOHA - The time of the system is divided into slots so that the system can send only one frame at that slot. It is better than the pure ALOHA because the chance of collision is low.

Q A bit stream 10011101 is transmitted using CRC method. The generator polynomial is $x^3 + 1$. What is the actual bit transmitted? If the 3rd bit is inverted, how will the receiver detect this error?

$$\text{generator} = x^3 + x^2 + x + 1 \\ = \underline{\underline{1001}}$$

$$\begin{array}{r} 1001) 10011101000 (10001100 \\ - 1001 \\ \hline x 0001 \\ \hline 0000 \end{array}$$

$$\begin{array}{r} x 0011 \\ \hline 0000 \end{array}$$

$$\begin{array}{r} x 0110 \\ \hline 0000 \end{array}$$

$$\begin{array}{r} x 1101 \\ \hline 1001 \end{array}$$

$$\begin{array}{r} x 1000 \\ \hline 1001 \end{array}$$

$$\begin{array}{r} x 0010 \\ \hline 0000 \end{array}$$

$$\begin{array}{r} x 0100 \\ \hline 1000 \end{array}$$

$x 1000 \rightarrow \text{CRC bit}$

~~100111010001000~~ A

10011101100 Ans.

3rd bit inverted -

~~10011101~~

\rightarrow ~~10111101~~

~~1001 | 10111101000 | 101~~

~~1001~~

~~x 0101~~

~~0000~~

~~x 1011~~

~~1001~~

~~x 0100~~

~~0000~~

~~1001 | 10111101100 | 10101000~~

~~1001~~

~~x 0101~~

~~0000~~

~~x 1011~~

~~1001~~

~~x 0100~~

~~0000~~

~~x 1001~~

~~001001~~

~~x 0001~~

~~0000~~

~~x 0010~~

~~0000~~

~~x 0100~~

~~0000~~

~~x 1001000~~

\rightarrow since, bits are no
000 this confirms
error.

~~IMP~~ CSMA (Carrier Sense Multiple Access)

It is a MAC protocol which sends the traffic on channel before transmitting the data. If the channel is idle, the station can send the data or else wait for the channel to become idle, hence it reduces the chance of collision.

CSMA/CD (Carrier sense multiple access/Collision detection)

It is a MAC protocol, it sense the shared channel before broadcasting the frames. If the channel is idle, it transmit the frame and checks whether the transmission was successful. If the frame was successfully received it sends the another frame. If any collision is detected, the station sends a jam signal. The algorithm of CSMA/CD is -

- (i) When the frame is ready, the transmitting station checks whether the channel is free or not.
- (ii) If the channel is busy, the station waits until the channel becomes idle. If the collision is detected, jam signal is sent.
- (iii) The station completes the frame transmission.

CSMA/CA (Collision Avoidance)

It is a MAC protocol for avoidance of collision. When a data frame is sent, the channel receives an acknowledgement to check whether the channel is clear or not. If it receives two signals, a collision is detected.

Algorithm of CSMA/CA is —

- (i) If the frame is ready, the transmitting station checks whether channel is idle or not.
- (ii) If channel is busy, it waits for the channel to become idle.
- (iii) If the channel is idle, station waits for an interframe gap and then sends the frame.
- (iv) After sending the frame, it sets a timer.
- (v) The station waits for an acknowledgement before the expiry of timer.