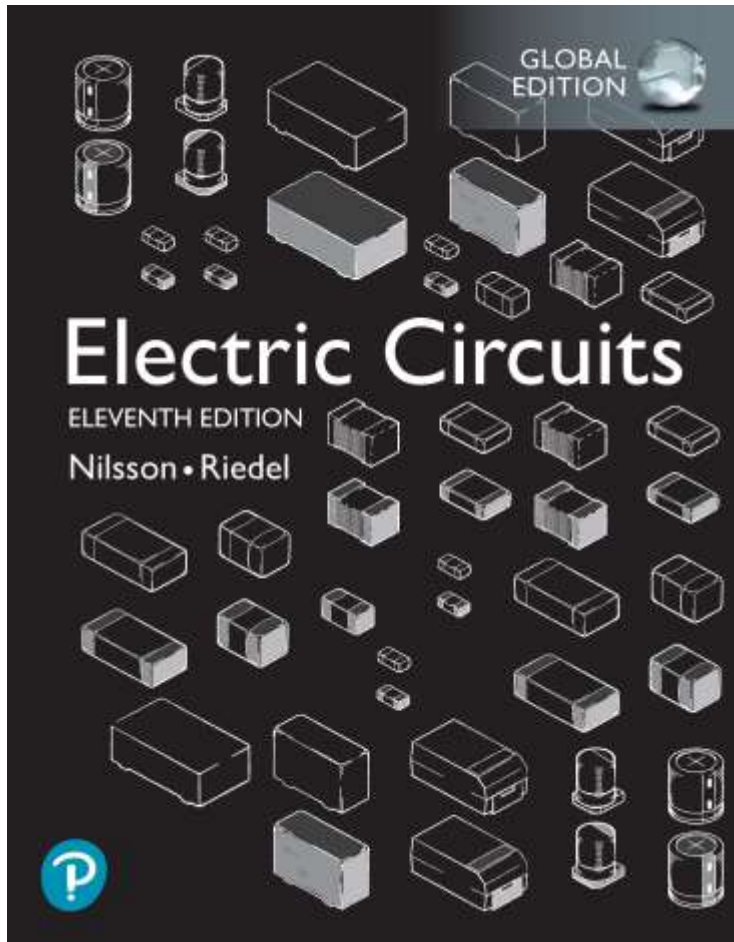


# Electric Circuits

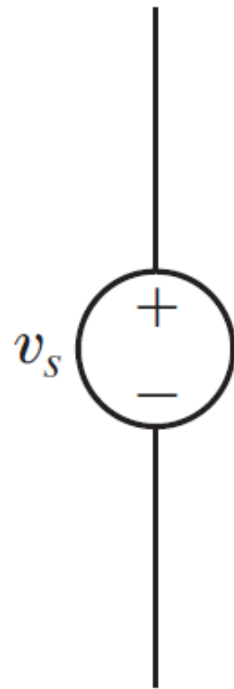
Eleventh Edition, Global Edition



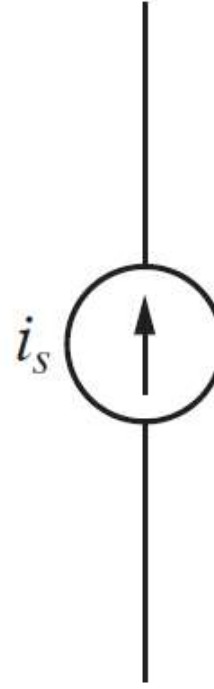
## Chapter 2

### Circuit Elements

The circuit symbols for (a) an ideal independent voltage source and (b) an ideal independent current source.

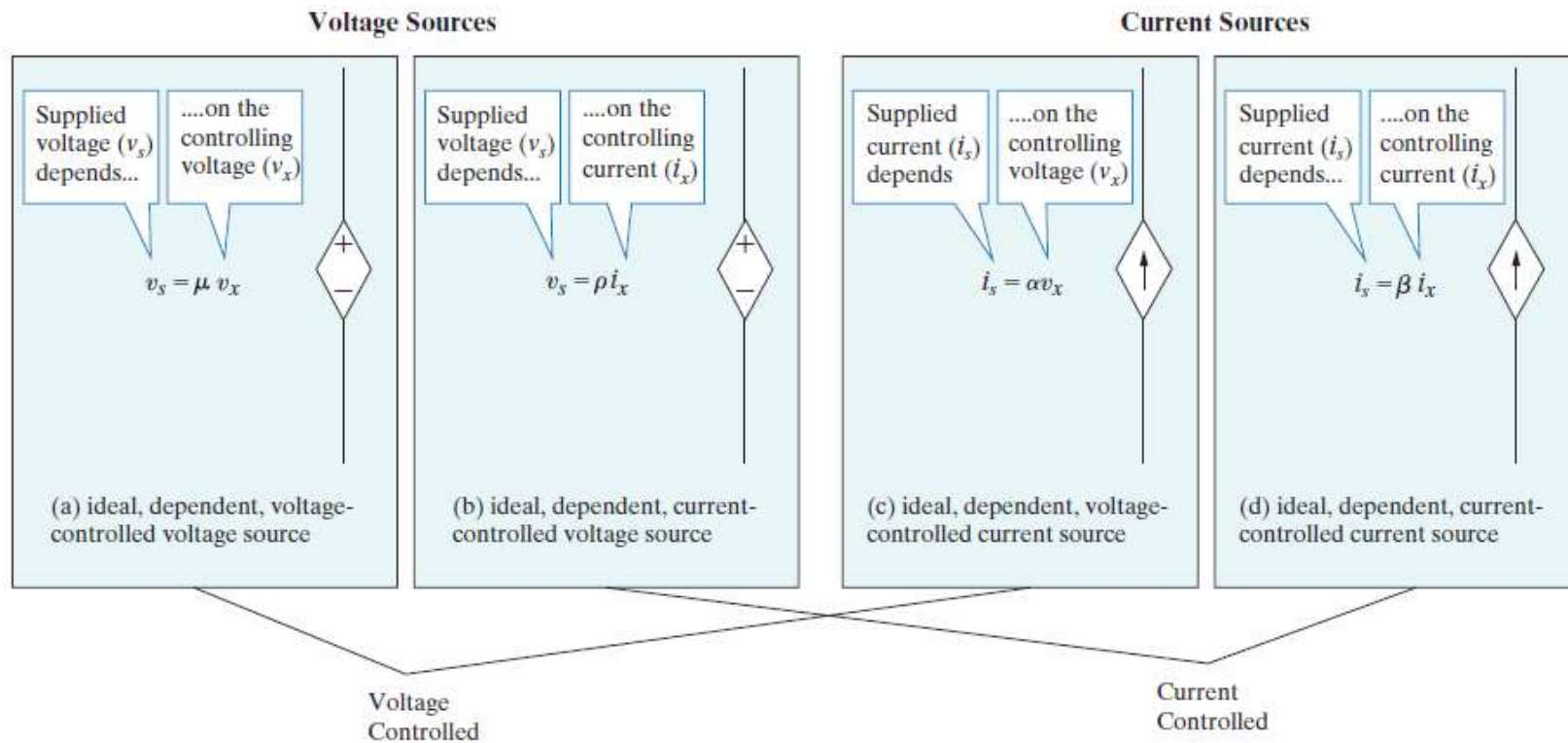


(a)

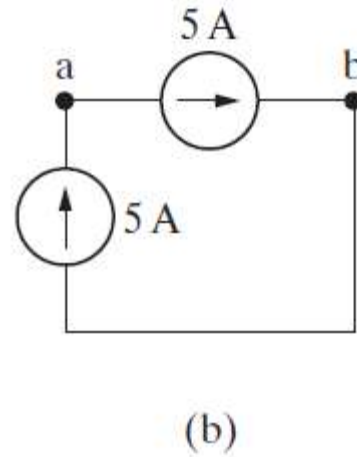
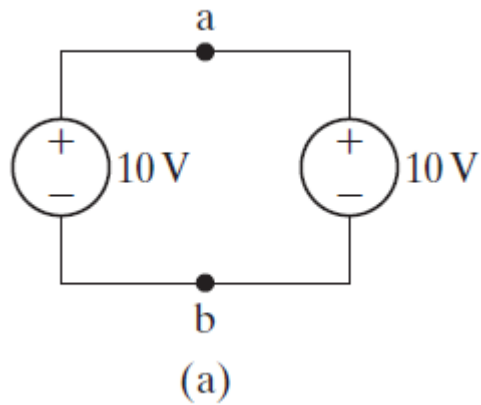


(b)

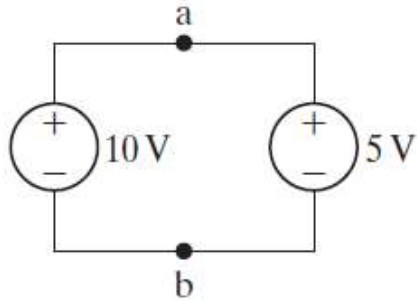
# (a) (b) Circuit symbols for ideal dependent voltage sources and (c) (d) ideal dependent current sources.



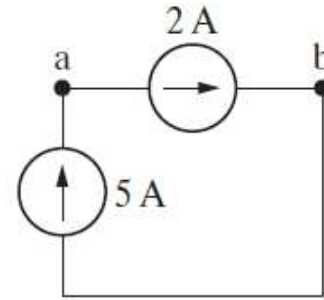
# Example 1 (1)



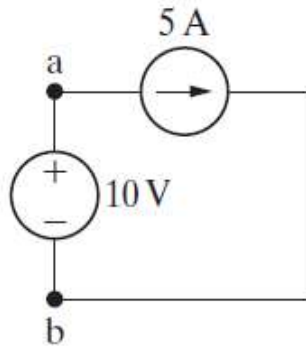
# Example 1(2)



(c)

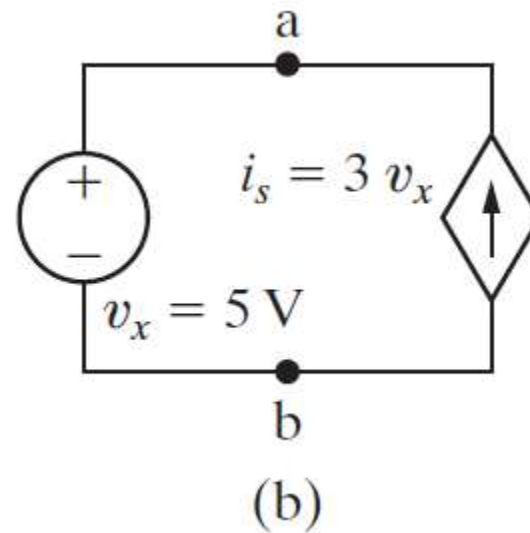
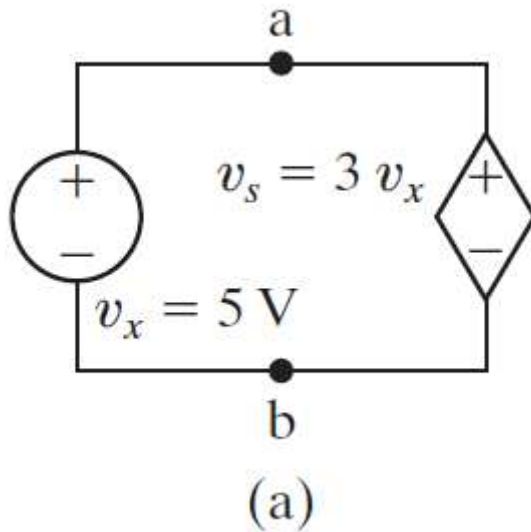


(d)

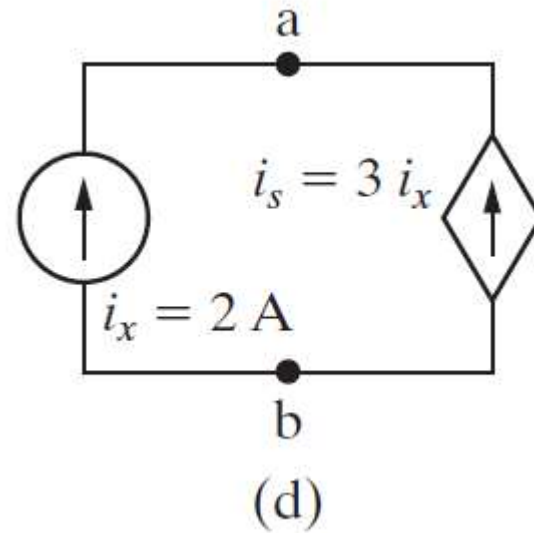
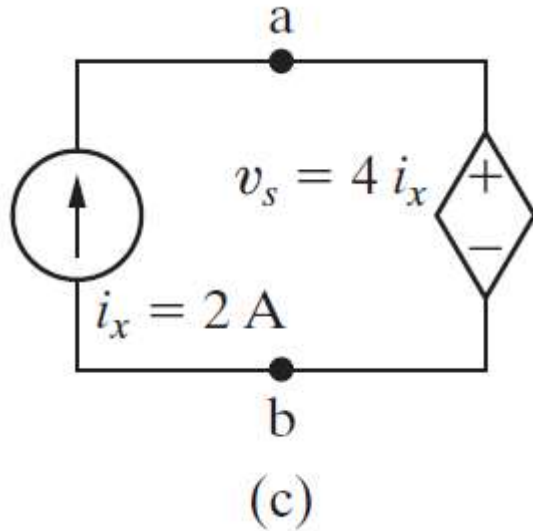


(e)

## Example 2 (1 of 2)



## Example 2 (2 of 2)



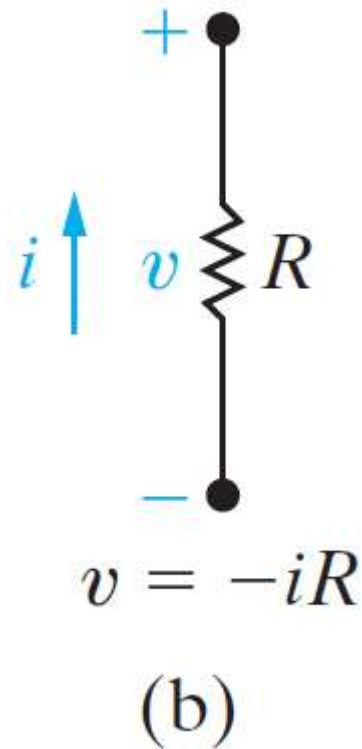
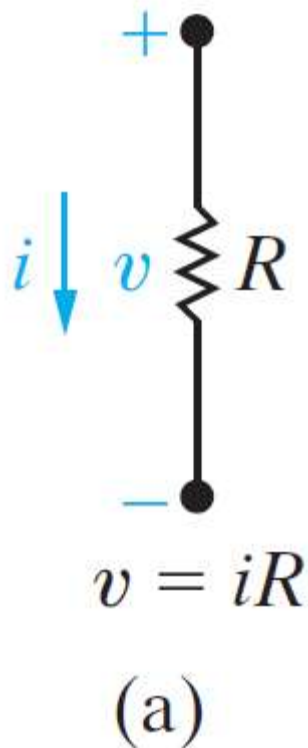
# Electrical Resistance



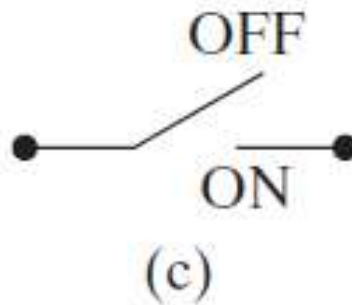
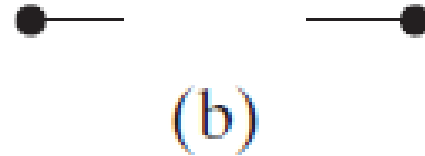
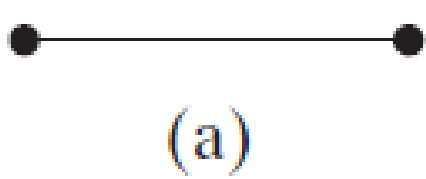


# OHM'S LAW

Two possible reference choices for the current and voltage at the terminals of a resistor and the resulting equations.



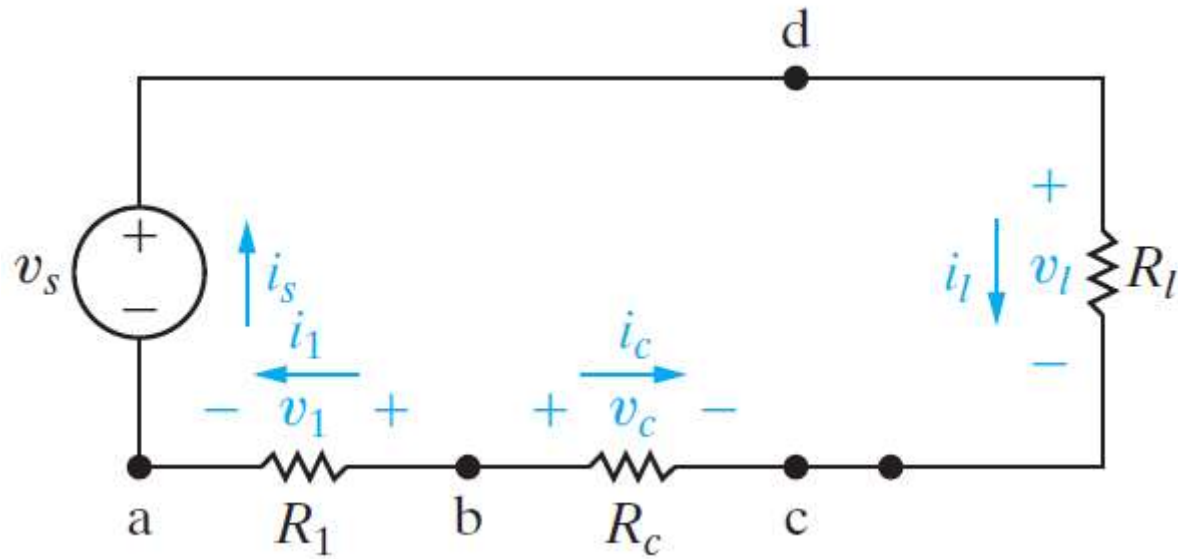
# Circuit symbols. (a) Short circuit. (b) Open circuit. (c) Switch.



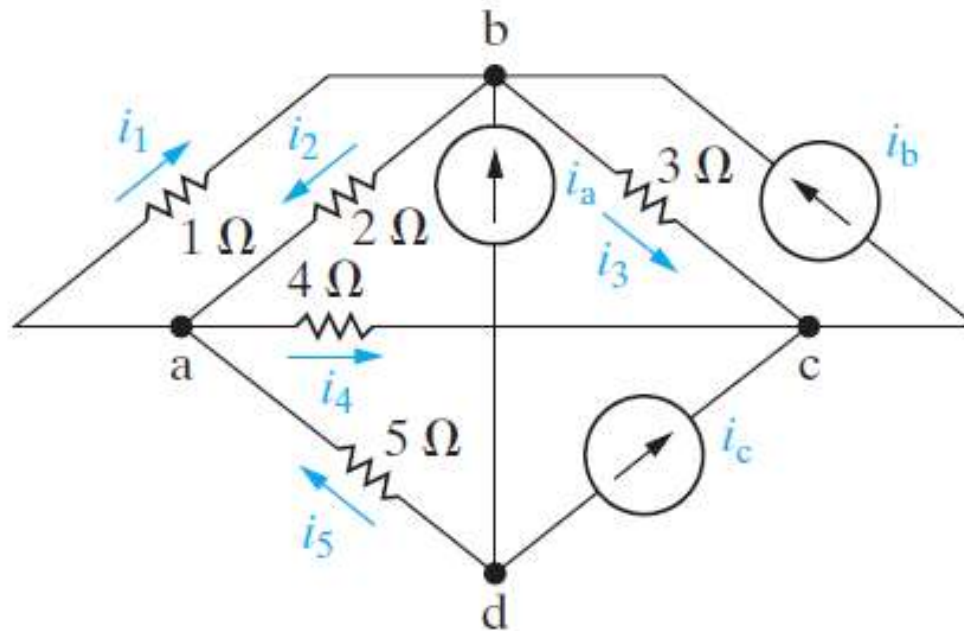
## Kirchhoff's current law:

- The algebraic sum of all the currents at any node in a circuit equals zero.

# KCL



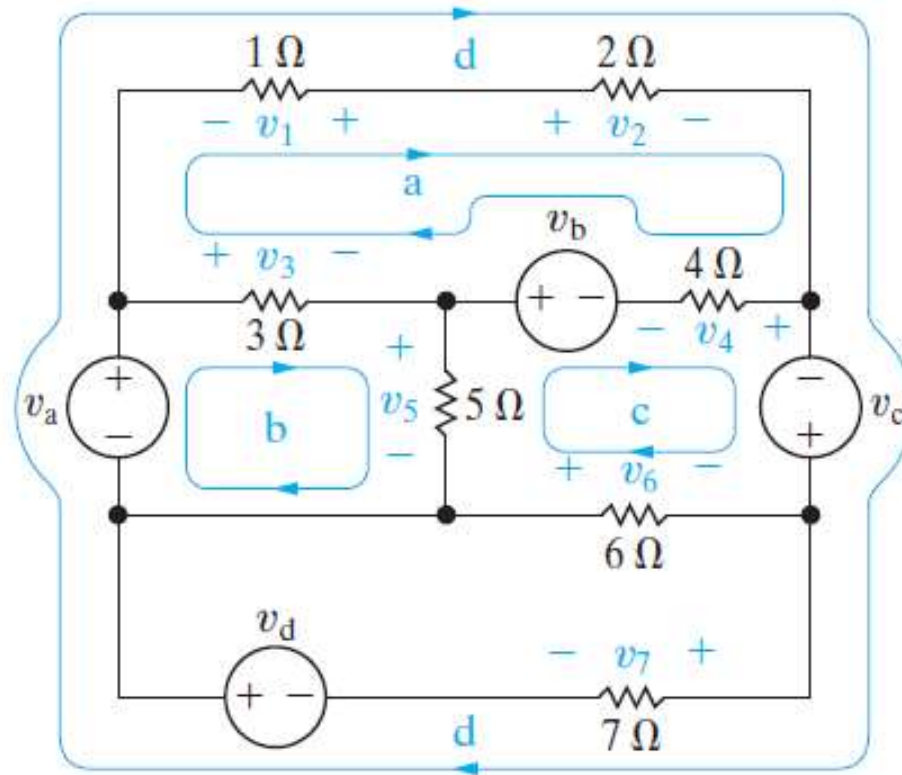
# Example (KCL)



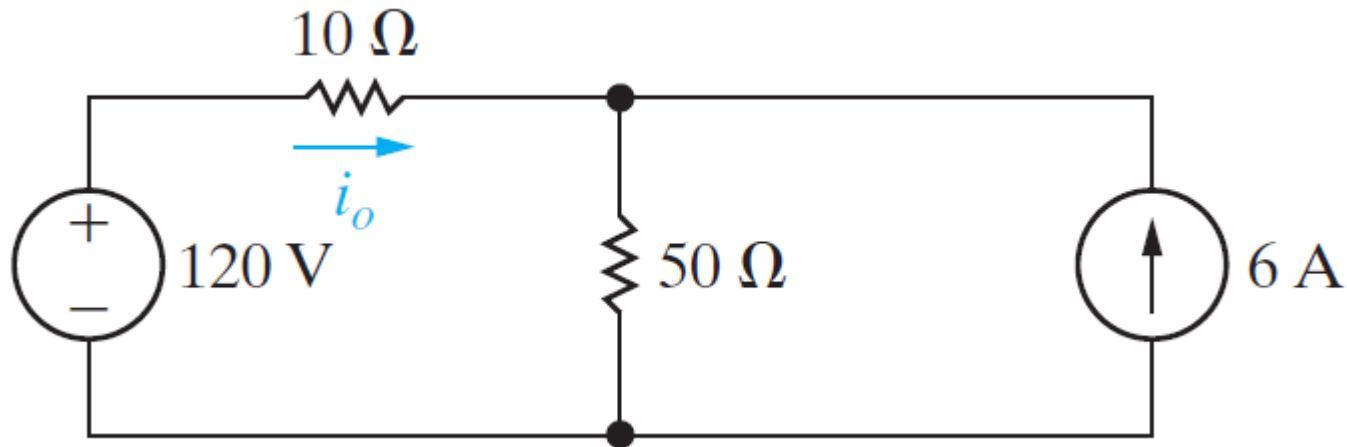
# Kirchhoff's voltage law

- The algebraic sum of all the voltages around any closed path in a circuit equals zero.

# Example (KVL)



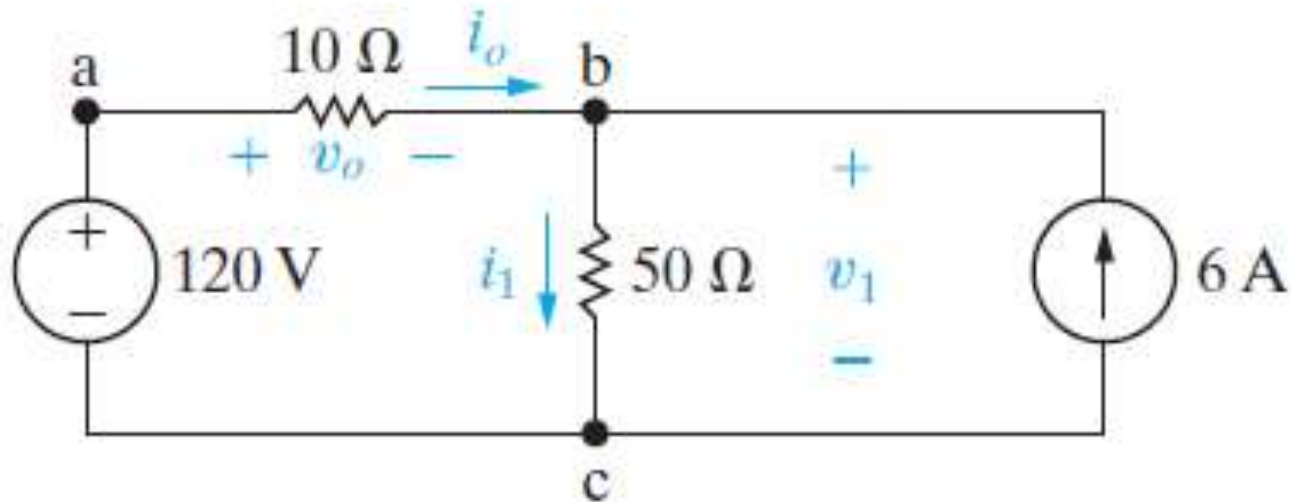
# Example



- a) Use Kirchhoff's laws and Ohm's law to find  $i_o$  in the circuit shown in Fig.
- b) Test the solution for by verifying that the total power generated equals the total power dissipated.



**Solution: The circuit shown in Fig. with the unknowns  $i_1$ ,  $v_o$ , and  $v_1$  defined.**



The power dissipated in the  $50\ \Omega$  resistor is

$$p_{50\Omega} = (3)^2(50) = 450\ \text{W}.$$

The power dissipated in the  $10\ \Omega$  resistor is

$$p_{10\Omega} = (-3)^2(10) = 90\ \text{W}.$$

The power delivered to the  $120\ \text{V}$  source is

$$p_{120\text{V}} = -120i_o = -120(-3) = 360\ \text{W}.$$

The power delivered to the  $6\ \text{A}$  source is

$$p_{6\text{A}} = -v_1(6), \quad \text{but} \quad v_1 = 50i_1 = 150\ \text{V}.$$

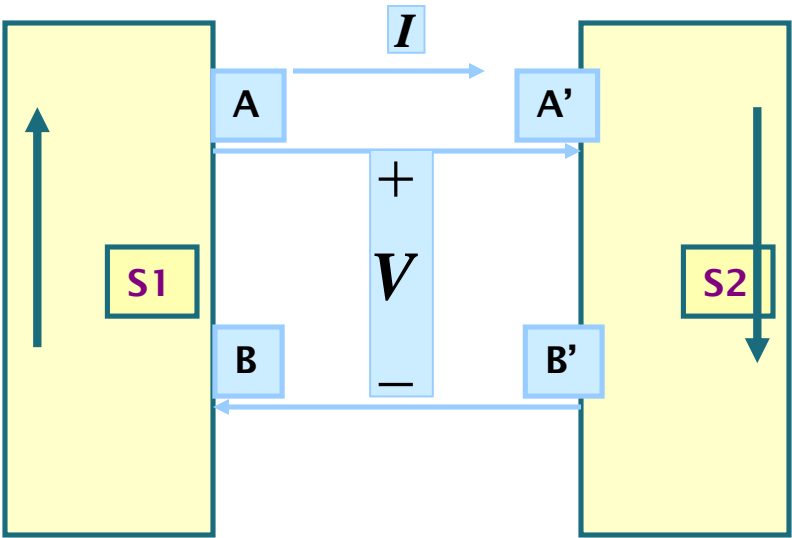
Therefore

$$p_{6\text{A}} = -150(6) = -900\ \text{W}.$$

The  $6\ \text{A}$  source is delivering  $900\ \text{W}$ , and the  $120\ \text{V}$  source is absorbing  $360\ \text{W}$ . The total power absorbed is  $360 + 450 + 90 = 900\ \text{W}$ . Therefore, the solution verifies that the power delivered equals the power absorbed.

# UNDERSTANDING PASSIVE SIGN CONVENTION

We must examine the voltage across the component and the current through it



$$P_{S1} = V_{AB} I_{AB}$$

$$P_{S2} = V_{A'B'} I_{A'B'}$$

Voltage(V)	Current A - A'	S1	S2
positive	positive	supplies	receives
positive	negative	receives	supplies
negative	positive	receives	supplies
negative	negative	supplies	receives

ON S<sub>1</sub>

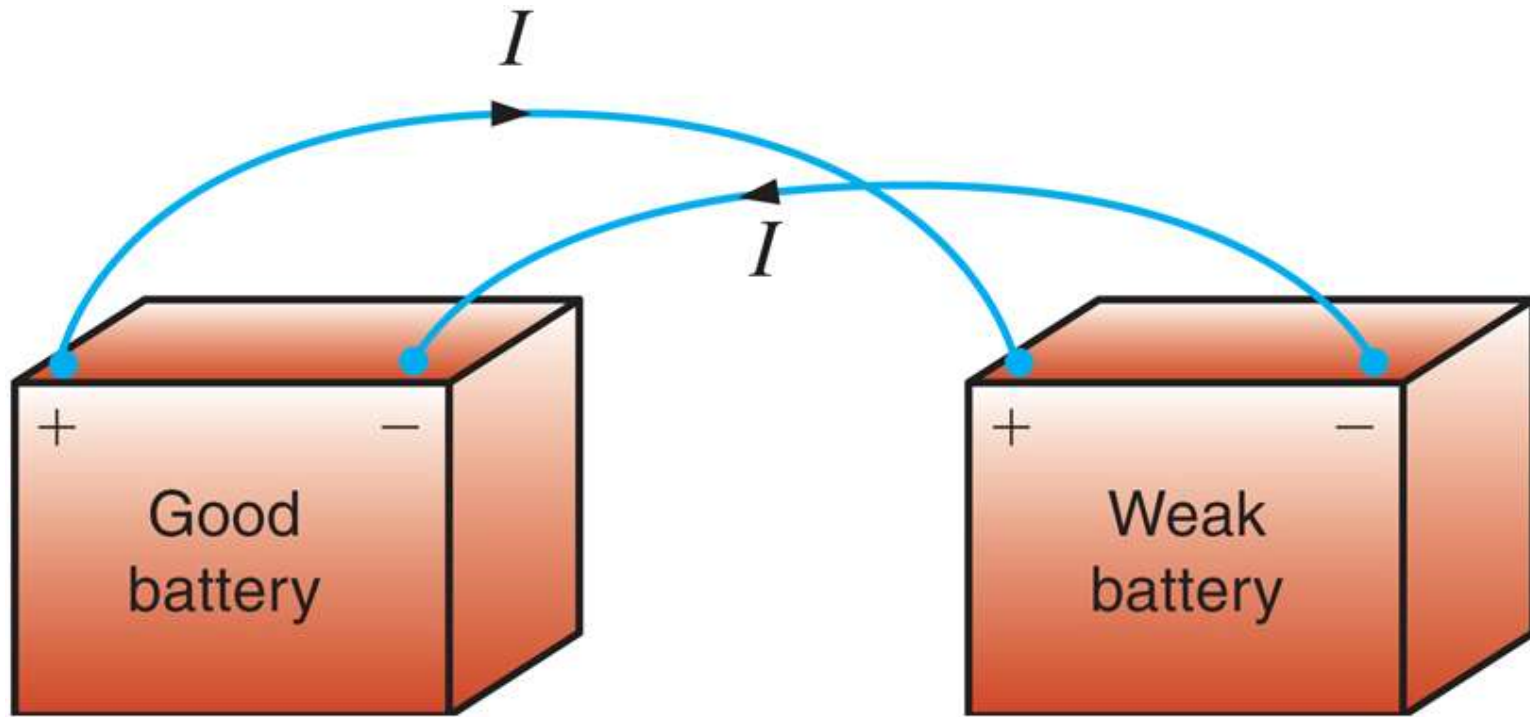
$$V_{AB} > 0, I_{AB} < 0$$

ON S<sub>2</sub>

$$V_{A'B'} > 0, I_{A'B'} > 0$$

ON S<sub>2</sub>

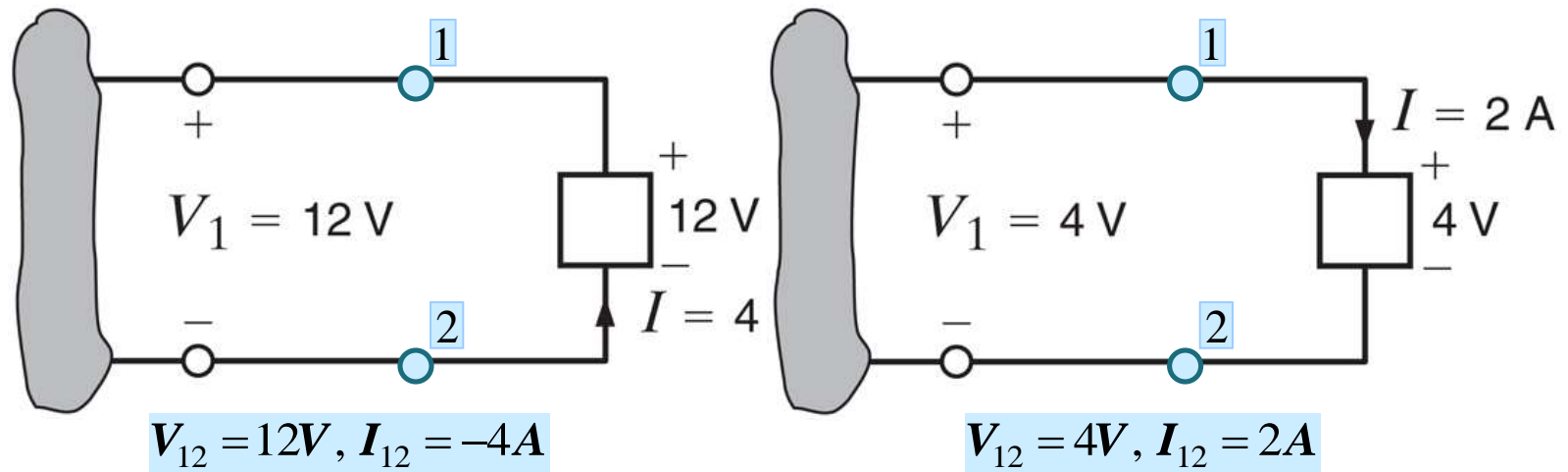
$$V_{A'B'} < 0, I_{A'B'} > 0$$

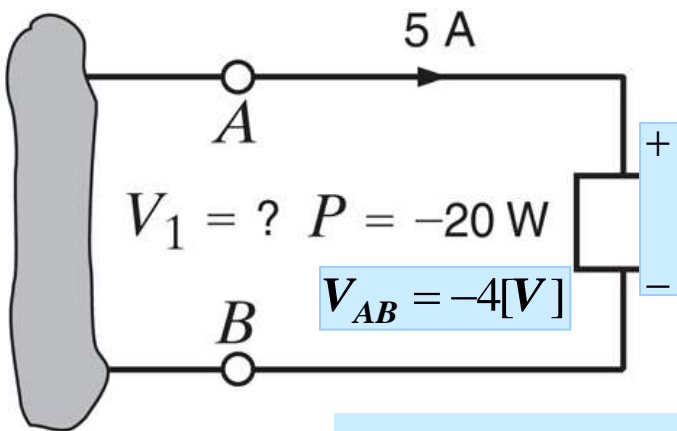


**CHARGES RECEIVE ENERGY.  
THIS BATTERY SUPPLIES ENERGY**

**CHARGES LOSE ENERGY.  
THIS BATTERY RECEIVES THE ENERGY**

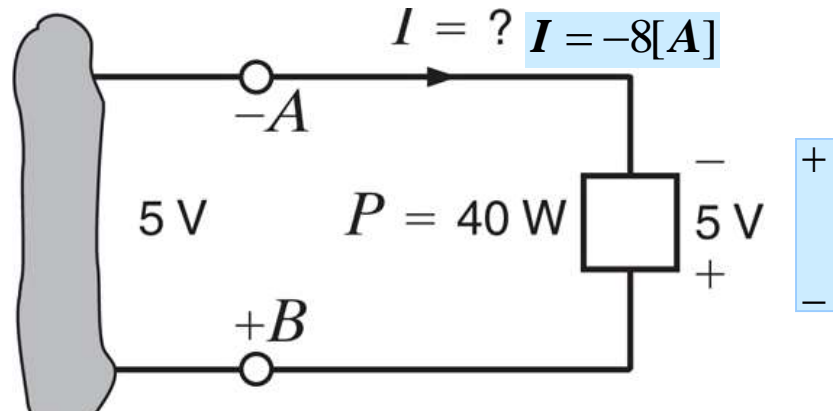
WHEN IN DOUBT LABEL THE TERMINALS  
OF THE COMPONENT



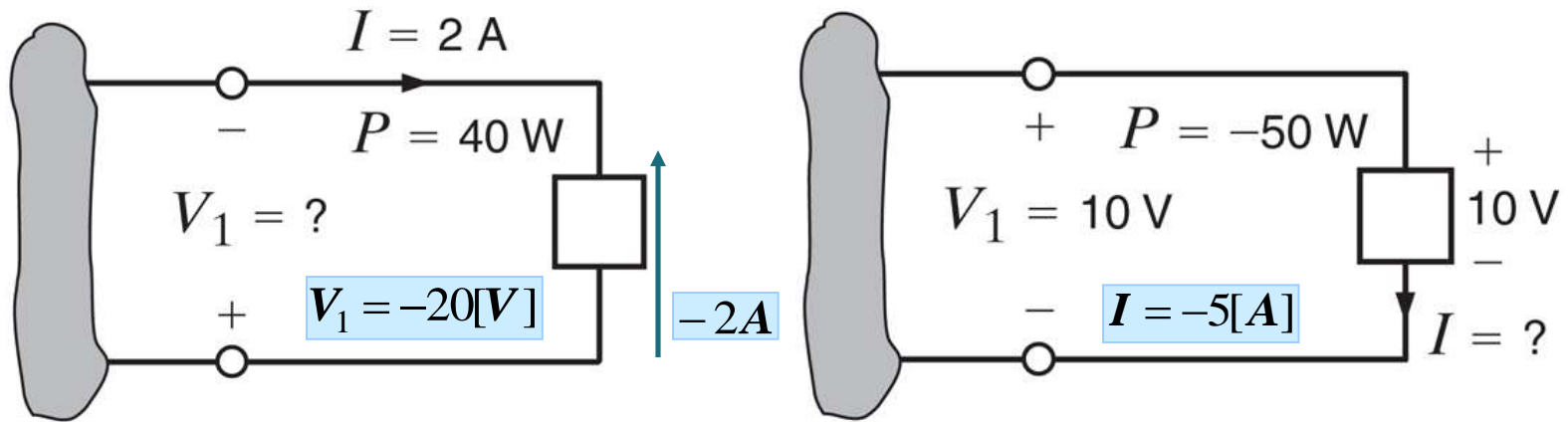


$$-20 \text{ [W]} = V_{AB} \times (5 \text{ A})$$

**SELECT VOLTAGE REFERENCE POLARITY  
BASED ON CURRENT REFERENCE DIRECTION**



$$40 \text{ [W]} = (-5 \text{ V}) \times I$$



$$40[\text{W}] = V_1 \times (-2\text{A})$$

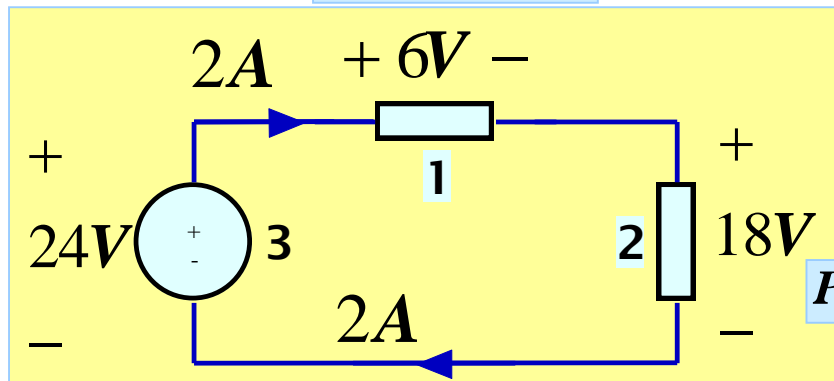
SELECT HERE THE CURRENT REFERENCE DIRECTION  
BASED ON VOLTAGE REFERENCE POLARITY

$$-50[\text{W}] = (10[\text{V}]) \times I$$

WHICH TERMINAL HAS HIGHER VOLTAGE AND WHICH IS THE CURRENT FLOW DIRECTION

COMPUTE POWER ABDORBED OR SUPPLIED BY EACH ELEMENT

$$P_1 = (6V)(2A)$$



$$\begin{aligned} P_1 &= 12W \\ P_2 &= 36W \\ P_3 &= -48W \end{aligned}$$

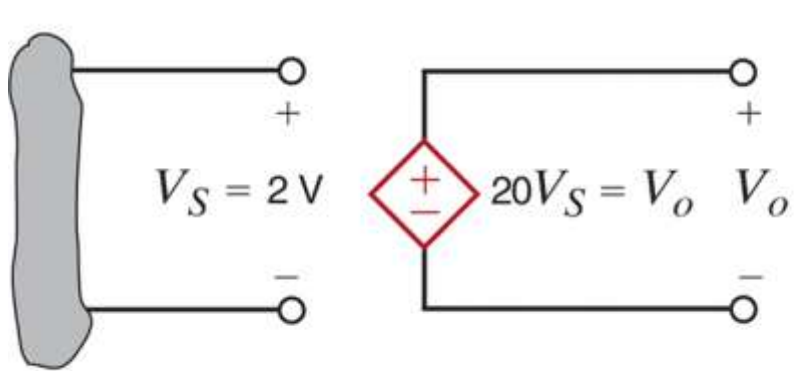
$$P_2 = (18V)(2A)$$

$$P_3 = (24V)(-2A) = (-24V)(2A)$$

IMPORTANT: NOTICE THE POWER BALANCE IN THE CIRCUIT



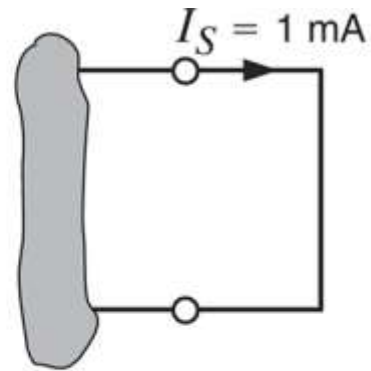
## EXERCISES WITH DEPENDENT SOURCES



**FIND  $V_o$**

(a)

**$V_o = 40[\text{V}]$**

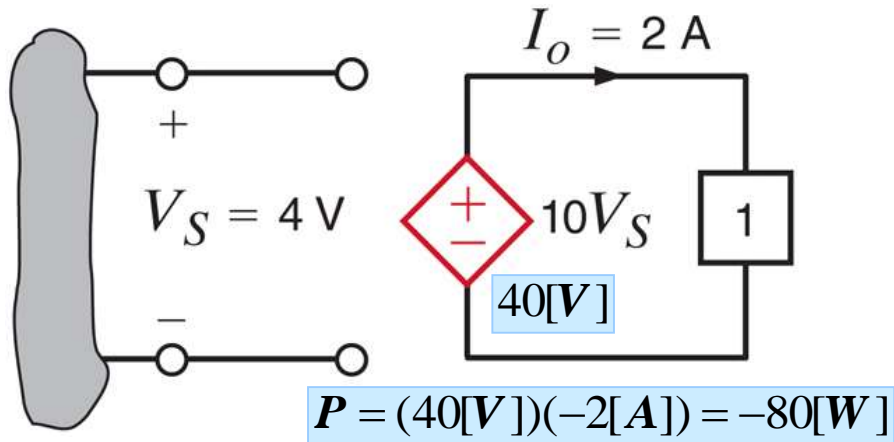


**FIND  $I_o$**

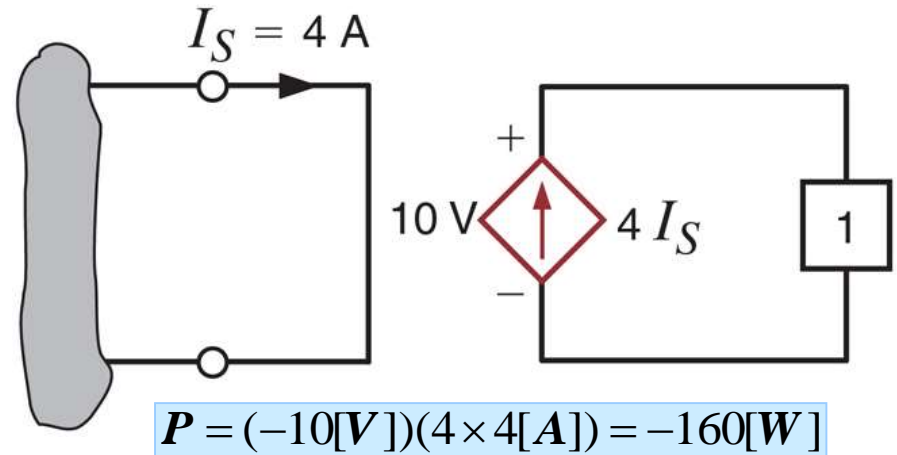
(b)

**$I_o = 50\text{ mA}$**

DETERMINE THE POWER SUPPLIED BY THE DEPENDENT SOURCES

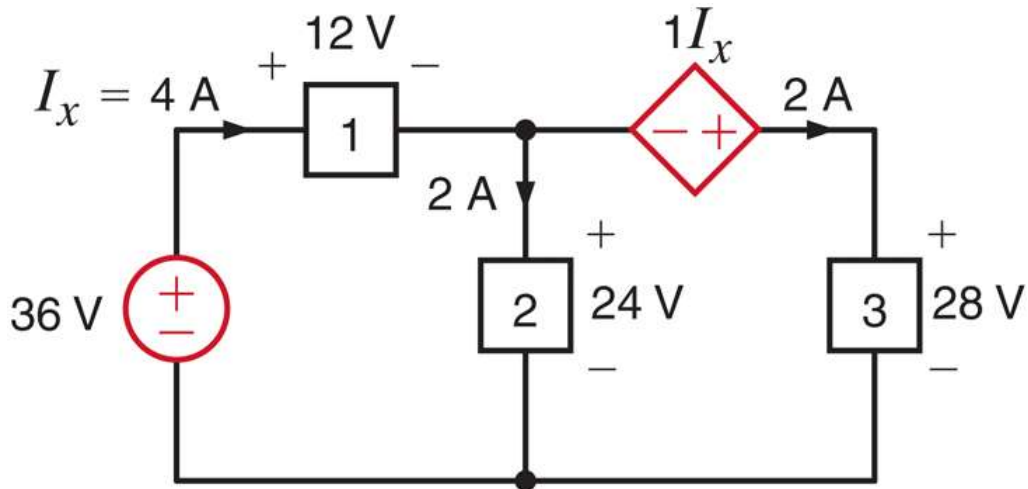


TAKE VOLTAGE POLARITY REFERENCE



TAKE CURRENT REFERENCE DIRECTION

## POWER ABSORBED OR SUPPLIED BY EACH ELEMENT



$$P_1 = (12\text{V})(4\text{A}) = 48[\text{W}]$$

$$P_2 = (24\text{V})(2\text{A}) = 48[\text{W}]$$

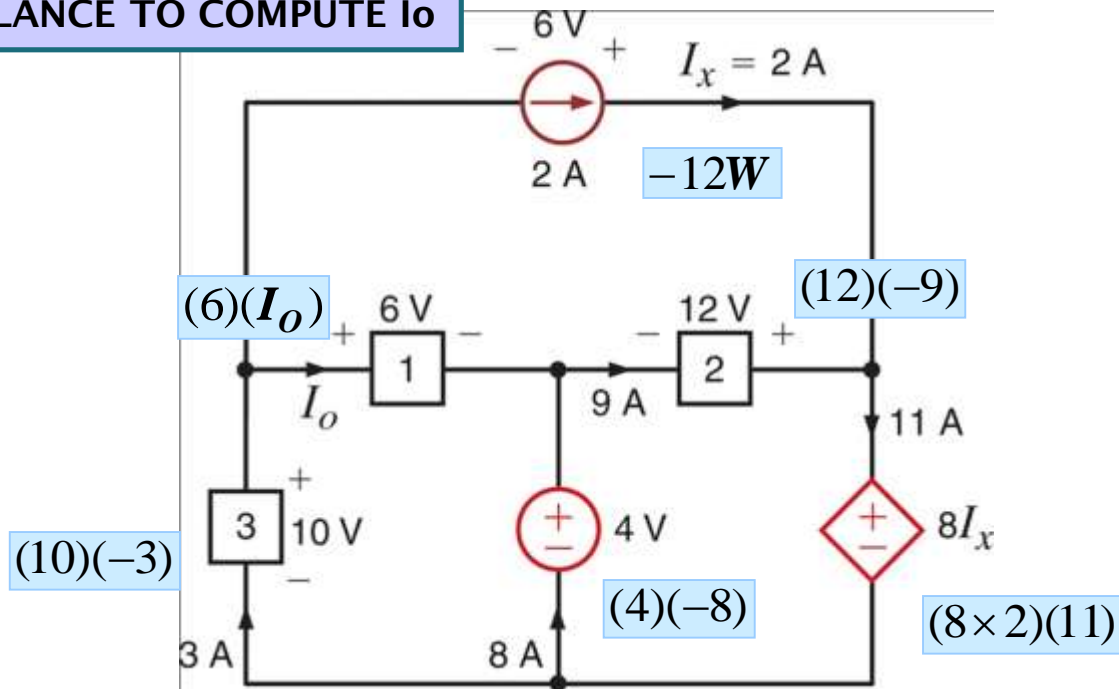
$$P_3 = (28\text{V})(2\text{A}) = 56[\text{W}]$$

$$P_{DS} = (1I_x)(-2\text{A}) = (4\text{V})(-2\text{A}) = -8[\text{W}]$$

$$P_{36\text{V}} = (36\text{V})(-4\text{A}) = -144[\text{W}]$$

NOTICE THE POWER BALANCE

# USE POWER BALANCE TO COMPUTE $I_o$



$$P_{2A} = (6)(-2) = -12 \text{ W}$$

$$P_1 = (6)(I_o) = 6I_o \text{ W}$$

$$P_2 = (12)(-9) = -108 \text{ W}$$

$$P_3 = (10)(-3) = -30 \text{ W}$$

$$P_{4V} = (4)(-8) = -32 \text{ W}$$

$$P_{DS} = (8I_x)(11) = (16)(11) = 176 \text{ W}$$

## POWER BALANCE

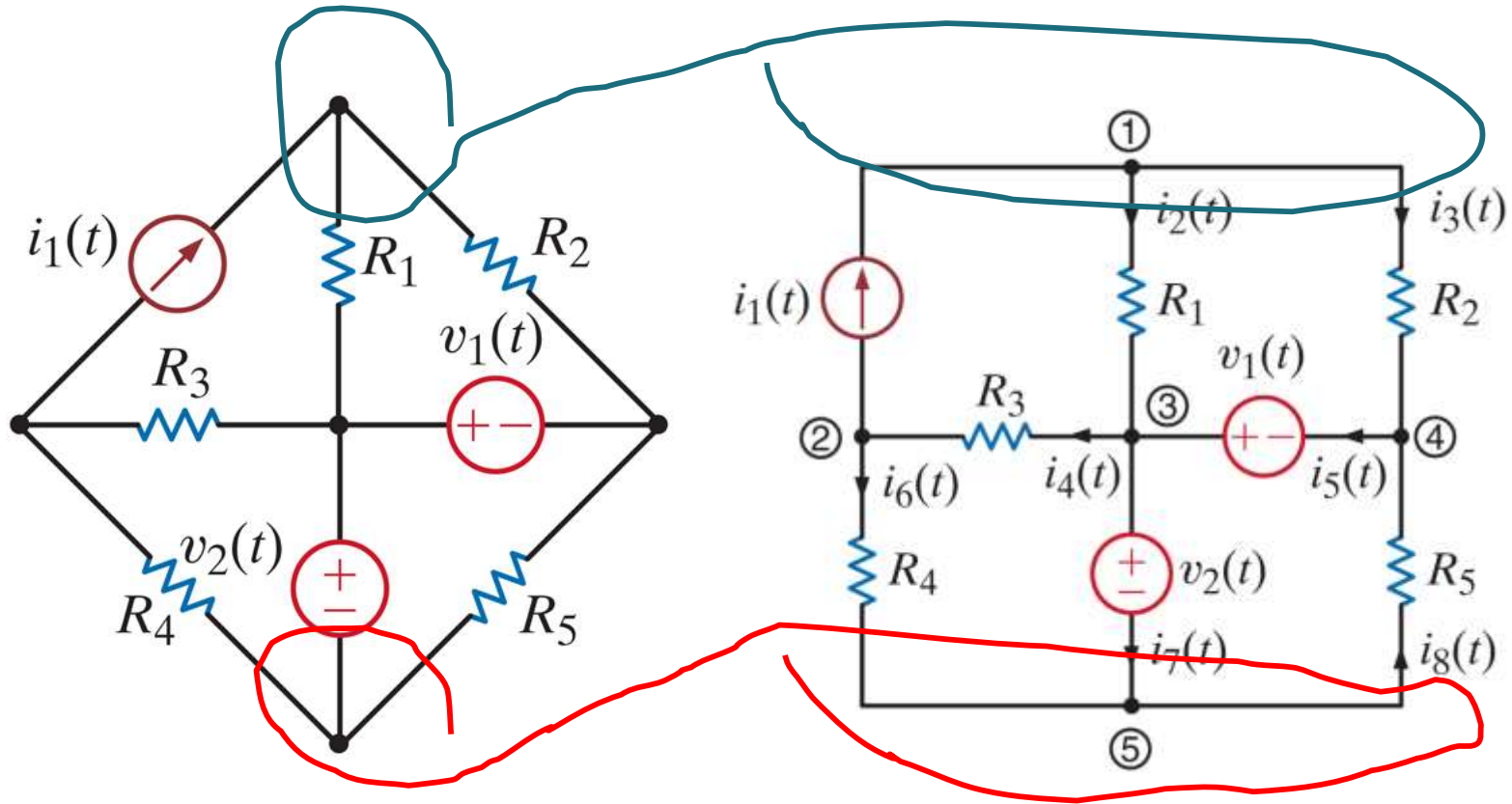
$$-12 + 6I_o - 108 - 30 - 32 + 176 = 0$$

$$I_o = 1[A]$$

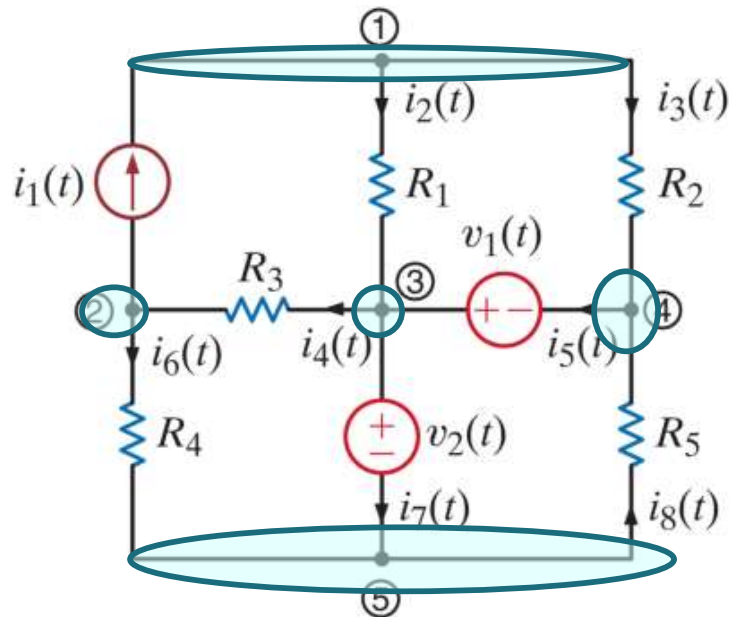
# KCL

- **SUM OF CURRENTS FLOWING INTO A NODE IS EQUAL TO SUM OF CURRENTS FLOWING OUT OF THE NODE**

A node is a point of connection of two or more circuit elements.  
It may be stretched out or compressed for visual purposes..  
But it is still a node



WRITE ALL KCL EQUATIONS



$$-i_1(t) + i_2(t) + i_3(t) = 0$$

$$i_1(t) - i_4(t) + i_6(t) = 0$$

$$-i_2(t) + i_4(t) - i_5(t) + i_7(t) = 0$$

$$-i_3(t) + i_5(t) - i_8(t) = 0$$

$$-i_6(t) - i_7(t) + i_8(t) = 0$$

THE FIFTH EQUATION IS THE SUM OF THE FIRST FOUR... IT IS REDUNDANT!!!

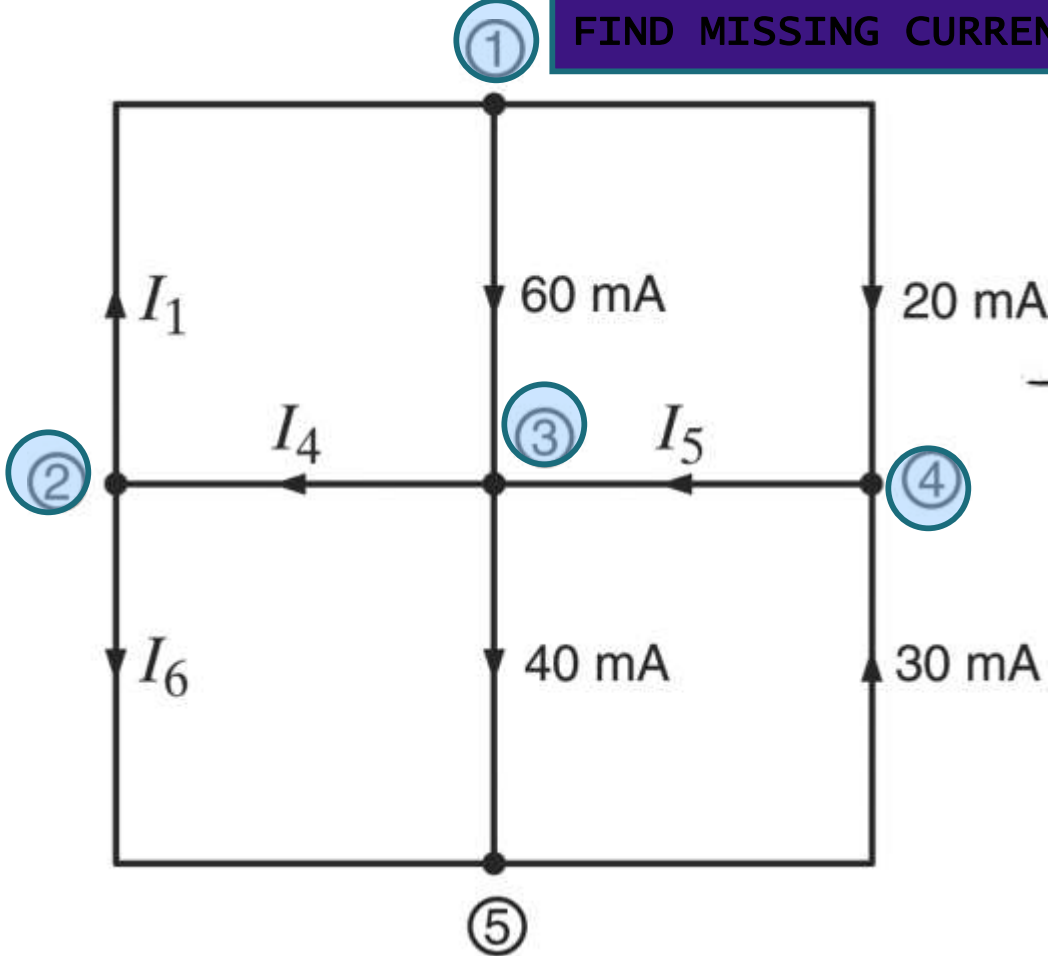
# FIND MISSING CURRENTS

$$-I_1 + 0.06 + 0.02 = 0$$

$$I_1 - I_4 + \underline{I_6} = 0$$

$$-0.06 + \underline{I_4} - I_5 + 0.04 = 0$$

$$-0.02 + \underline{I_5} - 0.03 = 0$$

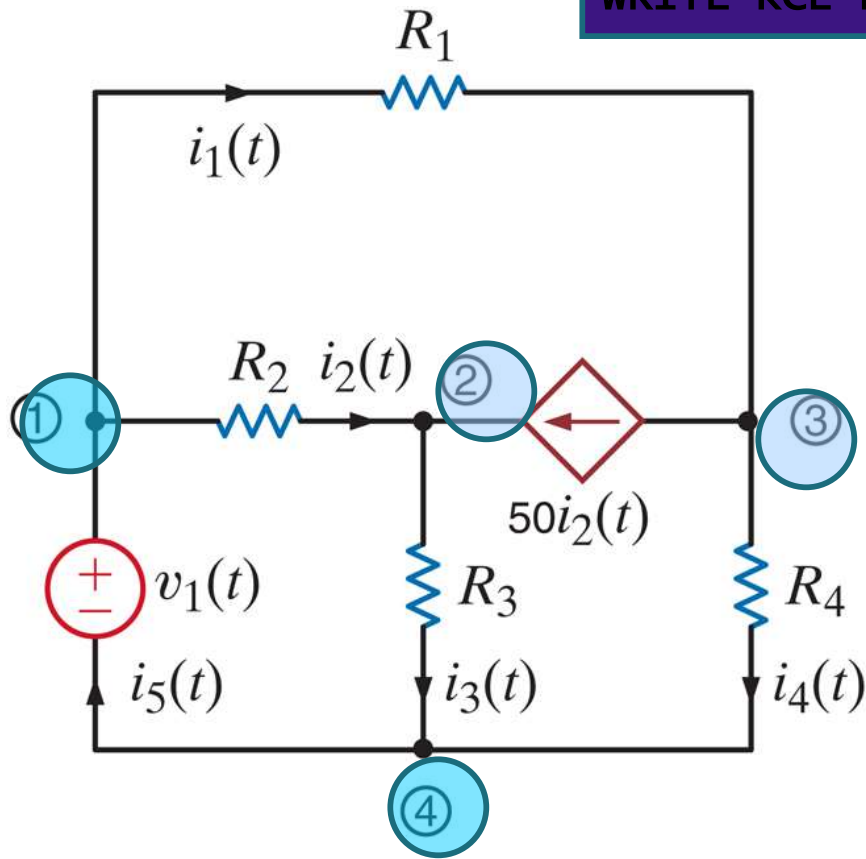


KCL DEPENDS ONLY ON THE INTERCONNECTION.  
THE TYPE OF COMPONENT IS IRRELEVANT

KCL DEPENDS ONLY ON THE TOPOLOGY OF THE CIRCUIT



WRITE KCL EQUATIONS FOR THIS CIRCUIT



$$i_1(t) + i_2(t) - i_5(t) = 0$$

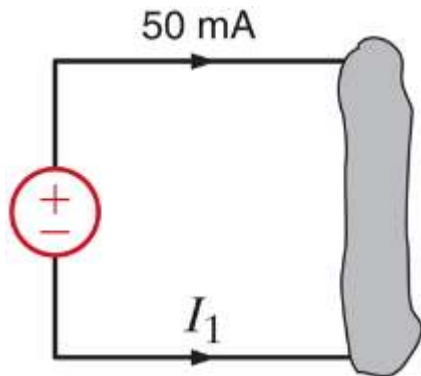
$$-i_2(t) + i_3(t) - 50i_2(t) = 0$$

$$-i_1(t) + 50i_2(t) + i_4(t) = 0$$

$$i_5(t) - i_3(t) - i_4(t) = 0$$

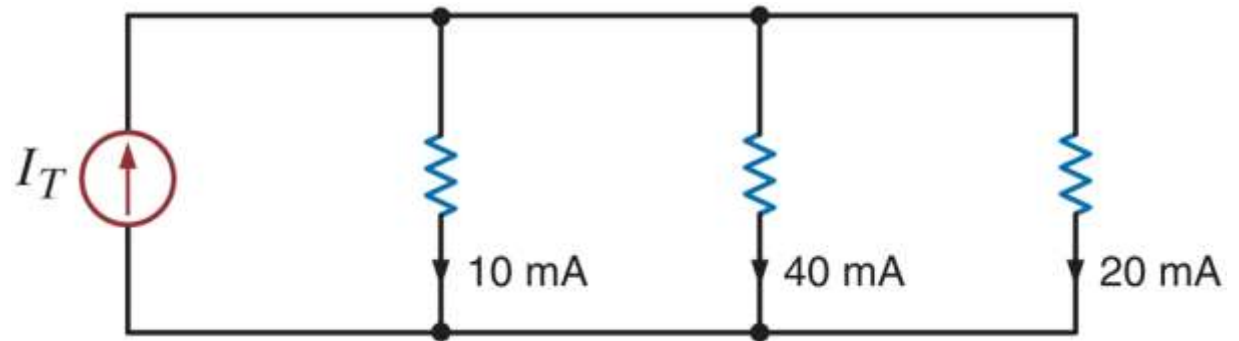
- THE LAST EQUATION IS AGAIN LINEARLY DEPENDENT OF THE PREVIOUS THREE
- THE PRESENCE OF A DEPENDENT SOURCE DOES NOT AFFECT APPLICATION OF KCL  
KCL DEPENDS ONLY ON THE TOPOLOGY

Find  $I_1$



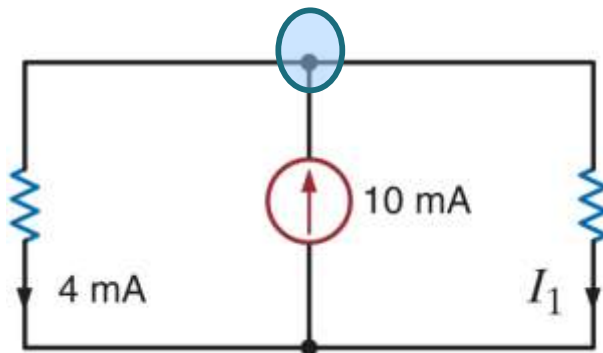
$$I_1 = -50 \text{ mA}$$

Find  $I_T$



$$I_T = 10 \text{ mA} + 40 \text{ mA} + 20 \text{ mA}$$

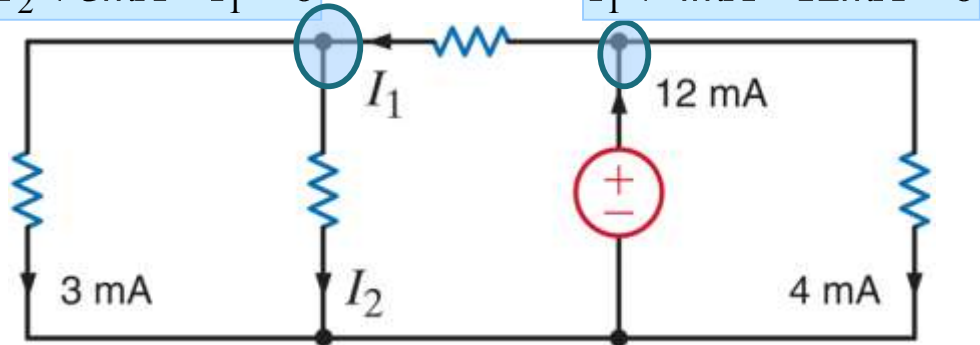
Find  $I_1$



$$10 \text{ mA} - 4 \text{ mA} - I_1 = 0$$

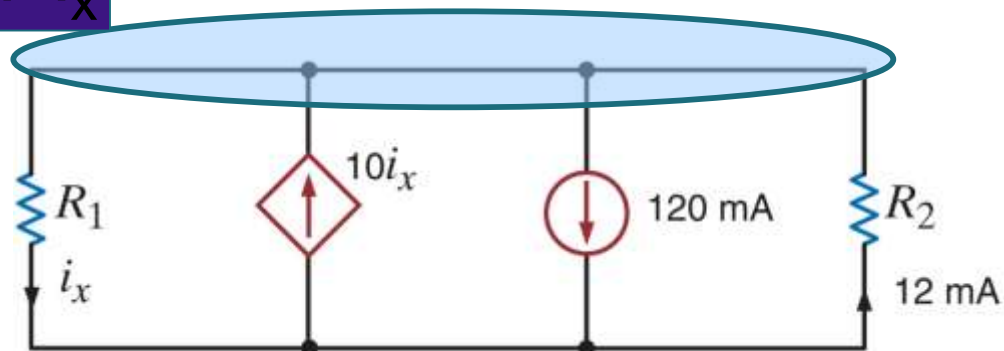
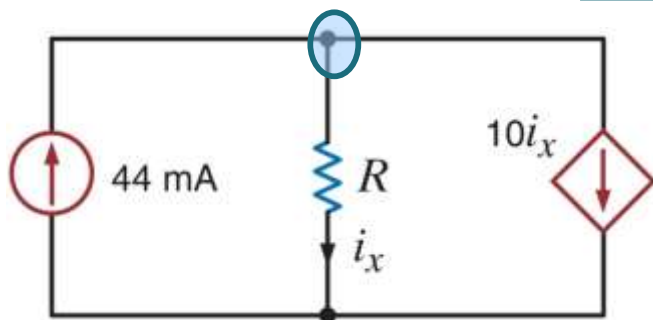
Find  $I_1$  and  $I_2$

$$I_2 + 3 \text{ mA} - I_1 = 0$$



$$I_1 + 4 \text{ mA} - 12 \text{ mA} = 0$$

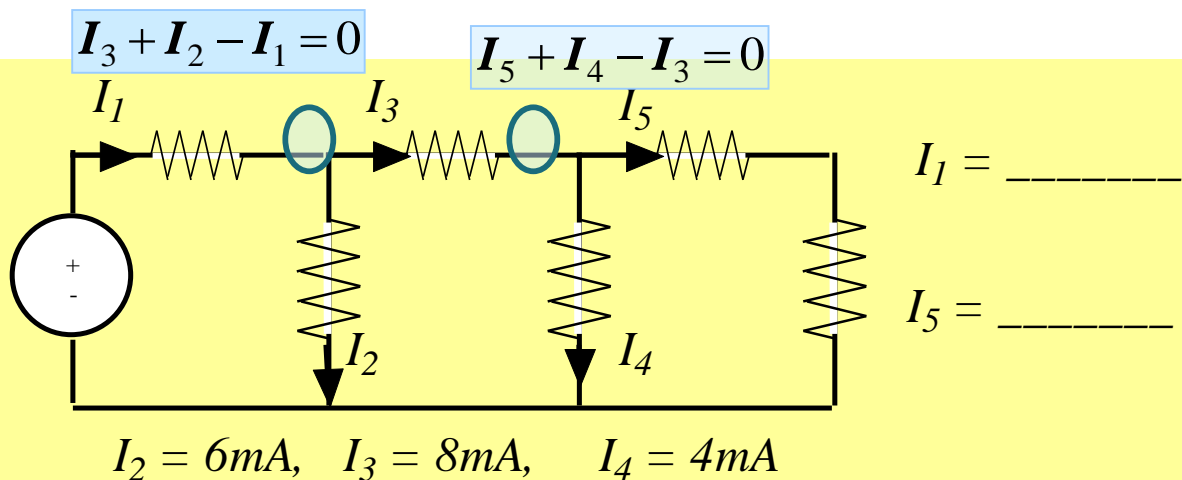
Find  $i_x$



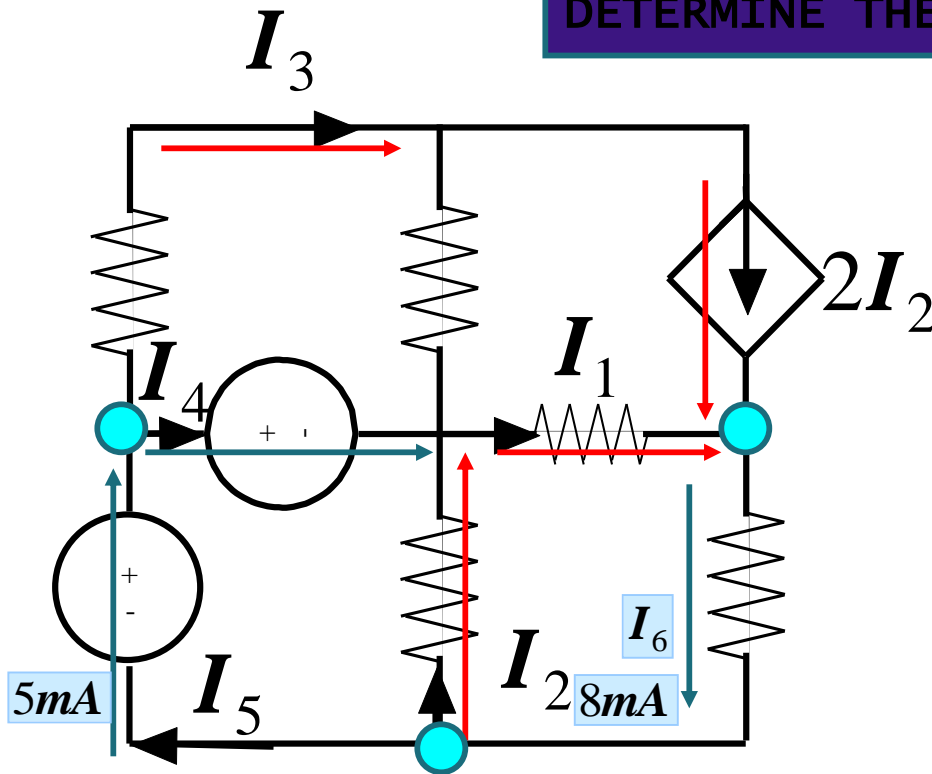
$$10i_x + i_x - 44\text{mA} = 0$$

$$i_x = 4\text{mA}$$

$$i_x - 10i_x + 120\text{mA} - 12\text{mA} = 0$$



DETERMINE THE CURRENTS INDICATED



$$I_4 = 2mA$$

$$I_5 = 5mA$$

THE PLAN

MARK ALL THE KNOWN CURRENTS

FIND NODES WHERE ALL BUT ONE CURRENT ARE KNOWN

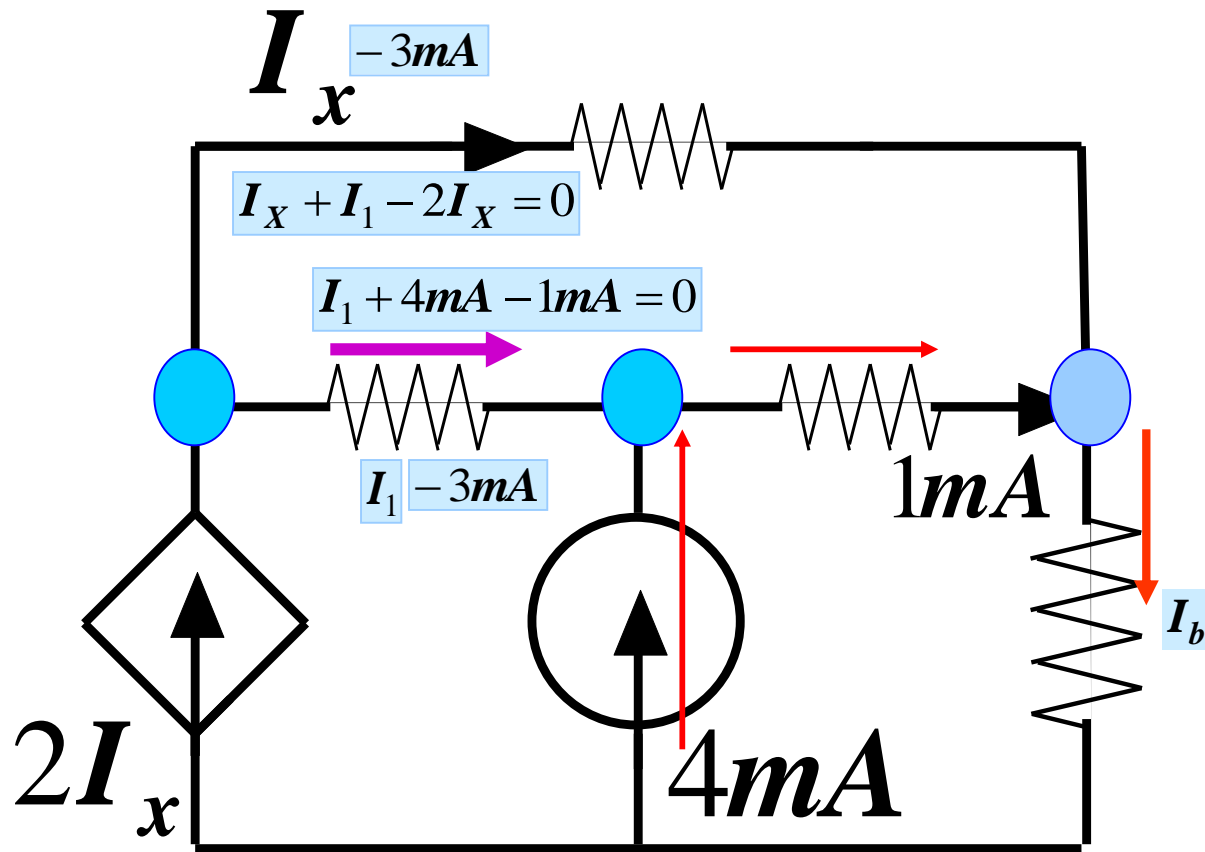
$$I_1 = 2mA, I_2 = 3mA, I_3 = 5mA$$

$$I_6 - I_1 - 2I_2 = 0 \Rightarrow I_6 = 8mA$$

$$I_5 + I_2 - I_6 = 0$$

$$I_4 + I_3 - I_5 = 0$$

# FIND $I_x$



## VERIFICATION

$$I_b = 1mA + I_x = -2mA$$

$$2I_x + 4mA = I_b$$

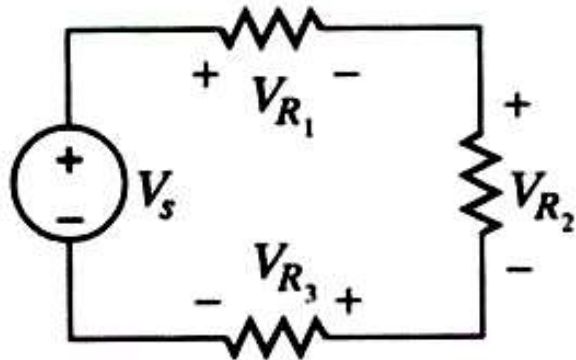
# KVL

**KVL: THE ALGEBRAIC SUM OF VOLTAGE DROPS AROUND ANY LOOP MUST BE ZERO**

$$\begin{array}{c} \bigcirc \\ A \end{array} - V + \begin{array}{c} \bigcirc \\ B \end{array} \equiv \begin{array}{c} \bigcirc \\ A \end{array} + (-V) - \begin{array}{c} \bigcirc \\ B \end{array}$$

A VOLTAGE RISE IS  
A NEGATIVE DROP

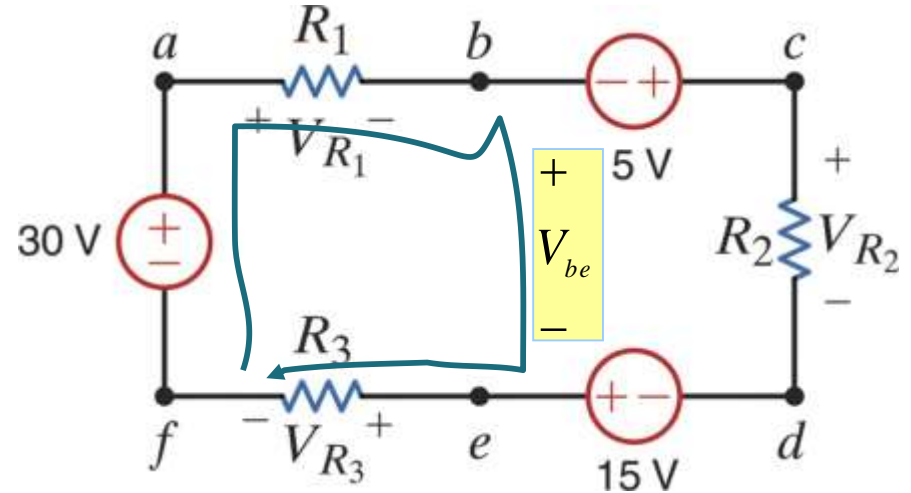
Write the KVL equation for the following loop, traveling clockwise:



$$-V_S + V_{R_1} + V_{R_2} + V_{R_3} = 0$$

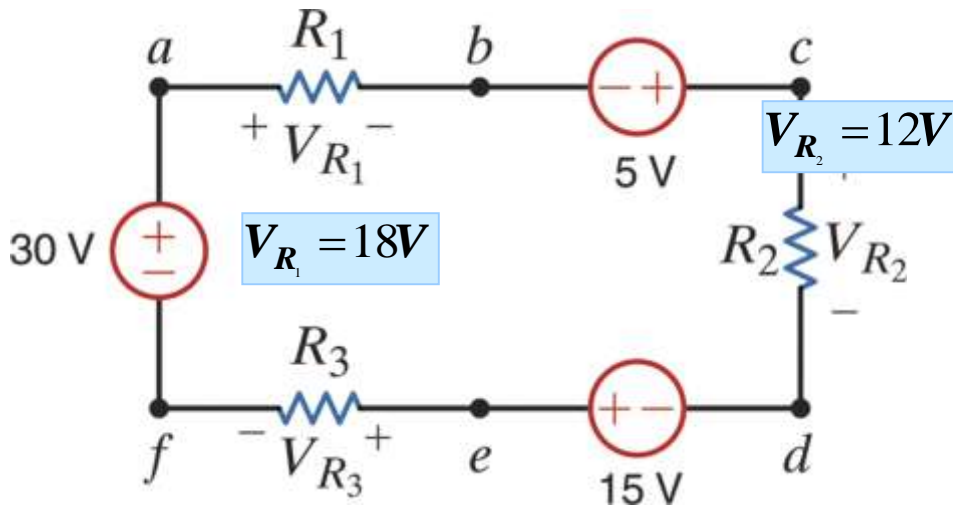
PROBLEM SOLVING TIP: KVL IS USEFUL TO DETERMINE A VOLTAGE - FIND A LOOP INCLUDING THE UNKNOWN VOLTAGE

THE LOOP DOES NOT HAVE TO BE PHYSICAL



EXAMPLE:  $V_{R_1}$ ,  $V_{R_3}$  ARE KNOWN  
DETERMINE THE VOLTAGE  $V_{be}$

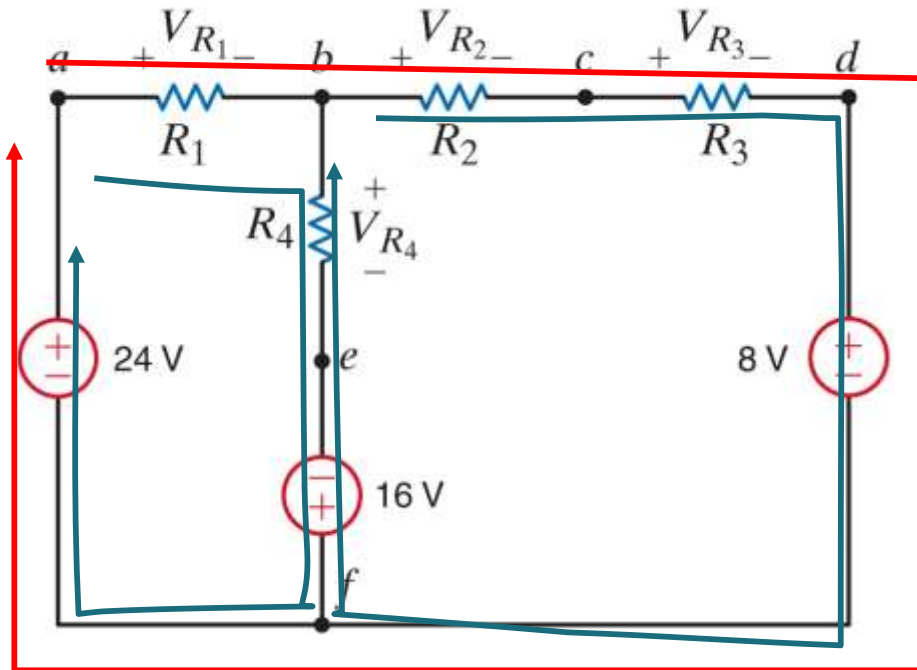
$$V_{R_1} + V_{be} + V_{R_3} - 30[V] = 0$$



LOOP *abcdefa*

$$+V_{R_1} - 5 + V_{R_2} - 15 + V_{R_3} - 30 = 0$$

BACKGROUND: WHEN DISCUSSING KCL WE SAW THAT NOT ALL POSSIBLE KCL EQUATIONS ARE INDEPENDENT. WE SHALL SEE THAT THE SAME SITUATION ARISES WHEN USING KVL



$$V_{R_1} + V_{R_4} - 16 - 24 = 0$$

$$V_{R_2} + V_{R_3} + 8 + 16 - V_{R_4} = 0$$

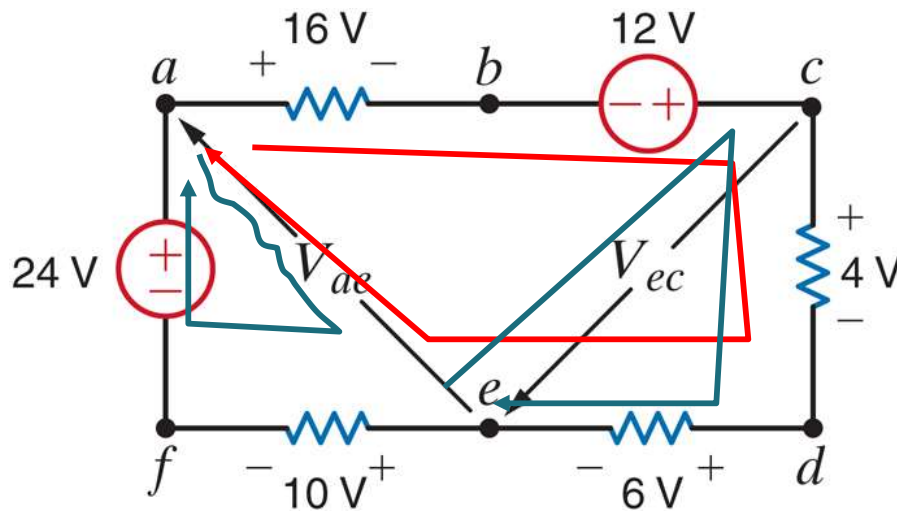
$$V_{R_1} + V_{R_2} + V_{R_3} + 8 - 24 = 0$$

THE THIRD EQUATION IS THE SUM OF THE OTHER TWO!!





FIND THE VOLTAGES  $V_{ae}$ ,  $V_{ec}$



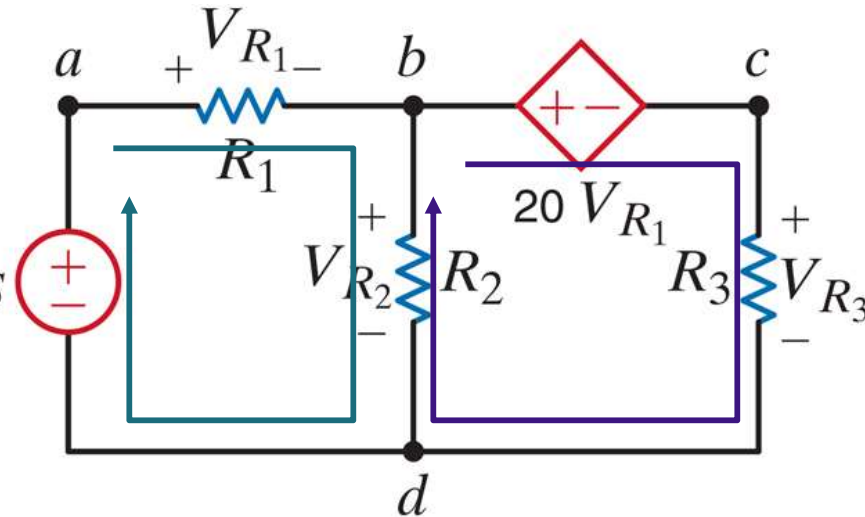
$$V_{ae} + 10 - 24 = 0$$

$$16 - 12 + 4 + 6 - V_{ae} = 0$$

GIVEN THE CHOICE USE THE SIMPLEST LOOP

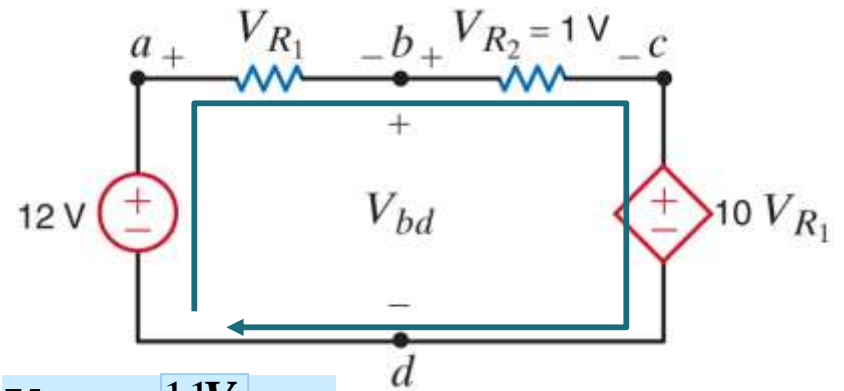
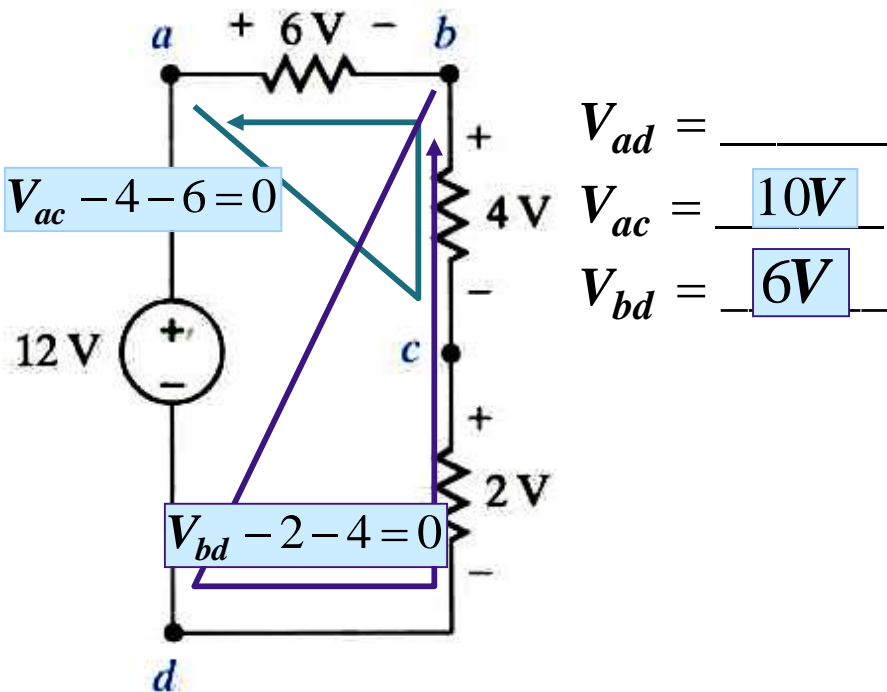
$$4 + 6 + V_{ec} = 0$$

DEPENDENT SOURCES ARE HANDLED WITH THE SAME EASE



$$V_{R_1} + V_{R_2} - V_S = 0$$

$$20V_{R_1} + V_{R_3} - V_{R_2} = 0$$

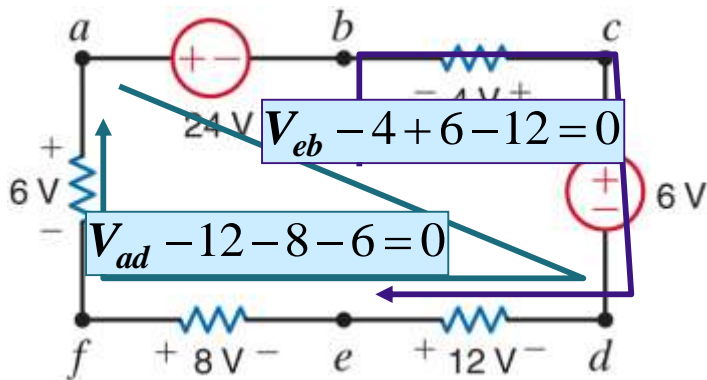


MUST FIND  $V_{R_1}$  FIRST

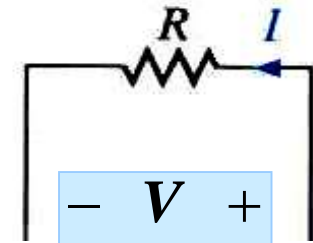
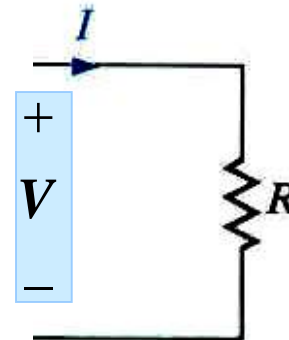
$$-12 + V_{R_1} + 1 + 10V_{R_1} = 0 \Rightarrow V_{R_1} = 1V$$

DEPENDENT SOURCES ARE NOT REALLY DIFFICULT TO ANALYZE

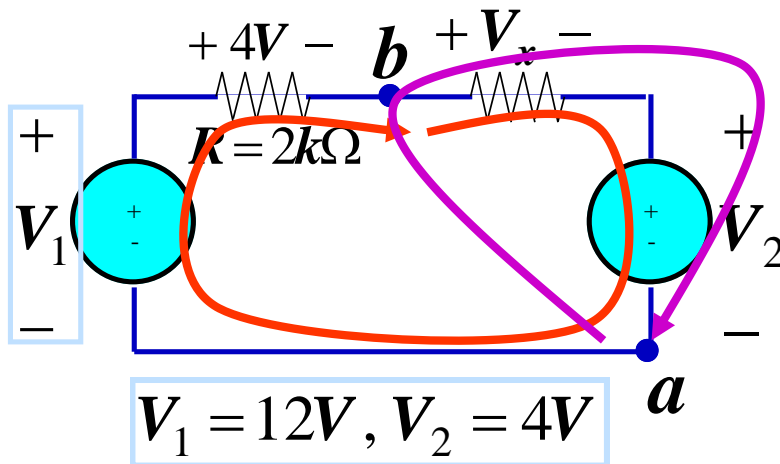
REMINDER: IN A RESISTOR THE VOLTAGE AND CURRENT DIRECTIONS MUST SATISFY THE PASSIVE SIGN CONVENTION



$$V_{ad} = \underline{\hspace{2cm}}, \quad V_{eb} = \underline{\hspace{2cm}}$$



# SAMPLE PROBLEM



DETERMINE

$$V_x = 4V$$

$$V_{ab} = -8V$$

Power dissipated on  
the 2k resistor

$$P_{2k} =$$

Remember  
past topics

We need to find a closed path where only one voltage is unknown

**FOR  $V_x$**

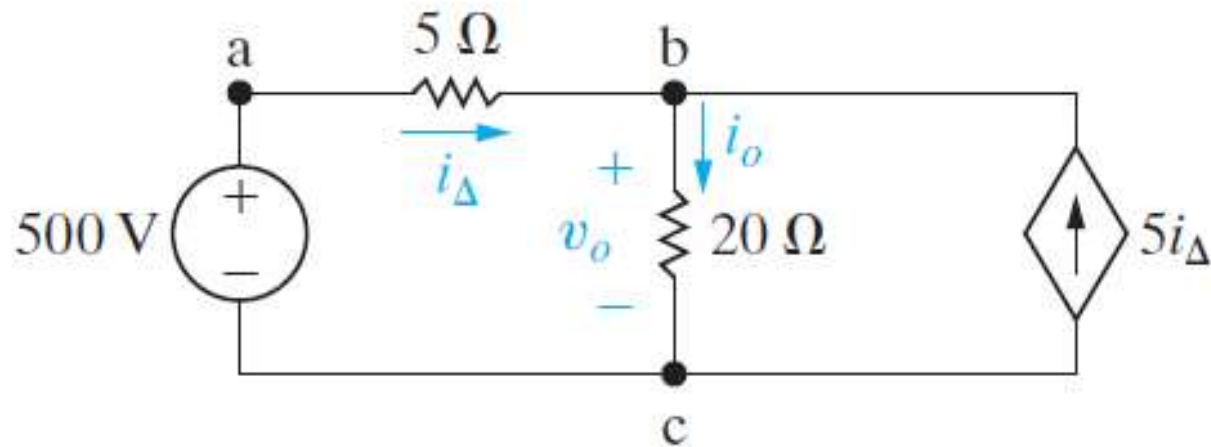
$$V_x + V_2 - V_1 + 4 = 0$$

$$V_x + 4 - 12 + 4 = 0$$

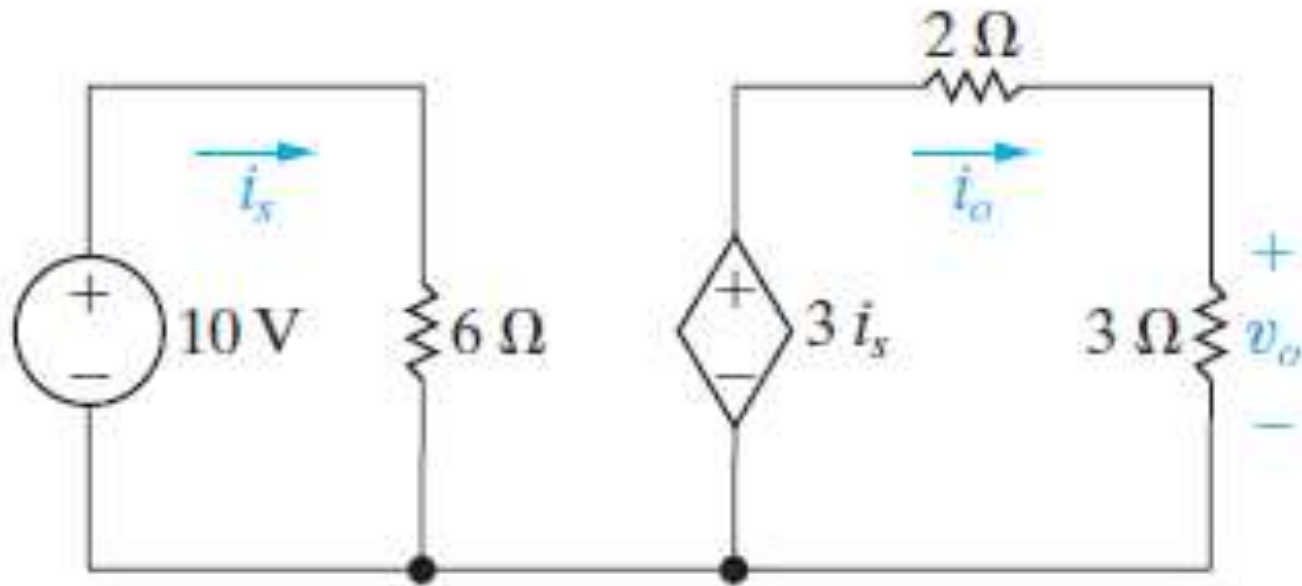
$$V_x + V_2 + V_{ab} = 0$$

$$V_{ab} = -V_x - V_2$$

# Example: A circuit with a dependent source.



# Example:



# Example

