Lab Report

LAB — 09 CSE — 206

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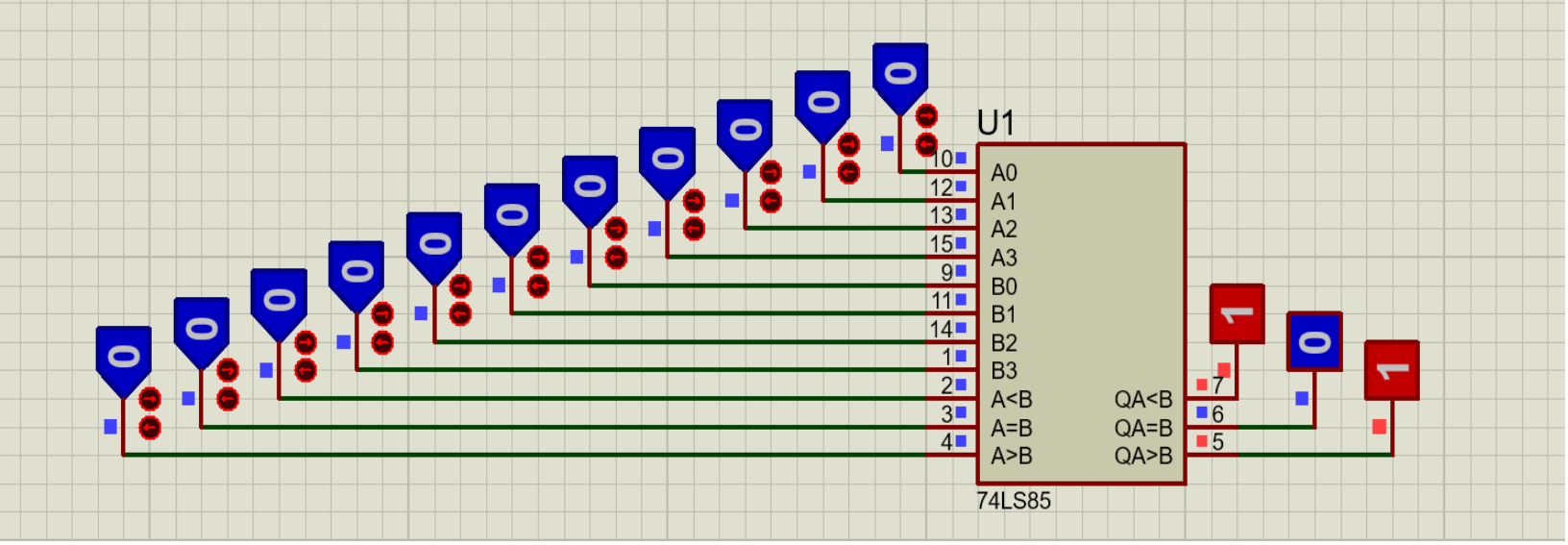
Lab-09

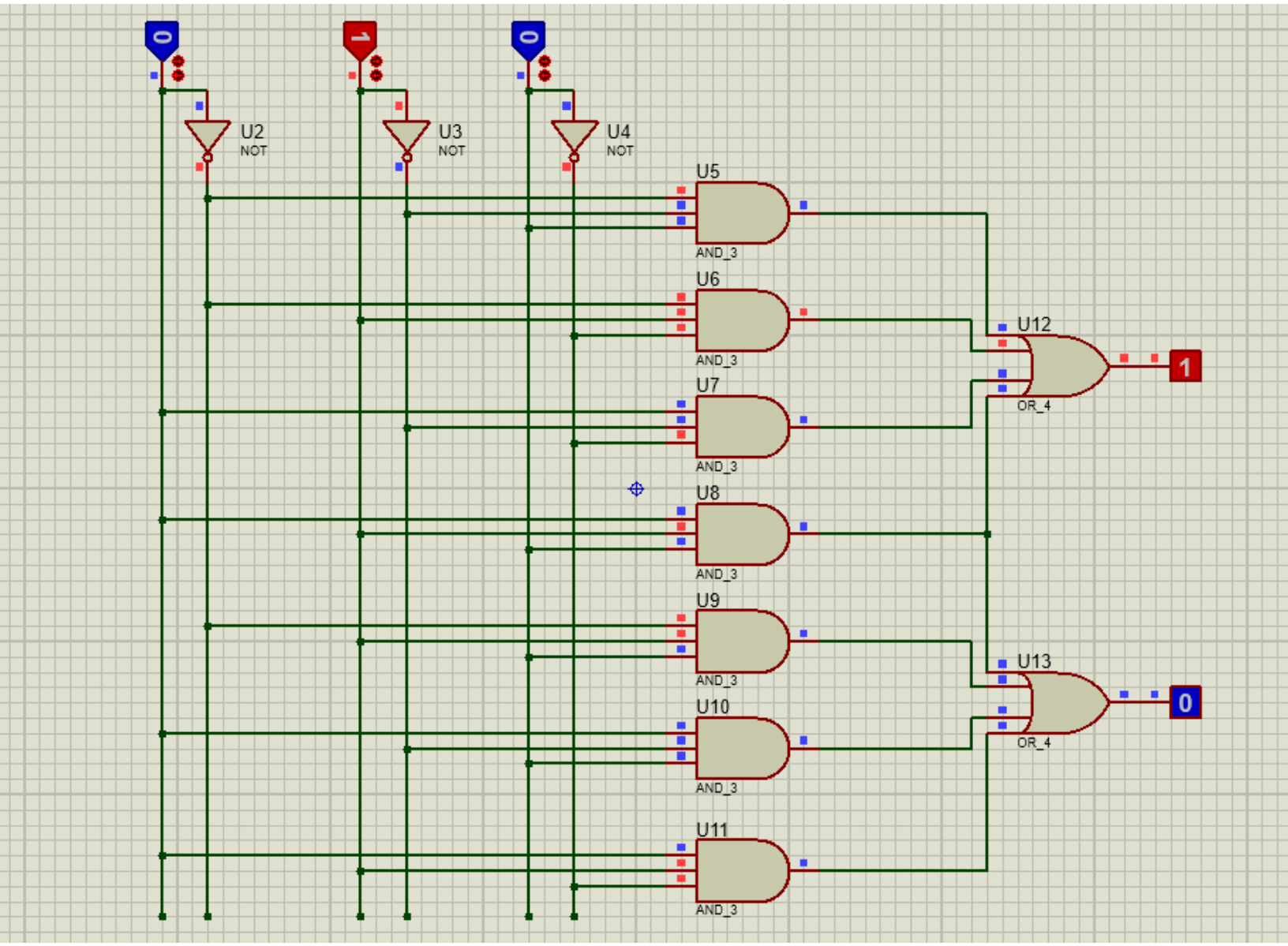
Name of the experiment: Designing of 4-bit magnitude comparator using 74LS85 Ic and full adder using decoder.

Description:

A comparator that is used for comparing two binary numbers each of four bits are called a 4-bit magnitude comparator. It compares whether a binary number is equal, less or greater than the other binary numbers. For building this or with logic gates we will have two inputs (A and B) and have three outputs: one for A>B, another one for A<B and lastly for A=B condition.



4–bit Magnitude Comparator Using 74LS85 IC



Full Adder Using Decoder

Conclution:

(i) We have learnt about magnitude comparator.

(ii) We have learnt about full adder and how to implement it with decoder.

(iii) We have learnt how to implement a 4-bit magnitude comparator using 74LS85 IC.

THE END