

## **Lab Report**

**LAB — 06** 

**CSE - 206** 

#### Presented By:

• Name: Tunazzinur Rahman Kabbo

• Intake: 44

• Sec: 07

• **ID**: 19202103268

#### **Presented To:**

Iffat Tamanna

Lecturer, BUBT
Department of Computer Science & Engineering
Email: iffat@bubt.edu.bd

### Lab-06

Name of the experiment: check the operation of 2 to 4 line decoder and 3 to 8 line decoder.

## Description!

A decoder is a circuit that changes a code into a set of signals. A common type of decoder is the line decoder which takes an n-digit binary number and decodes it into 2n data lines.

2 to 4 line decoder: This 2 to 4 line decoder includes two imputs (x, y) and four outputs (Do, P1, D2, D3).

Truth Table:

X	Y	Do	D <sub>1</sub>	D2	$D_3$
0	0	1	0	0	<i>O</i> : :
0		0	١	0	0
_1	0	0	0	1	0
1	1	0	0	0	1

 $D^0 = X,A, \qquad D^3 = XA,$ 

3 to 8 line decoder: In this decoder there are three inputs (X, Y, Z) and eight outputs (Do, D1, D2, D3, D4, D5, D6, D7)

Truth Table:

X	Y	X	Do	D'	Dz	D3.	D <sub>4</sub>	Ds	De	DZ
0	0	0	1	0	0	Ö	0	0	0	0
0	0	1 "	0	1	0	0	Ó	0	0	0
0	1	0	0	0	1	0	0	0	0	0
0	1	1	0	0	0	1	0	0	0	0
1	0	0	0	0	0	0	1	0	0	0
1	0	1	0	0	0	0	0	1	0	O
1	1	0	Q	0	0	0	0	0	4	Ō
1	1	1	0	0	0	Ó	0	Ö	0	1

Do= X, A, S,

DI=XIXIX

D2= 7'47'

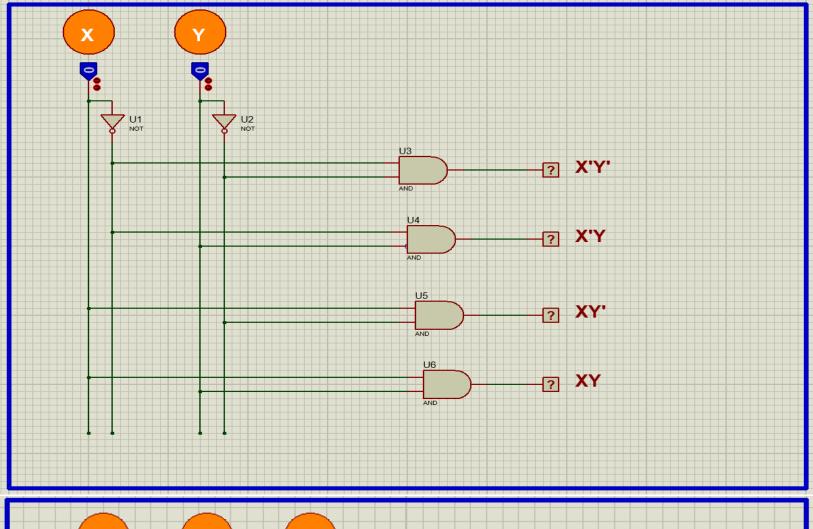
D3 = X'YZ

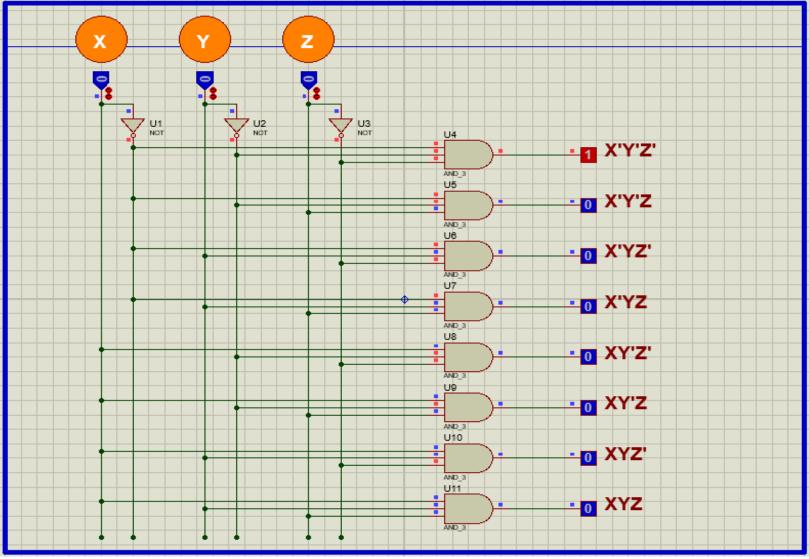
D4 = XY'E'

D5=X4,8

DG=XYZ'

DX=XY8





# Conalysion

- (i) We have learnt what is 2 to 4 line decoders and 3 to 8 line decoders.
- (ii) We have beaunt how to implement 2 to 4 line and 3 to 8 line decoder, via logic gates.
- (iii) We have learnt how to find out the relation between input and output of a decoder.

