

Lab Report

LAB — **03**

CSE — 206

Presented By:

• Name: Tunazzinur Rahman Kabbo

• Intake: 44

• Sec: 07

• **ID**: 19202103268

Presented To:

Iffat Tamanna

Lecturer, BUBT
Department of Computer Science & Engineering
Email: iffat@bubt.edu.bd

	LAB -03		
Name of	the experiment:	Implementation	of
Boolean	function.		

Equipment:

Without Simplification:

- (i) Not Grate
- (ii) Logic State
- (iii) Logic Probe
- (iv) 4 input AND Geate (AND_4)
- (M x input OR Grate(OR_x)

With Simplification:

- (i) Not Gate
- (ii) Logic State
- (iii) Logic Probe
- (iv) 2 input AND Grate (AND)
- (V) 3 input AND Grate (AND_3)
- (vi) 3 input OR Gute (OR_3)

Description: Boolean functions are an important object in logic gate implementation. This functions implementation means logic gates involves connecting output of one logic gate to the input of another gates. Commonly used logic gates are AND, OR, NOT etc. These gates are easy to use and easy for explanation.

Geiven equation:

ABCD + AB

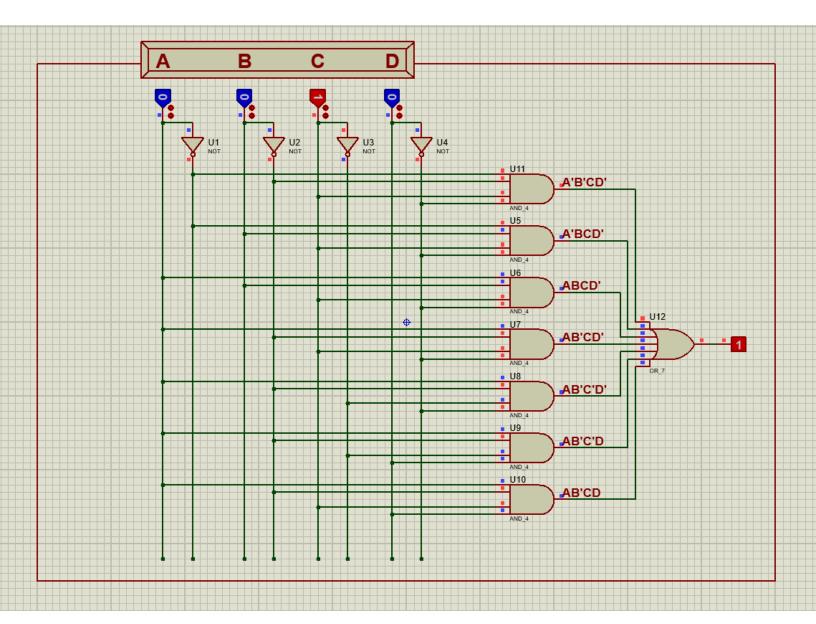
Simplifications

=> ĀCD(B+B) + ABCD + ABCD + ABCD + ABCD + ABCD [Distributive Law]

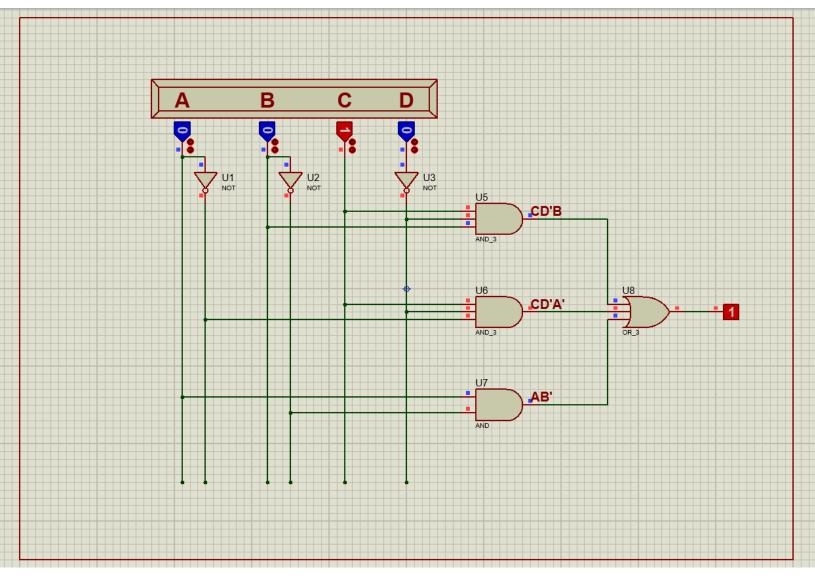
-> ACD 1 + ABCD + ABCD + ABCD + ABCD [Complement and Identity Law]

^	T			+
A	B 0	0	D	Output
0	1	0	0	0
0000	0	0	1	0
0	0	1	0	1
0	0	1	1	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	1
0	1	1	1	0
1	0	0	Ô	1
1	0	0	1	J
1	Ŏ	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	_1	1	0	1
1	1	1	1	0

Truth Table



Without Simplification



With Simplification

Conclution:

- (i) We learnt how to implement Boolean function using the basic gates.
- (ii) We learnt how to implement Boolean function using truth table.
- (iii) We have also learnt how to implement aincuits in Protein Software.

