# **CS 766**: Analysis of Concurrent Programs

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# **Outline**

Operational Semantics of  ${\sf SC}$ 

Axiomatic Semantics of SC

# Operational Semantics of SC

#### **Formal Semantics**

- Here we present (small-step) operational semantics
- Allowed behavior is captured as runs of a Labeled Transition System (LTS)

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- The states of the LTS capture some state information about the program
  - o pc, register storage, "shared memory"
- The transitions of the LTS capture execution steps
  - o E.g., a thread reading from a local register, or a memory update

# A Minimal Concurrent Programming Language

#### **Domains:**

 $r \in \mathsf{Reg}$  local registers  $x \in \mathsf{Loc}$  shared-memory locations  $v \in \mathsf{Data}$  data domain  $i \in \{1, \dots, k\}$  thread identifiers

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#### **Expressions and commands:**

$$e ::= r \mid v \mid e + e \mid e \cdot e \mid \dots$$
 $c ::=$  skip  $\mid$  If  $e$  then  $c$  else  $c \mid$  While  $e$  do  $c \mid$  fence()
 $c; c \mid r := e \mid r := x \mid x := e \mid r :=$ ARW  $(x, e, e)$ 

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**Concurrent programs:**  $P: i \mapsto c_i$ , maps a program  $c_i$  to each thread i

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- The behavior of the concurrent program is an LTS with transitions  $P, S, M \Rightarrow P', S', M'$ , capturing either
  - $\circ~\epsilon$  transitions which are internal for P,S or M, or
  - $\circ$  i:  $\ell$  transitions on which threads interact with the shared memory

# **Thread Operations**

**Store:**  $s: Reg \rightarrow Data$ 

**Initial store:**  $s = \lambda r.0$  for all i

**States:**  $\langle c, s \rangle \in \mathsf{Command} \times \mathsf{Store}$ 

#### **Transitions:**

$$\frac{\langle c_1,s\rangle \xrightarrow{\ell} \langle c_1',s'\rangle}{\langle \mathbf{skip};c,s\rangle \xrightarrow{\epsilon} \langle c,s\rangle} \frac{\langle c_1,s\rangle \xrightarrow{\ell} \langle c_1',s'\rangle}{\langle c_1;c_2,s\rangle \xrightarrow{\ell} \langle c_1';c_2,s'\rangle} \frac{s'=s[r\mapsto s(e)]}{\langle r:=e,s\rangle \xrightarrow{\epsilon} \langle \mathbf{skip},s'\rangle}$$

$$\frac{\ell=R(x,v),s'=s[r\mapsto v]}{\langle r:=x,s\rangle \xrightarrow{\ell} \langle \mathbf{skip},s'\rangle} \frac{\ell=W(x,s(e))}{\langle x:=e,s\rangle \xrightarrow{\ell} \langle \mathbf{skip},s\rangle}$$

$$\frac{s(e)\neq 0}{\langle \mathbf{lf}\ e\ \mathbf{then}\ c_1\ \mathbf{else}\ c_2\rangle \xrightarrow{\epsilon} \langle c_1,s\rangle} \frac{s(e)=0}{\langle \mathbf{lf}\ e\ \mathbf{then}\ c_1\ \mathbf{else}\ c_2\rangle \xrightarrow{\epsilon} \langle c_2,s\rangle}$$

 $\langle \mathsf{While}\ e\ \mathsf{do}\ c,s\rangle \xrightarrow{\epsilon} \langle \mathsf{If}\ e\ \mathsf{then}\ c; \mathsf{While}\ e\ \mathsf{do}\ c\ \mathsf{else}\ \mathsf{skip},s\rangle$ 

# Thread Operations (Cont'd)

$$\frac{\ell = R(x, v), v \neq s(e_r)}{\langle r := \mathsf{ARW} \ (x, e_r, e_w), s \rangle \xrightarrow{\ell} \langle \mathsf{skip}, s[r \mapsto 0] \rangle}$$

$$\frac{\ell = ARW(x, s(e_r), s(e_w))}{\langle r := \mathsf{ARW} \ (x, e_r, e_w), s \rangle \xrightarrow{\ell} \langle \mathsf{skip}, s[r \mapsto 1] \rangle}$$

$$\frac{\langle \mathsf{fence}, s \rangle \xrightarrow{F} \langle \mathsf{skip}, s \rangle}{\langle \mathsf{fence}, s \rangle \xrightarrow{F} \langle \mathsf{skip}, s \rangle}$$

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# **Concurrent Program Operations**

**State:** 
$$\langle P, S \rangle \in \mathsf{Program} \times (\{1, \dots, k\} \to \mathsf{Store})$$

**Transition:** Thread *i* makes a step

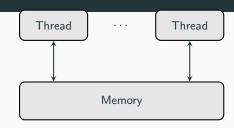
$$\frac{\langle P(i), S(i) \rangle \xrightarrow{\ell} \langle c, s \rangle}{\langle P, S \rangle \xrightarrow{i:\ell} \langle P[i \mapsto c], S[i \mapsto s] \rangle}$$

# **SC** Memory

# **SC Memory State:**

 $M:\mathsf{Loc}\to\mathsf{Data}$ 

**Initial state:**  $M_0 = \lambda x.0$ 

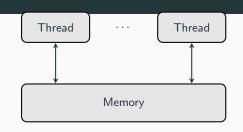


# **SC** Memory

#### **SC Memory State:**

 $M:\mathsf{Loc}\to\mathsf{Data}$ 

Initial state:  $M_0 = \lambda x.0$ 



#### **Transitions:**

$$\frac{\ell = W(x, v)}{M \xrightarrow{i:\ell} M[x \mapsto v]}$$

$$\frac{\ell = ARW(x, v_r, v_w), M(x) = v_r}{M \xrightarrow{i:\ell} M[x \mapsto v_w]}$$

$$\frac{\ell = R(x, v), M(x) = v}{M \xrightarrow{i:\ell} M}$$

$$\frac{\ell = F}{M \xrightarrow{i:\ell} M}$$

# **Concurrent System Operations (Program + SC Memory)**

**State:** 
$$\langle P, S, M \rangle \in \mathsf{Program} \times (\{1, \dots, k\} \to \mathsf{Store}) \times \mathsf{Memory}$$
 state

#### **Transitions:**

THREAD

$$\frac{\langle P, S \rangle \xrightarrow{i:\epsilon} \langle P', S' \rangle}{\langle P, S, M \rangle \Rightarrow \langle P', S', M \rangle}$$

Thread + Memory

$$\frac{\langle P,S\rangle \xrightarrow{i:\ell} \langle P',S'\rangle, M \xrightarrow{i:\ell} M'}{\langle P,S,M\rangle \Rightarrow \langle P',S',M'\rangle}$$

Concurrent program and memory synchronize on non-silent transitions

#### **Axiomatic Semantics General**

- Axiomatic semantics are defined on program executions(traces)
- An execution contains sets of events, which capture the execution of program instructions
- Events are related with various **relations**, capturing, e.g., the order that they were executed, the write that a read is observing, etc.
- Axiomatic semantics are phrased as rules (axioms) that these relations must satisfy

#### **Events**

#### Labels

An event label is one of the following

$$R(x, v_r)$$
  $W(x, v_w)$   $ARW(x, v_r, v_w)$   $F$ 

where  $x \in \mathsf{Loc}$  and  $v_r, v_w \in \mathsf{Data}$ 

#### **Events**

#### **Labels**

An event label is one of the following

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where  $x \in Loc$  and  $v_r, v_w \in Data$ 

#### **Events**

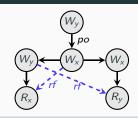
An **event** is a triple  $\langle id, i, \ell \rangle$  where

- $id \in \mathbb{N}$  is an event id
- $i \in \{0, 1, \dots, k\}$  is a thread id, and
- ullet lis a label

E.g.,  $\langle 47,3,W(x,1)\rangle$  means thread 3 executes event 47, which writes to shared location x the value 1

# **Execution Graphs**

Program executions are represented as execution graphs



#### **Execution Graphs**

An **execution graph** is a tuple  $G = \langle E, po, rf \rangle$ , where:

- E is a set of events
- po is a partial order on E (called the program order)
- rf is a binary relation on E such that the following hold
  - For every  $\langle w, r \rangle \in rf$ 
    - w has label W(x, v) or  $ARW(x, \cdot, v)$
    - ▶ r has label R(x, v) or  $ARW(x, v, \cdot)$
  - o  $rf^{-1}$  is a function with domain all events labeled  $R(\cdot,\cdot)$  or  $ARW(\cdot,\cdot,\cdot)$

#### Modification and From-Read Orders

#### **Modification Order**

Given a variable x, a modification order (on x)  $mo_x$  is a total order on events  $W(x,\cdot)$  and  $ARW(x,\cdot,\cdot)$ . The modification order is taken as  $mo = \bigcup_x mo_x$ .

Some papers call mo as coherence order and denote it by co



#### **Modification and From-Read Orders**

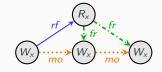
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#### From-read Order

Given an mo, the **from-read** order is defined as  $fr = rf^{-1}$ ;  $mo \setminus [id]$ .



- SC-consistent executions admit a total ordering sc on G.E such that
  - $\circ (po \cup rf)^+ \subseteq sc$
  - o The following pattern is not present



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#### **SC** Consistency

An execution graph G is **SC-consistent** if there exists a modification order mo such that  $po \cup rf \cup mo \cup fr$  is acyclic.

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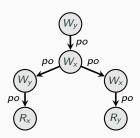
The two definitions are equivalent

# Store Buffer Graphs under SC

#### Store Buffer

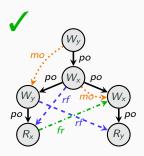
$$x = 0, y = 0$$

$$W(y,1);$$
  
 $a := R(x,\cdot)$   $W(x,1);$   
 $b := R(y,\cdot)$ 



# Store Buffer Graphs under SC

# Store Buffer x = 0, y = 0 W(y,1); a := R(x,0) W(x,1); b := R(y,1)

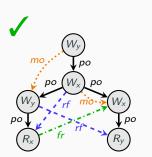


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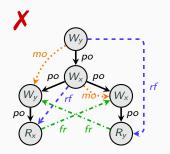
W(y, 1);

$$x = 0, y = 0$$
 $W(y,1);$ 
 $a := R(x,0)$ 
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#### Store Buffer

$$x = 0, y = 0$$
 $W(y,1);$ 
 $a := R(x,0)$ 
 $W(x,1);$ 
 $b := R(y,0)$ 



#### **Load Buffer**

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$$a := R(x, \cdot);$$

$$W(y, 1);$$

#### **Dekker's Protocol**

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$$W(y, 1);$$
  
 $a := R(x, \cdot);$   
if  $(a = 0)$   
 $CS_1$ 

$$x = 0, y = 0$$

$$\begin{vmatrix} W(x,1); \\ b := R(y,\cdot); \\ \text{if } (b = 0) \\ CS_2 \end{vmatrix}$$

$$\varphi = \neg (CS_1 \land CS_2)$$

#### 2+2W

# x = 0, y = 0 W(x,1); W(y,2); $a := R(y,\cdot);$ W(y,1); W(x,2); $b := R(x,\cdot);$ $\varphi = (a = 1 \land b = 1)$

# Oscillating

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$$x = 0, y = 0$$

$$W(x,1);$$

$$a := R(x,\cdot);$$

$$b := R(x,\cdot);$$

$$c := R(x,\cdot);$$

$$\varphi = (a = 1 \land b = 2 \land c = 1)$$

#### **1R1W**

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$$x = 0, y = 0$$

$$W(x, 1);$$

$$a := R(x, \cdot);$$

$$b := R(y, \cdot);$$

$$c := R(y, \cdot);$$

$$d := R(x, \cdot);$$

$$w(y, 1);$$

$$\varphi = (a = 1 \land b = 0 \land c = 1 \land d = 0)$$

# **Runs and Execution Graphs**

Given an execution graph G=(E,po,rf,mo), construct a run  $\rho$  which "agrees" with G, and conversely.

#### **Consistency Checking**

Given a partial execution graph G = (E, po), synthesize the reads-from rf as well as modification order mo so that  $\overline{G}$  can be extended to a full execution graph which is SC-consistent.

#### rf-Consistency Checking

Given a partial execution graph  $\overline{G} = (E, po, rf)$ , synthesize the modification order mo so that  $\overline{G}$  can be extended to a full execution graph which is SC-consistent.

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