

**CHAROTAR UNIVERSITY OF SCIENCE & TECHNOLOGY**  
**Chandubhai S Patel Institute of Technology**  
**V.T. Patel Department of Electronics & Communication**

**Report Summary**

**Expert talk**  
**On**  
**Digital VLSI Testing-Introduction to Design for Testability(DFT)**

**Date: - 02/03/2024**

**No. of Participants: - 49**

Students
49

**Resource Person:** 1.Dr.Arпита Patel.

Associate professor, EC Department, CSPIT, CHARUSAT

**Brief of talk:**

VLSI Design has become an important domain in the field of Digital and Analog circuits. The aim of this talk is to provide a basic knowledge of design concepts in VLSI Design and motivate the students towards research in the field of VLSI.

Following content were covered:

The workshop would cover following:

1. Understanding DFT
2. Controllability and observability
3. Scan chain insertion
4. Stuck at fault

Students ID	Students Name	
21EC082	Diya Thakkar	
21EC034	Ansh Patel	
21EC068	Khushi Shah	
21EC072	RISHI SHAH	
21EC027	SAKSHI PANCHAL	
21EC003	Jinal ahir	
21EC037	Hetvi Patel	
21EC041	Kush Patel	
21EC046	Patel Moksh	
21EC059	PERUMBILLY ELGIN SHAIJY	
21EC055	Vidhi Patel	
21EC008	krushna Chokshi	
21EC001	Aryan Kumar	
21EC002	SATWIK	
21EC060	Jainish Rabari	
21ec083	Tirthani Ankita	
21ec054	Patel Shivali	
21ec053	Rutu Patel	
20ec025	Harsh jangid	
21EC074	Ram Murti Singh	
21EC073	Chiranjivi Sharma	
21EC079	Manav Suthar	
21EC067	Chirayu Shah	
21EC062	Riya Rupapara	
21EC071	Shah Pray	
21EC085	Bhakti Vyas	
D22EC087	BANSARI D PATEL	
21ec061	ABHISHEK RANPARA	
21EC080	Priyanshu Talapara	
21EC018	Shuchi Karathiya	
21ec076	Saharsh Solanki	
21EC065	Savinay salooja	
21EC070	Shah Maulik	
21ec077	Malav Soni	

21EC029	Parekh Prasun M
21ec033	PATEL ANSH MUKESH BHAI
21ec040	kevin patel
21ec039	Patel Karan Mehulbhai
21EC005	Pratham Bagadi
21EC030	Parekh Vrund
21EC057	Patel Yashvi
21EC012	Fenil Jayani
21EC025	PANKAJ PADHIYAR
21EC080	Priyanshu Talapara
21ec050	Patel Param R
21ec068	Khushi shah
21EC067	Chirayu Shah
21ec083	Ankita
21ec071	Pray Shah



**V T Patel Department of Electronics & Communication Engineering**

# **VLSI CLUB-ChipXplorers**

is organizing

**EXPERT TALK**

on

**Digital VLSI Testing-Introduction to Design For Testability**

**DR. USHA MEHTA**

**PROFESSOR & HEAD, NIRMA UNIVERSITY**



**MARCH 2, 2024**

Time: **1:00 p.m onwards**

Venue:- **Seminar Hall, EC Building**

Scan & Register

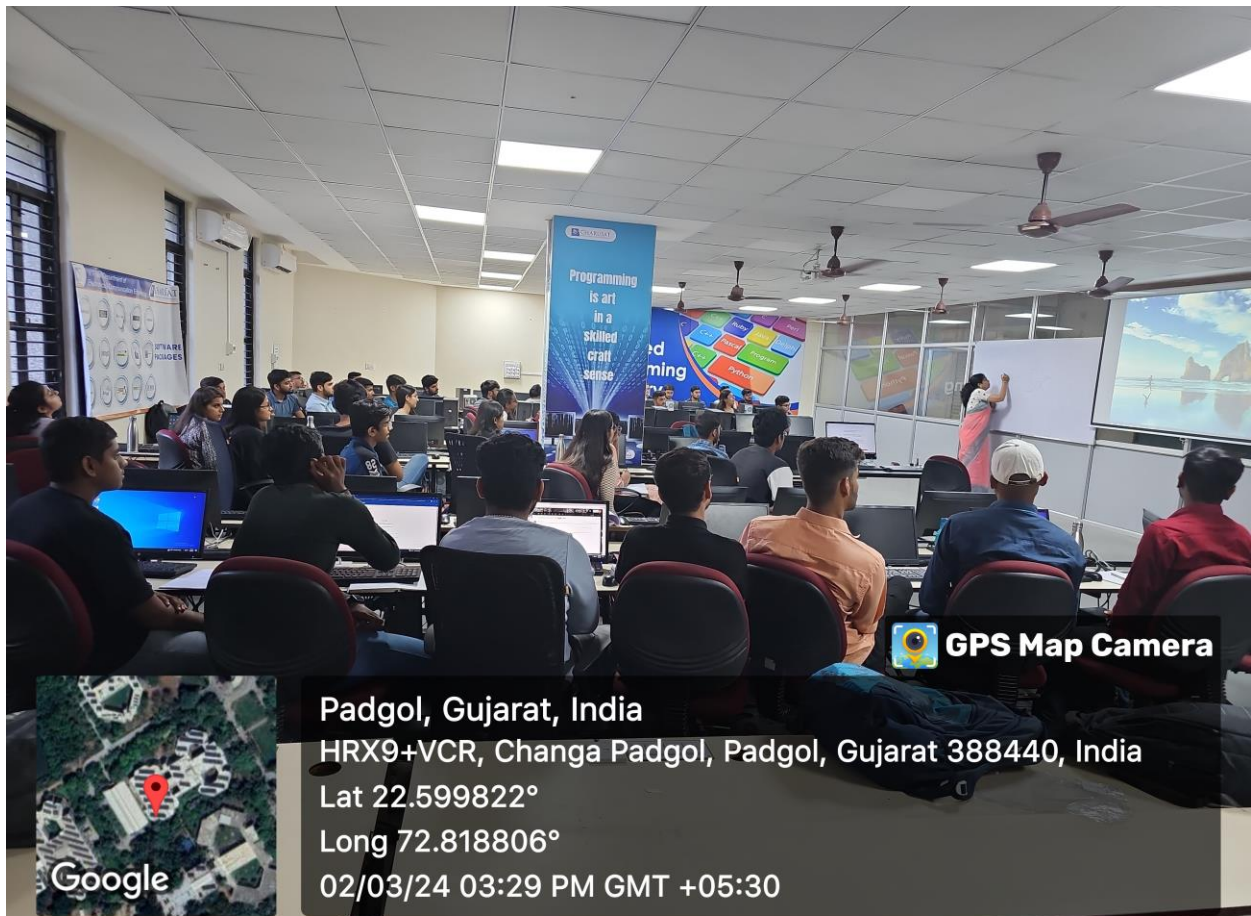


**Faculty Coordinator:**  
**Dr.Arпита Patel**

**Student Coordinators:**  
**Pratham Bagadi**  
**Rutu Patel**









Dr.Arpita Patel

Associate Professor

Ph.D

