

# Towards the Compression of First-Order Resolution Proofs by Lowering Unit Clauses

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**Abstract.** The recently developed **LowerUnits** algorithm compresses propositional resolution proofs generated by SAT- and SMT-solvers by lowering (i.e. postponing) resolution inferences involving unit clauses (i.e. clauses having exactly one literal). This paper describes a generalization of this algorithm to the case of first-order resolution proofs generated by automated theorem provers. An empirical evaluation of a simplified version of this algorithm on hundreds of proofs shows promising results.

## 1 Introduction

Most of the effort in automated reasoning so far has been dedicated to the design and implementation of proof systems and efficient theorem proving procedures. As a result, saturation-based first-order automated theorem provers have achieved a high degree of maturity, with resolution [?] and superposition [?] being among the most common underlying proof calculi. Proof production is an essential feature of modern state-of-the-art provers and proofs are crucial for applications where the user requires certification of the answer provided by the prover. Nevertheless, efficient proof production is non-trivial [?], and it is to be expected that the best, most efficient, provers do not necessarily generate the best, least redundant, proofs. And while the foundational problem of simplicity of proofs can be traced back at least to Hilbert’s 24th Problem [?], the maturity of automated deduction has made it particularly relevant today. Therefore, it is a timely moment to develop methods that post-process and simplify proofs.

For proofs generated by SAT- and SMT-solvers, which use propositional resolution as the basis for the DPLL and CDCL decision procedures, there is now a wide variety of proof compression techniques. Algebraic properties of the resolution operation that might be useful for compression were investigated in [5]. Compression algorithms based on rearranging and sharing chains of resolution inferences have been developed in [1] and [8]. Cotton [4] proposed an algorithm that compresses a refutation by repeatedly splitting it into a proof of a heuristically chosen literal  $\ell$  and a proof of  $\bar{\ell}$ , and then resolving them to form a

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new refutation. The **Reduce&Reconstruct** algorithm [7] searches for locally redundant subproofs that can be rewritten into subproofs of stronger clauses and with fewer resolution steps. A linear time proof compression algorithm based on partial regularization was proposed in [2] and improved in [6]. Furthermore, [6] also described a new linear time algorithm called **LowerUnits**, which delays resolution with unit clauses.

In contrast, for first-order theorem provers, there has been up to now (to the best of our knowledge) no attempt to design and implement an algorithm capable of taking a first-order resolution DAG-proof and efficiently simplifying it, outputting a possibly shorter pure first-order resolution DAG-proof. There are algorithms aimed at simplifying first-order sequent calculus tree-like proofs, based on cut-introduction [?,?], and while in principle resolution DAG-proofs can be translated to sequent-calculus tree-like proofs (and then back), such translations lead to undesirable efficiency overheads. There is also an algorithm [?] that looks for terms that occur often in any TSTP [?] proof (including first-order resolution DAG-proofs) and introduces abbreviations for these terms. However, as the definitions of the abbreviations are not part of the output proof, it cannot be checked by a pure first-order resolution proof checker.

In this paper, we initiate the process of lifting propositional proof compression techniques to the first-order case, starting with the simplest known algorithm: **LowerUnits** (described in Section 3). As shown in Section 4, even for this simple algorithm, the fact that first-order resolution makes use of unification leads to many challenges that simply do not exist in the propositional case. In Section 5 we describe a sophisticated algorithm that overcomes these challenges. Furthermore, in Section 6 we describe a simpler version of this algorithm, which is easier to implement and possibly more efficient, at the cost of compressing less. In Section 7 we present experimental results obtained by applying the simpler algorithm on hundreds of proofs generated with the **SPASS** theorem prover [?]. The next section introduces the first-order resolution calculus using notations that are more convenient for describing proof transformation operations.

## 2 The Resolution Calculus

We assume that there are infinitely many variable symbols (e.g.  $X, Y, Z, X_1, X_2, \dots$ ), constant symbols (e.g.  $a, b, c, a_1, a_2, \dots$ ), function symbols of every arity (e.g.  $f, g, f_1, f_2, \dots$ ) and predicate symbols of every arity (e.g.  $p, q, p_1, p_2, \dots$ ). A *term* is any variable, constant or the application of an  $n$ -ary function symbol to  $n$  terms. An *atomic formula* (*atom*) is the application of an  $n$ -ary predicate symbol to  $n$  terms. A *literal* is an atom or the negation of an atom. The *complement* of a literal  $\ell$  is denoted  $\bar{\ell}$  (i.e. for any atom  $p$ ,  $\bar{p} = \neg p$  and  $\neg \bar{p} = p$ ). The set of all literals is denoted  $\mathcal{L}$ . A *clause* is a multiset of literals.  $\perp$  denotes the *empty clause*. A *unit clause* is a clause with a single literal. Sequent notation is used for clauses (i.e.  $p_1, \dots, p_n \vdash q_1, \dots, q_m$  denotes the clause  $\{\neg p_1, \dots, \neg p_n, q_1, \dots, q_m\}$ ).  $\text{FV}(t)$  (resp.  $\text{FV}(\ell)$ ,  $\text{FV}(\Gamma)$ ) denotes the set of variables in the term  $t$  (resp. in the literal  $\ell$  and in the clause  $\Gamma$ ). A *substitution*  $\{X_1 \setminus t_1, X_2 \setminus t_2, \dots\}$  is a mapping

from variables  $\{X_1, X_2, \dots\}$  to, respectively, terms  $\{t_1, t_2, \dots\}$ . The application of a substitution  $\sigma$  to a term  $t$ , a literal  $\ell$  or a clause  $\Gamma$  results in, respectively, the term  $t\sigma$ , the literal  $\ell\sigma$  or the clause  $\Gamma\sigma$ , obtained from  $t$ ,  $\ell$  and  $\Gamma$  by replacing all occurrences of the variables in  $\sigma$  by the corresponding terms in  $\sigma$ . The set of all substitutions is denoted  $\mathcal{S}$ . A *unifier* of a set of literals is a substitution that makes all literals in the set equal. A *resolution proof* is a directed acyclic graph of clauses where the edges correspond to the inference rules of resolution and contraction (as explained in detail in Definition 1). A *resolution refutation* is a resolution proof with root  $\perp$ .

**Definition 1 (First-Order Resolution Proof).**

A directed acyclic graph  $\langle V, E, \Gamma \rangle$ , where  $V$  is a set of nodes and  $E$  is a set of edges labeled by literals and substitutions (i.e.  $E \subset V \times \mathcal{L} \times \mathcal{S} \times V$  and  $v_1 \xrightarrow[\sigma]{\ell} v_2$  denotes an edge from node  $v_1$  to node  $v_2$  labeled by the literal  $\ell$  and the substitution  $\sigma$ ), is a proof of a clause  $\Gamma$  iff it is inductively constructible according to the following cases:

- **Axiom:** If  $\Gamma$  is a clause,  $\hat{\Gamma}$  denotes some proof  $\langle \{v\}, \emptyset, \Gamma \rangle$ , where  $v$  is a new (axiom) node.
- **Resolution:** If  $\psi_L$  is a proof  $\langle V_L, E_L, \Gamma_L \rangle$  with  $\ell_L \in \Gamma_L$  and  $\psi_R$  is a proof  $\langle V_R, E_R, \Gamma_R \rangle$  with  $\ell_R \in \Gamma_R$ , and  $\sigma_L$  and  $\sigma_R$  are substitutions such that  $\ell_L \sigma_L = \ell_R \sigma_R$  and  $\text{FV}((\Gamma_L \setminus \{\ell_L\}) \sigma_L) \cap \text{FV}((\Gamma_R \setminus \{\ell_R\}) \sigma_R) = \emptyset$ , then  $\psi_L \odot_{\ell_L \sigma_L}^{\sigma_L \sigma_R} \psi_R$  denotes a proof  $\langle V, E, \Gamma \rangle$  s.t.

$$\begin{aligned} V &= V_L \cup V_R \cup \{v\} \\ E &= E_L \cup E_R \cup \left\{ \rho(\psi_L) \xrightarrow[\sigma_L]{\ell_L} v, \rho(\psi_R) \xrightarrow[\sigma_R]{\ell_R} v \right\} \\ \Gamma &= (\Gamma_L \setminus \{\ell_L\}) \sigma_L \cup (\Gamma_R \setminus \{\ell_R\}) \sigma_R \end{aligned}$$

where  $v$  is a new (resolution) node and  $\rho(\varphi)$  denotes the root node of  $\varphi$ .

- **Contraction:** If  $\psi'$  is a proof  $\langle V', E', \Gamma' \rangle$  and  $\sigma$  is a unifier of  $\{\ell_1, \dots, \ell_n\}$  with  $\{\ell_1, \dots, \ell_n\} \subseteq \Gamma'$ , then, letting  $\ell = \ell_k \sigma$  (for any  $k \in \{1, \dots, n\}$ ),  $\lfloor \psi \rfloor_{\sigma}^{\ell}$  denotes a proof  $\langle V, E, \Gamma \rangle$  s.t.

$$\begin{aligned} V &= V' \cup \{v\} \\ E &= E' \cup \{ \rho(\psi') \xrightarrow[\sigma]{\ell} v \} \\ \Gamma &= (\Gamma' \setminus \{\ell_1, \dots, \ell_n\}) \sigma \cup \{\ell\} \end{aligned}$$

where  $v$  is a new (contraction) node and  $\rho(\varphi)$  denotes the root node of  $\varphi$ .  $\square$

The resolution and contraction (factoring) rules described above are the standard rules of the resolution calculus. The presentation of the resolution rule here uses two substitutions, in order to explicitly handle the necessary renaming of variables, which is usually left implicit in many presentations of the resolution calculus.

When the literals and substitutions involved in a resolution or contraction inference are irrelevant or clear from the context, we may write simply  $\psi_L \odot \psi_R$  instead of  $\psi_L \odot_{\ell_L \ell_R}^{\sigma_L \sigma_R} \psi_R$  and  $\lfloor \psi \rfloor$  instead of  $\lfloor \psi \rfloor_{\sigma}^{\ell}$ . When parenthesis are omitted,  $\odot$  is assumed to be left-associative. In the propositional case, we omit contractions (treating clauses essentially as sets instead of multisets) and  $\psi_L \odot_{\ell \ell}^{\emptyset \emptyset} \psi_R$  is abbreviated by  $\psi_L \odot_{\ell} \psi_R$ .

If  $\psi = \varphi_L \odot \varphi_R$  or  $\psi = \lfloor \varphi \rfloor$ , then  $\varphi$ ,  $\varphi_L$  and  $\varphi_R$  are *direct subproofs* of  $\psi$  and  $\psi$  is a *child* of both  $\varphi_L$  and  $\varphi_R$ . The transitive closure of the direct subproof relation is the *subproof* relation. A subproof which has no direct subproof is an *axiom* of the proof.  $V_{\psi}$ ,  $E_{\psi}$  and  $F_{\psi}$  denote, respectively, the nodes, edges and proved clause (conclusion) of  $\psi$ . If  $\psi$  is a proof ending with a resolution node, then  $\psi_L$  and  $\psi_R$  denote, respectively, the left and right premises of  $\psi$ .

**Input:** a proof  $\varphi$   
**Input:**  $D$  a set of subproofs  
**Output:** a proof  $\varphi'$  obtained by deleting the subproofs in  $D$  from  $\varphi$   
**Data:** a map  $'$ , initially empty, eventually mapping any  $\xi$  to  $\text{delete}(\xi, D)$

```

1 if  $\varphi \in D$  or  $\rho(\varphi)$  has no premises then return  $\varphi$ 
2 else
3   let  $\varphi_L \odot_\ell \varphi_R = \varphi$  ;
4    $\varphi'_L \leftarrow \text{delete}(\varphi_L, D)$  ;
5    $\varphi'_R \leftarrow \text{delete}(\varphi_R, D)$  ;
6   if  $\varphi'_L \in D$  then return  $\varphi'_R$  else if  $\varphi'_R \in D$  then return  $\varphi'_L$ 
7   else if  $\ell \notin \Gamma_{\varphi'_L}$  then return  $\varphi'_L$  else if  $\bar{\ell} \notin \Gamma_{\varphi'_R}$  then return  $\varphi'_R$ 
8   else return  $\varphi'_L \odot_\ell \varphi'_R$ 

```

Algorithm 1: delete

### 3 The Propositional LowerUnits Algorithm

We denote by  $\psi \setminus \{\varphi_1, \varphi_2\}$  the result of deleting the subproofs  $\varphi_1$  and  $\varphi_2$  from the proof  $\psi$  and fixing it according to Algorithm 1<sup>1</sup>. We say that a subproof  $\varphi$  in a proof  $\psi$  can be lowered if there exists a proof  $\psi'$  such that  $\psi' = \psi \setminus \{\varphi\} \odot \varphi$  and  $\Gamma_{\psi'} \subseteq \Gamma_\psi$ . If  $\varphi$  originally participated in many resolution inferences within  $\psi$  (i.e. if  $\varphi$  had many children in  $\psi$ ) then lowering  $\varphi$  compresses the proof (in number of resolution inferences), because  $\psi \setminus \{\varphi\} \odot \varphi$  contains a single resolution inference involving  $\varphi$ .

It has been noted in [6] that, in the propositional case,  $\varphi$  can always be lowered if it is a *unit* (i.e. its conclusion clause is unit). This led to the invention of **LowerUnits** (Algorithm 2), which aims at transforming a proof  $\psi$  into  $(\psi \setminus \{\mu_1, \dots, \mu_n\}) \odot \mu_1 \odot \dots \odot \mu_n$ , where  $\mu_1, \dots, \mu_n$  are all units with more than one child. Units with only one child are ignored because no compression is gained by lowering them. The order in which the units are reintroduced is important: if a unit  $\varphi_2$  is a subproof of a unit  $\varphi_1$  then  $\varphi_2$  has to be reintroduced later than (i.e. below)  $\varphi_1$ .

In Algorithm 2, units are collected in a queue during a bottom-up traversal (lines 2-3), then they are deleted from the proof (line 4) and finally reintroduced in the bottom of the proof (lines 5-7). In [?] it has been observed that the two traversals (one for collection and one for deletion) could be merged into a single traversal, if we collect units during deletion. As deletion is a top-down traversal, it is then necessary to collect the units in a stack. This improvement leads to Algorithm 3.

<sup>1</sup> The deletion algorithm is a variant of the RECONSTRUCT-PROOF algorithm presented in [3]. The basic idea is to traverse the proof in a top-down manner, replacing each subproof having one of its premises marked for deletion (i.e. in  $D$ ) by its other premise (cf. ??).

**Input:** a proof  $\psi$   
**Output:** a compressed proof  $\psi^*$   
**Data:** a map  $\cdot'$ : after line 4, it maps any  $\varphi$  to  $\text{delete}(\varphi, D)$

```

1 Units  $\leftarrow \emptyset$ ; // queue to store collected units
2 for every subproof  $\varphi$ , in a bottom-up traversal of  $\psi$  do
3   if  $\varphi$  is a unit with more than one child then enqueue  $\varphi$  in Units
4  $\psi' \leftarrow \text{delete}(\psi, \text{Units})$ ;
   // Reintroduce units
5  $\psi^* \leftarrow \psi'$ ;
6 for every unit  $\varphi$  in Units do
7   let  $\{\ell\} = \Gamma_\varphi$ ;
8   if  $\bar{\ell} \in \Gamma_{\psi'}$  then  $\psi^* \leftarrow \psi^* \odot_\ell \varphi$ 

```

**Algorithm 2:** LowerUnits

**Input:** a proof  $\psi$   
**Output:** a compressed proof  $\psi^*$   
**Data:** a map  $\cdot'$ , eventually mapping any  $\varphi$  to  $\text{delete}(\varphi, \text{Units})$

```

1  $D \leftarrow \emptyset$ ; // set for storing subproofs that need to be deleted
2 Units  $\leftarrow \emptyset$ ; // stack for storing collected units
3 for every subproof  $\varphi$ , in a top-down traversal of  $\psi$  do
4   if  $\varphi$  is an axiom then  $\varphi' \leftarrow \varphi$  else
5     let  $\varphi_L \odot_\ell \varphi_R = \varphi$ ;
6     if  $\varphi_L \in D$  and  $\varphi_R \in D$  then add  $\varphi$  to  $D$  else if  $\varphi_L \in D$  then
7        $\varphi' \leftarrow \varphi'_R$  else if  $\varphi_R \in D$  then  $\varphi' \leftarrow \varphi'_L$ 
8     else if  $\ell \notin \Gamma_{\varphi'_L}$  then  $\varphi' \leftarrow \varphi'_L$  else if  $\bar{\ell} \notin \Gamma_{\varphi'_R}$  then  $\varphi' \leftarrow \varphi'_R$ 
9     else  $\varphi' \leftarrow \varphi'_L \odot_\ell \varphi'_R$ 
9   if  $\varphi$  is a unit with more than one child then
10     push  $\varphi'$  onto Units;
11     add  $\varphi$  to  $D$ ;

   // Reintroduce units
12  $\psi^* \leftarrow \psi'$ ;
13 while Units  $\neq \emptyset$  do
14    $\varphi' \leftarrow \text{pop}$  from Units;
15   let  $\{\ell\} = \Gamma_\varphi$ ;
16   if  $\ell \in \Gamma_{\psi^*}$  then  $\psi^* \leftarrow \psi^* \odot_\ell \varphi'$ 

```

**Algorithm 3:** Improved LowerUnits (with a single traversal)

## 4 First-Order Challenges

TODO by Jan (just writing some ideas so far—not yet final by any means)

In this section, we discuss the challenges introduced by adapting **LowerUnits** to the first-order case. The first example illustrates how to extend **LowerUnits** to first-order logic in the obvious way. Examples 2 and on illustrate concerns that are introduced by the unification process that must be over come in order to successfully postpone resolution with a unit clause as a result of this extension.

*Example 1.* Resolution with a particular unit clause  $u$ , with literal  $\ell$  may be performed with a clause  $v$  provided  $v$  contains  $\bar{\ell}$  and there is some unifier  $\sigma$  such that  $\ell\sigma$  contains the same variables as  $\bar{\ell}$  (or such that  $\bar{\ell}\sigma$  contains the same variables as  $\ell$ ). After applying  $\sigma$  to the resolvents, the literals match syntactically, and so this behaves like the propositional case. Thus, the notion of looking for unifiable formulas to postpone resolution with  $u$  is natural. Consider the following proof of  $\psi$ , where we will postpone resolution with  $\eta_2$ :

$$\frac{\frac{\eta_1: p(Y) \vdash q(Z) \quad \eta_2: \vdash p(Y)}{\eta_3: \vdash q(Z)} \quad \eta_4: p(X), q(Z) \vdash}{\eta_5: p(X) \vdash} \quad \eta_2 \quad \psi: \perp$$

In order to postpone resolution with  $\eta_2$ , the formulas  $p(Y)$  (in  $\eta_1$ ) and  $p(X)$  (in  $\eta_4$ ) will both remain after unifying these two nodes together. Unlike in the propositional case, where we could drop the repeated formula, in order to compress the proof soundly, we must first contract these formulas ( $\eta'_3$ ) into a single literal ( $\eta'_4$ ). Then we can finish the proof by reintroducing our postponed resolution, as in below.

$$\frac{\frac{\eta'_1: p(Y) \vdash q(Z) \quad \eta'_2: p(X), q(Z) \vdash}{\eta'_3: p(X), p(Y) \vdash} \quad \eta'_4: p(X) \vdash \quad \eta'_5: \vdash p(Y)}{\psi: \perp}$$

*Example 2.* When attempting to lower a unit clause in the first order case, additional properties must be satisfied. In addition to the requirement that there is a unifier  $\sigma$  that makes the unit clause syntactically equal to the formula it resolves away, we require that all such unifiers between  $u$  behave similarly in some sense. Consider the example below, where we consider postponing  $\eta_2$  in the proof of  $\psi$ .

$$\frac{\eta_2 \quad \frac{\frac{\eta_1: p(a) \vdash q(Y), r(Z) \quad \eta_2: \vdash p(X)}{\eta_3: \vdash q(Y), r(Z)} \quad \eta_4: r(X), p(b) \vdash s(Y)}{\eta_5: p(b) \vdash s(Y), q(Y)} \quad \eta_6: s(Y), q(Y) \vdash}{\eta_7: p(b) \vdash} \quad \psi: \perp$$

The literals resolved with  $u = \eta_2$  are  $p(a)$  (in  $\eta_1$ ) and  $p(b)$  (in  $\eta_7$ ). If we attempt to postpone resolution, at the contraction step the clause would be  $p(a), p(b)$ . This clause cannot be contracted, as there is no unifier between these terms that would make their variable sets equal (in fact, there are no variables at all). Thus, it would be a waste of time to attempt to postpone resolution with  $u$ , and so we require any unit we wish to lower to satisfy the following property.

*Property 1.* Things must be pair-wise unifiable...

*Example 3.* The following shows why the above is not necessarily enough (we must check the original sources of the aux formulas, and see if those can be contracted), otherwise we might not save anything.

$$\frac{\frac{\eta_1: r(Y), p(X \ q(Y \ b)), p(X \ Y) \vdash \quad \eta_2: \vdash p(U \ V)}{\eta_3: r(V), p(U \ q(V \ b)) \vdash} \quad \eta_4: \vdash r(W)}{\eta_5: p(U \ q(W \ b)) \vdash} \quad \eta_2}{\psi: \perp}$$

*Example 4.* Consider the following, which shows the dangers of ambiguous resolution in the first order case:

$$\frac{\frac{\eta_1: p(U), r(U \ V), r(V \ U), q(V) \vdash \quad \eta_2: \vdash p(c)}{\eta_3: r(c \ V), r(V \ c), q(V) \vdash} \quad \eta_4: \vdash r(X \ c)}{\eta_5: r(c \ X), q(X) \vdash} \quad \eta_6: \vdash r(W \ V)}{\eta_7: q(V) \vdash} \quad \eta_8: p(Z) \vdash q(d)}{\eta_9: p(Z) \vdash} \quad \eta_2}{\psi: \perp}$$

Which if we lower  $\eta_2: \vdash p(c)$ , we would attempt to resolve the following two sequents

$$\begin{aligned} p(U), r(U \ V), r(V \ U), q(V) \vdash \\ \vdash r(X \ c) \end{aligned}$$

Where we would have to be careful. If we used  $r(U \ V)$ , then we would use the unifier  $U -> X, V -> c$ , which would result in the following:

$$p(X), r(V \ X), q(c) \vdash$$

but the original proof does not have a method for resolving away  $q(c)$ , so we would not be able to complete the proof. On the other hand, if we chose  $r(V \ U)$ , we would unify with  $V -> X, U -> c$ , with which we could complete the proof:

$$\frac{\frac{\eta'_1: p(U), r(U \ V), r(V \ U), q(V) \vdash \quad \eta'_2: \vdash r(X \ c)}{\eta'_3: p(c), r(c \ X), q(X) \vdash} \quad \eta'_4: \vdash r(W \ V)}{\eta'_5: p(c), q(V) \vdash} \quad \eta'_6: p(Z) \vdash q(d)}{\eta'_7: p(c), p(Z) \vdash} \quad \eta'_8: p(c) \vdash} \quad \eta'_9: \vdash p(c)}{\psi: \perp}$$

A method to avoid this issue is discussed in section 6



**Input:** a proof  $\varphi$   
**Input:**  $D$  a set of subproofs  
**Output:** a proof  $\varphi'$  obtained by deleting the subproofs in  $D$  from  $\varphi$

```

1 if  $\varphi \in D$  or  $\rho(\varphi)$  has no premises then return  $\varphi$ 
2 else
3   let  $\varphi_L$  and  $\varphi_R$  be such that  $\varphi = \varphi_L \odot_{\ell_L \ell_R}^{\sigma_L \sigma_R} \varphi_R$  ;
4   let  $\varphi'_L = \text{delete}(\varphi_L, D)$  ;
5   let  $\varphi'_R = \text{delete}(\varphi_R, D)$  ;
6   if  $\varphi'_L \in D$  then return  $\varphi'_R$  else if  $\varphi'_R \in D$  then return  $\varphi'_L$ 
7   else if  $\ell \notin \Gamma_{\varphi'_L}$  then return  $\varphi'_L$  else if  $\bar{\ell} \notin \Gamma_{\varphi'_R}$  then return  $\varphi'_R$ 
8   else return  $\varphi'_L \odot_{\ell_L \ell_R}^{\sigma_L \sigma_R} \varphi'_R$ 

```

**Algorithm 4:** fo-delete

## 5 First-Order LowerUnits

**Proposition 1.** *Given a proof  $\psi$ , if there is a sequence  $U = (\varphi_1 \dots \varphi_n)$  of  $\psi$ 's subproofs and a sequence  $(\ell_1 \dots \ell_n)$  of literals such that  $\forall i \in [1 \dots n]$ ,  $\ell_i$  is the univalent literal of  $\varphi_i$  w.r.t.  $\Delta_{i-1} = \{\ell_1 \dots \ell_{i-1}\}$ , then the conclusion of*

$$\psi' = \psi \setminus \{U\} \odot_{\ell_n} \varphi_n \dots \odot_{\ell_1} \varphi_1$$

*subsumes the conclusion of  $\psi$ .*

*Proof.* The proposition is proven by induction on  $n$ , along with the fact that  $\psi \setminus \{U\} \notin U$ . For  $n = 0$ ,  $U = \emptyset$  and the properties trivially hold. Suppose a subproof  $\varphi_{n+1}$  of  $\psi$  is univalent w.r.t.  $\Delta_n$ , with univalent literal  $\ell_{n+1}$ . Because  $\ell_{n+1} \notin \Delta_n$ , there exists a subproof of  $\psi \setminus \{U\}$  with conclusion containing  $\bar{\ell}_{n+1}$ , and therefore  $\psi \setminus \{U\} \setminus \{\varphi_{n+1}\} \notin U \cup \{\varphi_{n+1}\}$ . Let  $\Gamma$  be the conclusion of  $\psi \setminus \{U\}$ . The conclusion of  $\psi' = \psi \setminus \{U \cup \{\varphi_{n+1}\}\} = \psi \setminus \{U\} \setminus \{\varphi_{n+1}\}$  is included in  $\Gamma \cup \{\bar{\ell}_{n+1}\}$ . The conclusion of  $\psi' \odot_{\ell_{n+1}} \varphi_{n+1}$  is included in  $\Gamma \cup \Delta_n$ . As  $\Gamma \subseteq \Gamma_\psi \cup \Delta_n$ , the conclusion of  $\psi' \odot_{\ell_{n+1}} \varphi_{n+1} \dots \odot_{\ell_1} \varphi_1$  is included in  $\Gamma_\psi$ .  $\square$

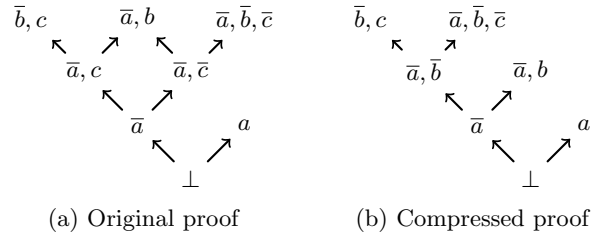


Fig. 1: Example of proof compression by LowerUnivalents

## 6 A Simpler First-Order LowerUnits

ToDo by Jan

Recall example 4. In order to avoid this, we introduce a proof rule that applies a substitution. So that we would get the following proof

$$\begin{array}{c}
 \frac{\eta_1: p(U), r(U \ V), r(V \ U), q(V) \vdash}{\eta_2: p(c), r(c \ V), r(V \ c), q(V) \vdash} \quad \eta_3: \vdash r(X \ c) \\
 \frac{\eta_2: p(c), r(c \ V), r(V \ c), q(V) \vdash}{\eta_4: p(c), r(c \ X), q(X) \vdash} \quad \eta_5: \vdash r(W \ V) \\
 \frac{\eta_4: p(c), r(c \ X), q(X) \vdash}{\eta_6: p(c), q(V) \vdash} \quad \eta_7: p(Z) \vdash q(d) \\
 \frac{\eta_6: p(c), q(V) \vdash}{\eta_8: p(c), p(Z) \vdash} \quad \eta_9: p(c) \vdash \\
 \frac{\eta_8: p(c), p(Z) \vdash}{\eta_9: p(c) \vdash} \quad \eta_{10}: \vdash p(c) \\
 \hline
 \psi: \perp
 \end{array}$$

Now  $r(V, c)$  appears in the first left resolvent, which was the left aux formula in the original proof. Thus, the implementation can find that formula, and choose it in order to resolve the ambiguous resolution, instead of guessing a formula from the left resolvent that unifies with the right resolvent, which might go wrong.

TODO: Explain where the sub came from.

TODO: define the rule formally here?

TODO: describe when the rule is invoked in the implementation

## 7 Experiments

ToDo by Jan

**LowerUnits** has been implemented as a prototype<sup>2</sup> in the functional programming language Scala<sup>3</sup> as part of the **Skeptik** library<sup>4</sup>. **LowerUnits** has been implemented as a recursive **delete** improvement.

The algorithm has been applied to **308** proofs produced by the **SPASS**<sup>5</sup> theorem prover on unsatisfiable benchmarks from the TPTP Problem Library<sup>6</sup>. The proofs used were restricted to those which could be solved within 300 seconds by **SPASS** on the Euler Cluster at the University of Victoria<sup>7</sup> using only the contraction and unifying resolution inference rules.

For each proof  $\psi$  (with the result of **LowerUnits** applied to the proof denoted by  $\alpha(\psi)$ ), the time to compress the proof ( $t(\psi)$ ), the compression ratio  $((|\psi| - |\alpha(\psi)|)/|\psi|)$ , the resolution compression ratio  $((|\psi|_R - |\alpha(\psi)|_R)/|\psi|_R)$ , the compression speed  $((|\psi| - |\alpha(\psi)|)/t(\psi))$ , and resolution compression speed

<sup>2</sup> Source code available at <https://github.com/jgorzny/Skeptik>

<sup>3</sup> <http://www.scala-lang.org/>

<sup>4</sup> <https://github.com/Paradoxika/Skeptik>

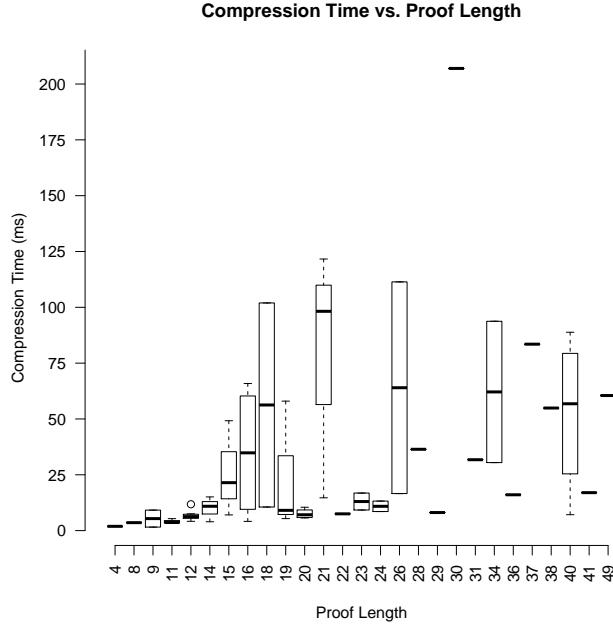
<sup>5</sup> <http://www.verit-solver.org/>

<sup>6</sup> <http://www.cs.miami.edu/tptp/>

<sup>7</sup> <https://rcf.uvic.ca/euler.php>

$((|\psi|_R - |\alpha(\psi)|_R)/t(\psi))$  were measured<sup>8</sup>, where  $|\psi|_R$  indicates the number of resolution inference rules in the proof  $\psi$ .

The experiments were executed on a laptop (2.8GHz Intel Core i7 processor with 4 GB of RAM (1333MHz DDR3) available to the Java Virtual Machine), and the prototype implementation performed well on this system. Figure ?? shows the compression time  $t(\psi)$  for each proof, sorted by proof length, and figure ?? (respectively figure ??) shows the compression speed (respectively resolution compression speed) for each proof, also sorted by proof length.



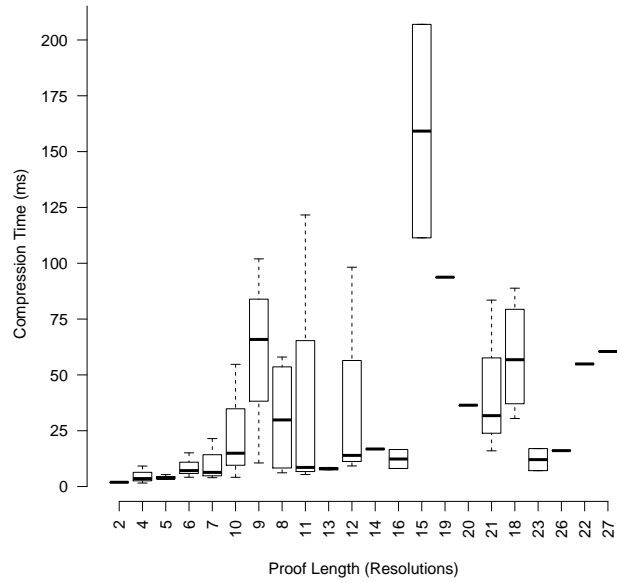
## 8 Conclusions and Future Work

ToDo: by Bruno

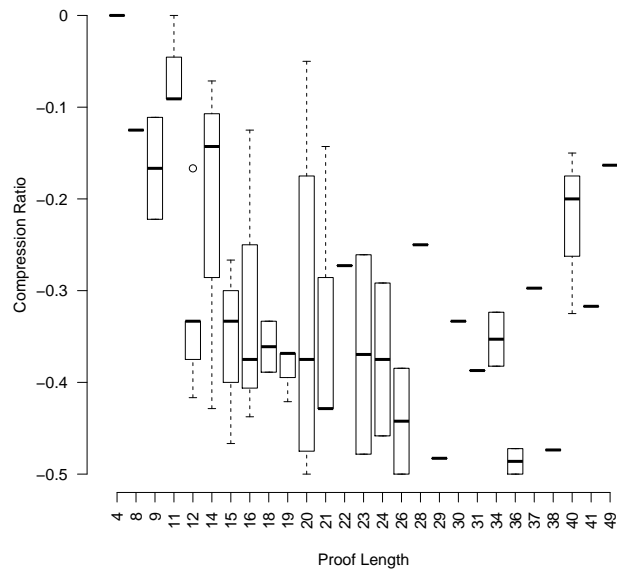
**LowerUnivalents**, the algorithm presented here, has been shown in the previous section to compress more than **LowerUnits**. This is so because, as demonstrated in Proposition ??, the set of subproofs it lowers is always a superset of the set of subproofs lowered by **LowerUnits**. It might be possible to lower even more subproofs by finding a characterization of (efficiently) lowerable subproofs broader than that of univalent subproofs considered here. This direction

<sup>8</sup> The raw data is available at <https://docs.google.com/spreadsheets/d/1F1-t2OuhypmTQhLU6yTj42aiZ5CqqaZvhVvOzeFgn0k/edit#gid=1182923972>

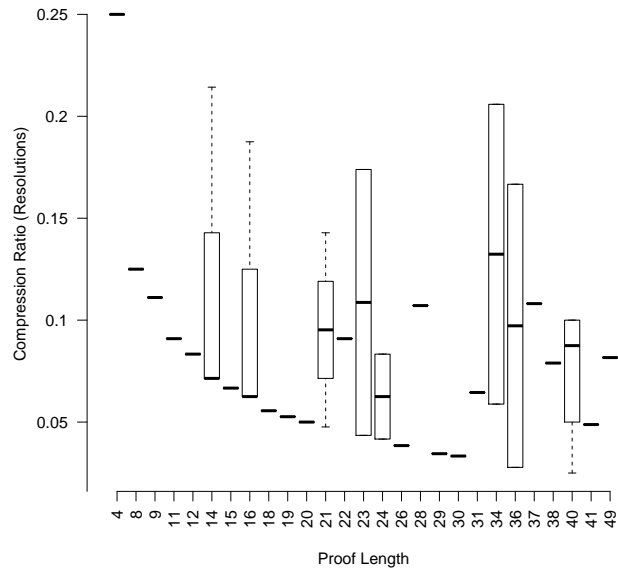
Compression Time vs. Proof Length (Resolutions)



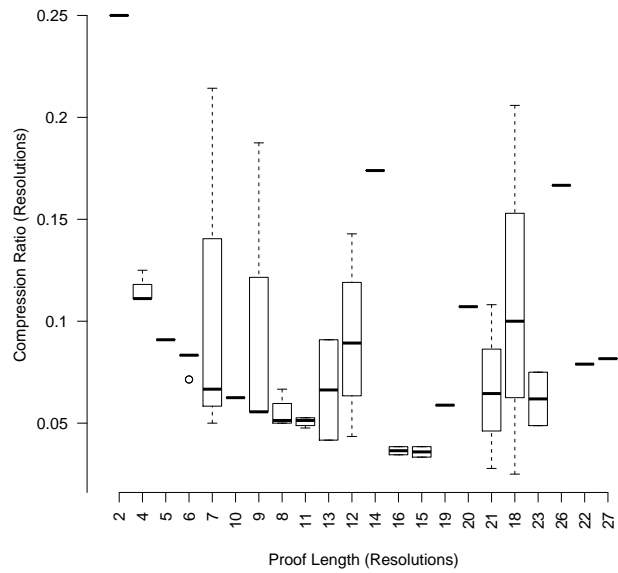
Compression Ratio vs. Proof Length



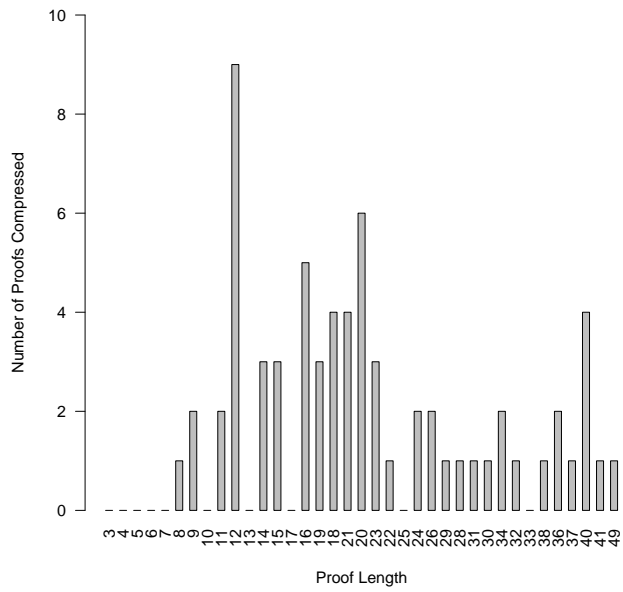
Compression Ratio (Resolutions) vs. Proof Length



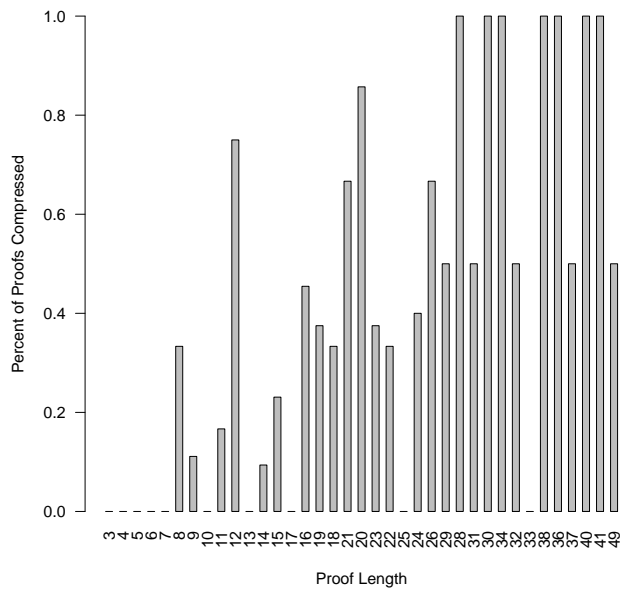
Compression Ratio (Resolutions) vs. Proof Length (Resolutions)

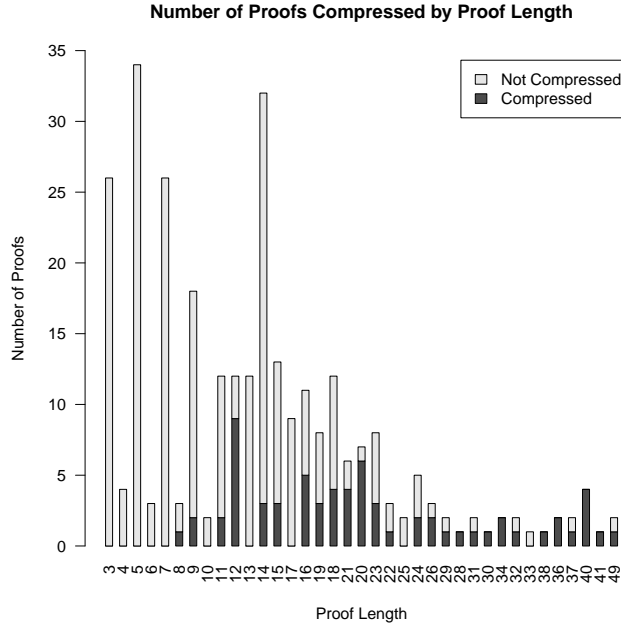


Number of Proofs Compressed by Proof Length



Percent of Proofs Compressed by Proof Length





for future work promises to be challenging, though, as evidenced by the non-triviality of the optimizations discussed in Section ?? for obtaining a linear-time implementation of **LowerUnivalents**.

As discussed in Section ??, the proposed algorithm can be embedded in the deletion traversal of other algorithms. As an example, it has been shown that the combination of **LowerUnivalents** with RPI, compared to the sequential composition of **LowerUnits** after RPI, results in a better compression ratio with only a small processing time overhead (Figure ??). Other compression algorithms that also have a subproof deletion or reconstruction phase (e.g. **Reduce&Reconstruct**) could probably benefit from being combined with **LowerUnivalents** as well.

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