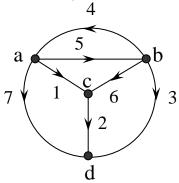
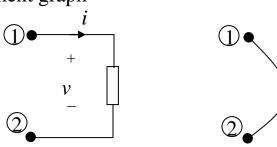
Graph Theory

A structure consisting of vertices and edges 4

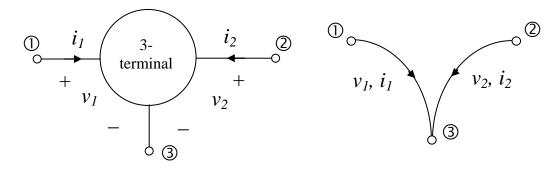


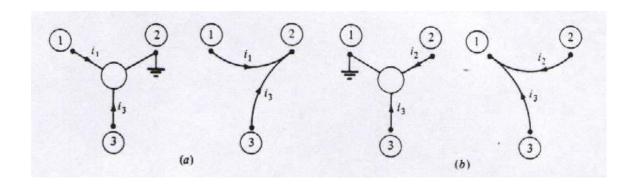
number of vertices=4 number of edges=7

Element graph

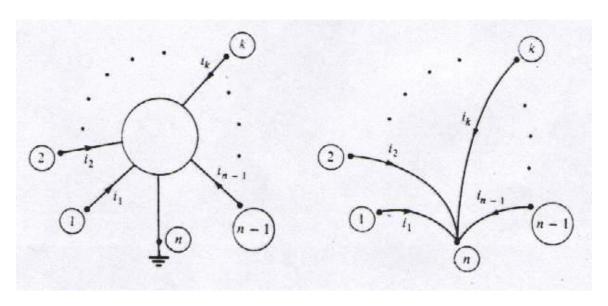


Passive sign convention should be met

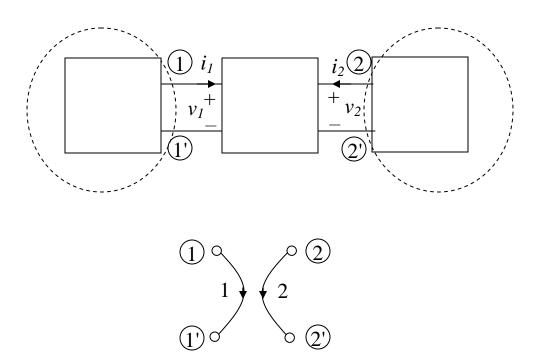


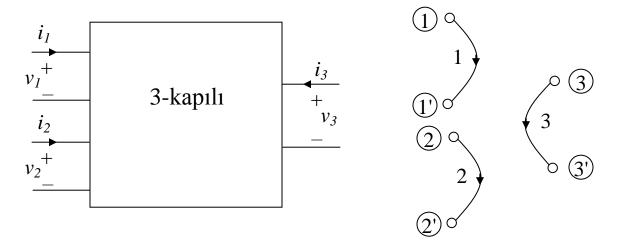


Element graph of an n-terminal

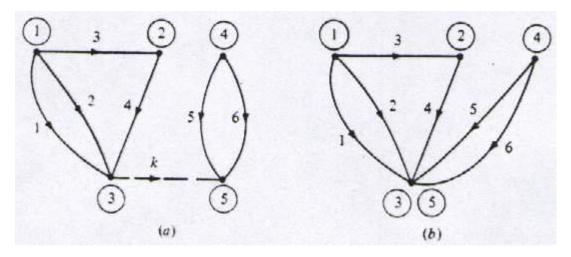


Two-port and multi-port elements

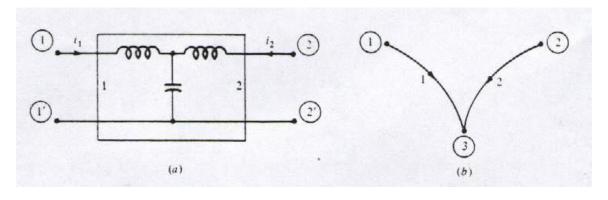




The graph of a 2-port element is disjoint. However any disjoint graph can be converted into a connected graph as shown below.



The subcircuit below can be considered either a 2port or 3-terminal.



Circuit graph: A graph is a pictorial structure which represents the interconnections of the elements in an electric circuit.

Edge: A directed line which represents a connection between vertices

Vertex (node, point): The edges of an edge

degre (of a vertex): the number of edges connected to a vertex

$$\delta(d_i)=4$$

An isolated vertex is a vertex with δ =0

In a graph with n_e elements and n vertices, the sum of degrees of all vertices equals to $2 n_e$.

Subgraph is graph formed from a subset of the vertices and edges of a given graph.

Path is a sequence of vertices and edges, with both endpoints of an edge appearing adjacent to it in the sequence

A connected graph is one in which each pair of vertices forms the endpoints of a path. Otherwise, it is disjoint graph.

Loop is a path whose endpoints are the same vertex. Degrees of all vertices in a loop is 2.

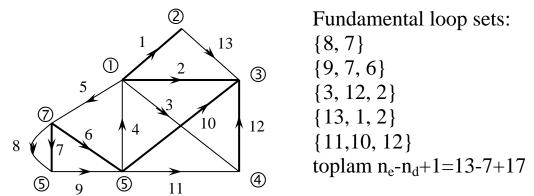
Tree: A tree is a connected and acyclic subgraph which contains all the vertices of the given graph.

Provided that the graph has n_e edges and n_d vertices, in a tree,

- 1. the number of edges is n_e -1.
- 2. There is one only one path interconnecting each pair of vertices.

Fundamental Loop set:

Each co-tree of a tree and some branched define a loop. For a given tree, there is n_e - n_d +1 such loops. These loop consitute fundamental loop set.

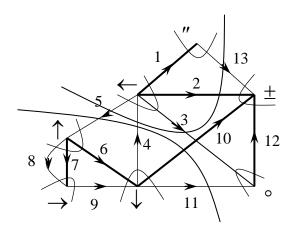


Cut-set: Some elements in graph G satisfying the followins contitute a cut-set.

- a) After removal of these elements, graph is split into two parts.
- b) Any subset of these elements satisfy a).

Fundemantal cut-set:

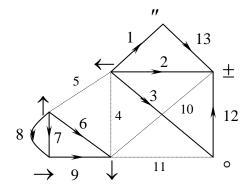
For a given tree, each of the branches contitutes a cut-set with only some branches of the tree. This cut-set is called fundemantal cut-set.



Connected graph

$$n_e$$
=13 => dal n_d -1=7-1=6
 n_d =7=> kiriş n_e - n_d +1=7

- Co-tree elements $G_K=\{3, 4, 5, 8, 9, 11, 13\}$
- Tree $G_T = \{1, 2, 6, 7, 10, 12\}$



{5, 4, 10, 11} is a cut-set

• Cut-set $G_{DK} = \{1, 2, 3, 4, 5\}$

Graph Matrices

1. Fundamental Loop Matrix

Fill in the entries of the matrix $\mathbf{B_t} = [b_{ij}]$ as follows:

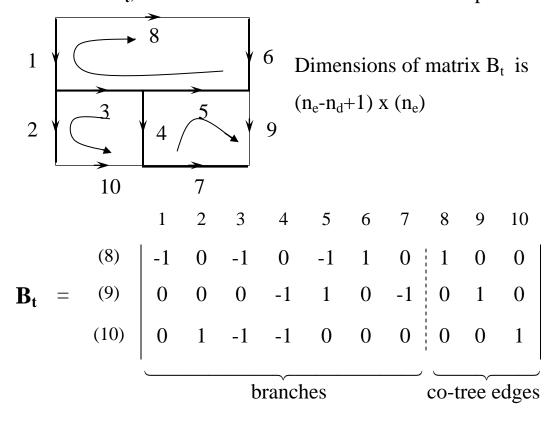
 $b_{ij}=0$; jth element is not included in the ith fundamental loop,

 $b_{ij}=1$; jth element is included in the ith fundamental loop, and the element is in the same orientation as the loop,

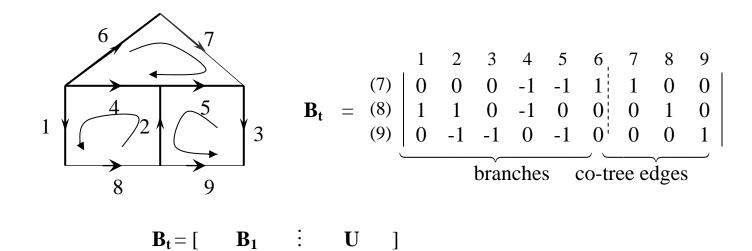
 b_{ij} =-1; jth element is included in the ith fundamental loop, and the element is in the opposite orientation as the loop.

(the orientation of a loop is the same as that of the co-tree)

The matrix \mathbf{B}_{t} , thus obtained is called fundamental loop matrix.



$$\mathbf{B_t} = [\underbrace{\mathbf{B_1}}_{\mathbf{n_d-1}} \quad \vdots \quad \underbrace{\mathbf{U}}_{\mathbf{n_e-n_d+1}}] \qquad n_e-n_d+1$$



 $Rank~\{B_t\} = n_e\text{-}n_d\text{+}1\text{= number of co-trees} = number of }$ fundamental loops

2. Fundamental Cut-Set Matrix

For each fundamental cut-set in graph G, cosntruct the fundamental cut-set matrix \mathbf{Q}_t =[\mathbf{q}_{ij}] according to the following steps:

 q_{ij} =0; jth element is not included in the set of ith fundamental cut,

 q_{ij} =1; jth element is included in the set of ith fundamental cut and it is in the same orientation as the cut-set.

 q_{ij} =-1; jth element is included in the set of ith fundamental cut and it is in the opposite orientation as the cut-set.

(the direction of the cut set is chosen as the direction of the branch)

The orientation of cut-set is the same as that of co-tree element and is taken positive.

$$\mathbf{Q_{t}} = (2) \begin{bmatrix} 1 & 2 & 3 & 4 & 5 & 6 & 7 \\ 1 & 0 & 0 & -1 & 1 & 0 & 1 \\ 0 & 1 & 0 & -1 & 1 & -1 & 1 \\ 0 & 0 & 1 & 1 & -1 & 1 & 0 \end{bmatrix}$$
branches co-tree edges

$$\mathbf{Q_t} = [\quad \mathbf{U} \quad \vdots \quad \mathbf{Q_1} \quad] \qquad \qquad n_{d}\text{-}1$$

$$\underbrace{\qquad \qquad }_{n_d\text{-}1} \qquad \underbrace{\qquad \qquad }_{n_e\text{-}n_d\text{+}1}$$

$$\mathbf{B_t} = \begin{matrix} (4) & 1 & 2 & 3 & 4 & 5 & 6 & 7 \\ (4) & 1 & 1 & -1 & 1 & 0 & 0 & 0 \\ (5) & -1 & -1 & 1 & 0 & 1 & 0 & 0 \\ (6) & 0 & 1 & -1 & 0 & 0 & 1 & 0 \\ (7) & -1 & -1 & 0 & 0 & 0 & 1 \end{matrix}$$

$$\mathbf{Q}_1 = -\mathbf{B}_1^{\mathrm{T}} \qquad \qquad \mathbf{B}_1 = -\mathbf{Q}_1^{\mathrm{T}}$$

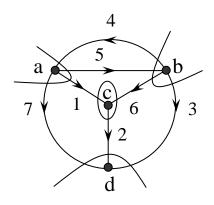
Property: $Q_tB_t^T=0 \iff B_tQ_t^T=0$

$$\mathbf{Q}_{t}\mathbf{B}_{t}^{\mathrm{T}} = [\mathbf{U} \ \mathbf{Q}_{1}][\mathbf{B}_{1} \ \mathbf{U}]^{\mathrm{T}} = [\mathbf{U} \ \mathbf{Q}_{1}]\begin{bmatrix} \mathbf{B}_{1}^{\mathrm{T}} \\ \mathbf{U} \end{bmatrix}$$
$$= \mathbf{B}_{1}^{\mathrm{T}} + \mathbf{Q}_{1} = \mathbf{B}_{1}^{\mathrm{T}} + (-\mathbf{B}_{1}^{\mathrm{T}}) = \mathbf{0}$$

Incidence Matrix

Choose all the cut-sets in such a way that one of the parts related to the cut-set consists of a single vertix. The fundamental loop matrix of this specific cut-set is the node incidence matrix, A.

✓ Dimensions of A is $(n_d) x (n_e)$



$$\overline{\mathbf{A}} = \begin{matrix} (a) & 1 & 2 & 3 & 4 & 5 & 6 & 7 \\ (a) & 1 & 0 & 0 & -1 & 1 & 0 & 1 \\ (b) & 0 & 0 & 1 & 1 & -1 & 1 & 0 \\ (c) & -1 & 1 & 0 & 0 & 0 & -1 & 0 \\ (d) & 0 & -1 & -1 & 0 & 0 & 0 & -1 \end{matrix}$$

4. Reduced incidence matrix, A

The matrix obtained by removing one of the rows of \overline{A} is called reduced incidence matrix

Reduced incidence matrix is a full rank matrix.

Property:

$$A = [\underbrace{A_1}_{branches} : \underbrace{A_2}_{co-trees}] \quad n_{d}-1 \Rightarrow \quad A_1^{-1}A = Q_T$$

$$A_1^{-1}A = [U : A_1^{-1}A_2] = Q_T = [U : Q_1]$$

$$\Rightarrow$$
 $Q_1 = A_1^{-1}A_2$

$$\mathbf{A} = [\quad \mathbf{A}_1 \quad \vdots \quad \mathbf{A}_2 \quad]$$

$$\mathbf{A_1} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 1 \\ -1 & 1 & 0 \end{bmatrix} \qquad A_2 = \begin{bmatrix} -1 & 1 & 0 & 1 \\ 1 & -1 & 1 & 0 \\ 0 & 0 & -1 & 0 \end{bmatrix}$$

$$\mathbf{Q_t} = \mathbf{A_1^{-1}A} = \begin{bmatrix} 1 & 0 & 0 \\ 1 & 0 & 1 \\ 0 & 1 & 0 \end{bmatrix} \begin{bmatrix} 1 & 0 & 0 & -1 & 1 & 0 & 1 \\ 0 & 0 & 1 & 1 & -1 & 1 & 0 \\ -1 & 1 & 0 & 0 & 0 & -1 & 0 \end{bmatrix}$$

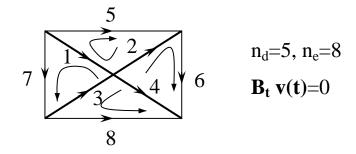
$$\mathbf{Q_{t}} = (2) \begin{bmatrix} 1 & 2 & 3 & 4 & 5 & 6 & 7 \\ 1 & 0 & 0 & -1 & 1 & 0 & 1 \\ 0 & 1 & 0 & -1 & 1 & -1 & 1 \\ 0 & 0 & 1 & 1 & -1 & 1 & 0 \end{bmatrix}$$
brances (U) co-trees (Q₁)

Second Postulate of Circuit Theory

The fundamental loop matrix of a chosen tree, \mathbf{B}_t and the vector composed of the element voltages $\mathbf{v}(t)$ in a given graph G satisfies:

$$\mathbf{B}_{\mathbf{t}} \mathbf{v}(\mathbf{t}) = \mathbf{0}$$

which is called fundemantal loop equations.



Fundamental loop equations are given by:

$$(5) \quad -v_1(t)-v_2(t)+v_5(t) = 0$$

(6)
$$v_2(t) - v_4(t) + v_6(t) = 0$$

(7)
$$-v_1(t)+v_3(t)+v_7(t) = 0$$

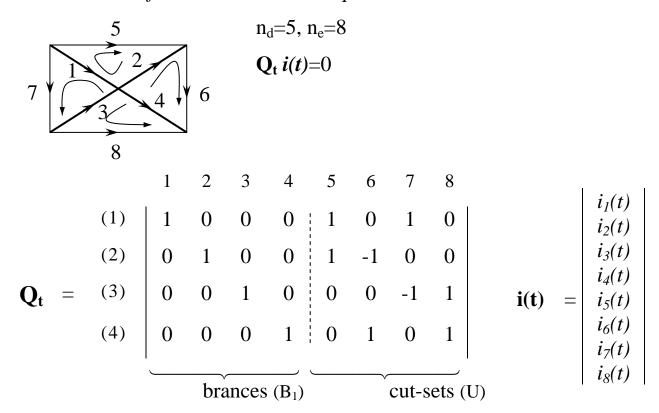
(8)
$$-v_3(t)-v_4(t)+v_8(t) = 0$$

Third Postulate of Circuit Theory

The fundamental cut-set matrix of a chosen tree, $\mathbf{B_t}$ and the vector composed of the element currents $\mathbf{i(t)}$ in a given graph G satisfies:

$$Q_t i(t) = 0$$

which is called fundemantal cut-set equations.



fundemantal cut-set equations are given by:

(1)
$$i_1(t) + i_5(t) + i_7(t) = 0$$

(2)
$$i_2(t) + i_5(t) - i_6(t) = 0$$

(3)
$$i_3(t) - i_7(t) + i_8(t) = 0$$

(4)
$$i_4(t) + i_6(t) + i_8(t) = 0$$

Since
$$Q_t = A_1^{-1}A$$

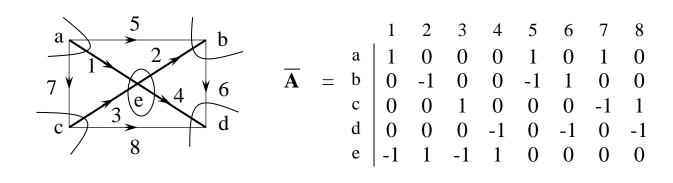
$$\mathbf{Q}_{\mathbf{t}} \mathbf{i}(\mathbf{t}) = \mathbf{0} \qquad \Leftrightarrow \qquad \mathbf{A} \mathbf{i}(\mathbf{t}) = \mathbf{0}$$

$$\begin{bmatrix} A_1 & A_2 \end{bmatrix} \begin{bmatrix} i_1(t) \\ i_2(t) \end{bmatrix} = 0$$
brances (B₁) cut-sets (U)

If we multiply both sides from left with A_1^{-1} ,

$$\mathbf{A}_1^{-1} \begin{bmatrix} \mathbf{A}_1 & \mathbf{A}_2 \end{bmatrix} \begin{bmatrix} \mathbf{i}_1(t) \\ \mathbf{i}_2(t) \end{bmatrix} = \begin{bmatrix} \mathbf{U} & \mathbf{A}_1^{-1} \mathbf{A}_2 \end{bmatrix} \begin{bmatrix} \mathbf{i}_1(t) \\ \mathbf{i}_2(t) \end{bmatrix} = \begin{bmatrix} \mathbf{U} & \mathbf{Q}_1 \end{bmatrix} \begin{bmatrix} \mathbf{i}_1(t) \\ \mathbf{i}_2(t) \end{bmatrix} =$$

$$\mathbf{Q}_{t} \begin{bmatrix} \mathbf{i}_{1}(t) \\ \mathbf{i}_{2}(t) \end{bmatrix} = \mathbf{Q}_{t} \mathbf{i}(t) = 0$$



Reduced incidence matrix, A

Incidence equations, A i(t) = 0

(a)
$$i_1(t) + i_5(t) + i_7(t) = 0$$

(b)
$$-i_2(t)-i_5(t)+i_6(t)=$$
 0

(c)
$$i_3(t) - i_7(t) + i_8(t) = 0$$

(d)
$$-i_4(t)-i_6(t)-i_8(t) = 0$$

$$\mathbf{A}_{1}^{-1} = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & -1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & -1 \end{bmatrix}$$

$$\mathbf{A_1^{-1}A_2} = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & -1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & -1 \end{bmatrix} \begin{bmatrix} 1 & 0 & 1 & 0 \\ -1 & 1 & 0 & 0 \\ 0 & 0 & -1 & 1 \\ 0 & -1 & 0 & -1 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 1 & 0 \\ 1 & -1 & 0 & 0 \\ 0 & 0 & -1 & 1 \\ 0 & 1 & 0 & 1 \end{bmatrix} = \mathbf{Q_1}$$

$$A_1^{-1} \begin{bmatrix} A_1 & A_2 \end{bmatrix} \begin{bmatrix} i_1(t) \\ i_2(t) \end{bmatrix} = \begin{bmatrix} U & A_1^{-1} A_2 \end{bmatrix} \begin{bmatrix} i_1(t) \\ i_2(t) \end{bmatrix} = i_1(t) + Q_1 i_2(t) = 0$$

Tellegen's Theorem

Assume that two different circuits have the same graph G.

Circuit D

Circuit D'

$$\mathbf{B}_{t}\mathbf{v}(t)=\mathbf{0}$$

$$\mathbf{B}_{t} \mathbf{v}'(t) = \mathbf{0}$$

$$Q_t i(t) = 0$$

$$Q_t i'(t) = 0$$

 $(\mathbf{v}(\mathbf{t}), \mathbf{i}(\mathbf{t}) \text{ ile } \mathbf{v}'(\mathbf{t}), \mathbf{i}'(\mathbf{t}) \text{ farklı})$

Explicitly,

$$\begin{bmatrix} B_1 & \mathbf{U} \end{bmatrix} \begin{bmatrix} \mathbf{v}_1(t) \\ \mathbf{v}_2(t) \end{bmatrix} = \mathbf{0}$$

$$\begin{bmatrix} \mathbf{B_1} & \mathbf{U} \end{bmatrix} \begin{bmatrix} \mathbf{v'_1}(t) \\ \mathbf{v'_2}(t) \end{bmatrix} = \mathbf{0}$$

$$\begin{bmatrix} \mathbf{U} & \mathbf{Q}_1 \end{bmatrix} \begin{bmatrix} \mathbf{i}_1(\mathbf{t}) \\ \mathbf{i}_2(\mathbf{t}) \end{bmatrix} = \mathbf{0}$$

$$\begin{bmatrix} \mathbf{U} & \mathbf{Q}_1 \end{bmatrix} \begin{bmatrix} \mathbf{i'}_1(\mathbf{t}) \\ \mathbf{i'}_2(\mathbf{t}) \end{bmatrix} = \mathbf{0}$$

$$\mathbf{v}_2(\mathbf{t}) + \mathbf{B}_1 \, \mathbf{v}_1(\mathbf{t}) = \mathbf{0}$$

$$v'_{2}(t) + B_{1} v'_{1}(t) = 0$$

$$i_1(t) + Q_1 i_2(t) = 0$$

$$i'_1(t) + Q_1 i'_2(t) = 0$$

$$v_2(t) = -B_1 v_1(t)$$

$$v'_{2}(t) = -B_{1} v'_{1}(t)$$

$$\mathbf{i}_1(\mathbf{t}) = -\mathbf{Q}_1 \, \mathbf{i}_2(\mathbf{t})$$

$$i'_1(t) = -Q_1 i'_2(t)$$

$$v^T(t) i(t) = \begin{bmatrix} v_1^T(t) & v_2^T(t) \end{bmatrix} \begin{bmatrix} i_1(t) \\ i_2(t) \end{bmatrix} = v_1^T(t) \, i_1(t) + v_2^T(t) \, i_2(t) =$$

$$v_1^{T}(t) [-Q_1 i_2(t)] + [-v_1^{T}(t) B_1^{T}] i_2(t) =$$

$$v_1^T(t) B_1^T i_2(t) - v_1^T(t) B_1^T i_2^T(t) = 0$$
 ($Q_1 = -B_1^T$)

Thus
$$\mathbf{v}^{\mathrm{T}}(\mathbf{t})\mathbf{i}(\mathbf{t}) \equiv \mathbf{0}$$

Similarly, we can deduce the followings for the circuit D'

$$\mathbf{v'}^{\mathrm{T}}(t) \mathbf{i'}(t) \equiv \mathbf{0}$$

$$\mathbf{v}^{\mathrm{T}}(\mathbf{t}) \; \mathbf{i}'(\mathbf{t}) \equiv \mathbf{0}$$

$$\mathbf{v'}^{\mathrm{T}}(t) \mathbf{i}(t) \equiv \mathbf{0}$$

From these expressions, we can obtain the following property:

In an electrical circuit, the sum of instantanous power equals zero,

i.e.
$$p(t) = \mathbf{v(t)}^{\mathbf{T}} \mathbf{i(t)} = \sum_{k=1}^{n_e} \mathbf{v_k(t)} \mathbf{i_k(t)} \equiv \mathbf{0}$$

Energy generated = energy dissipated (the law of conservation of energy)