

BLG212E Microprocessor Systems

HW#1

Due Date: 09.10.2017, 13:00

A 64K x 8 memory map includes the following components:

- Two 8K x 8 read/write memory chips which are located consecutively starting from the address \$0000
- A 4K x 8 read-only memory chip which ends at the address \$FFFF

a) Draw the memory map.

b) Draw the connections (address/data buses, read/write, CS) to the CPU including address decoder for the memory chips.