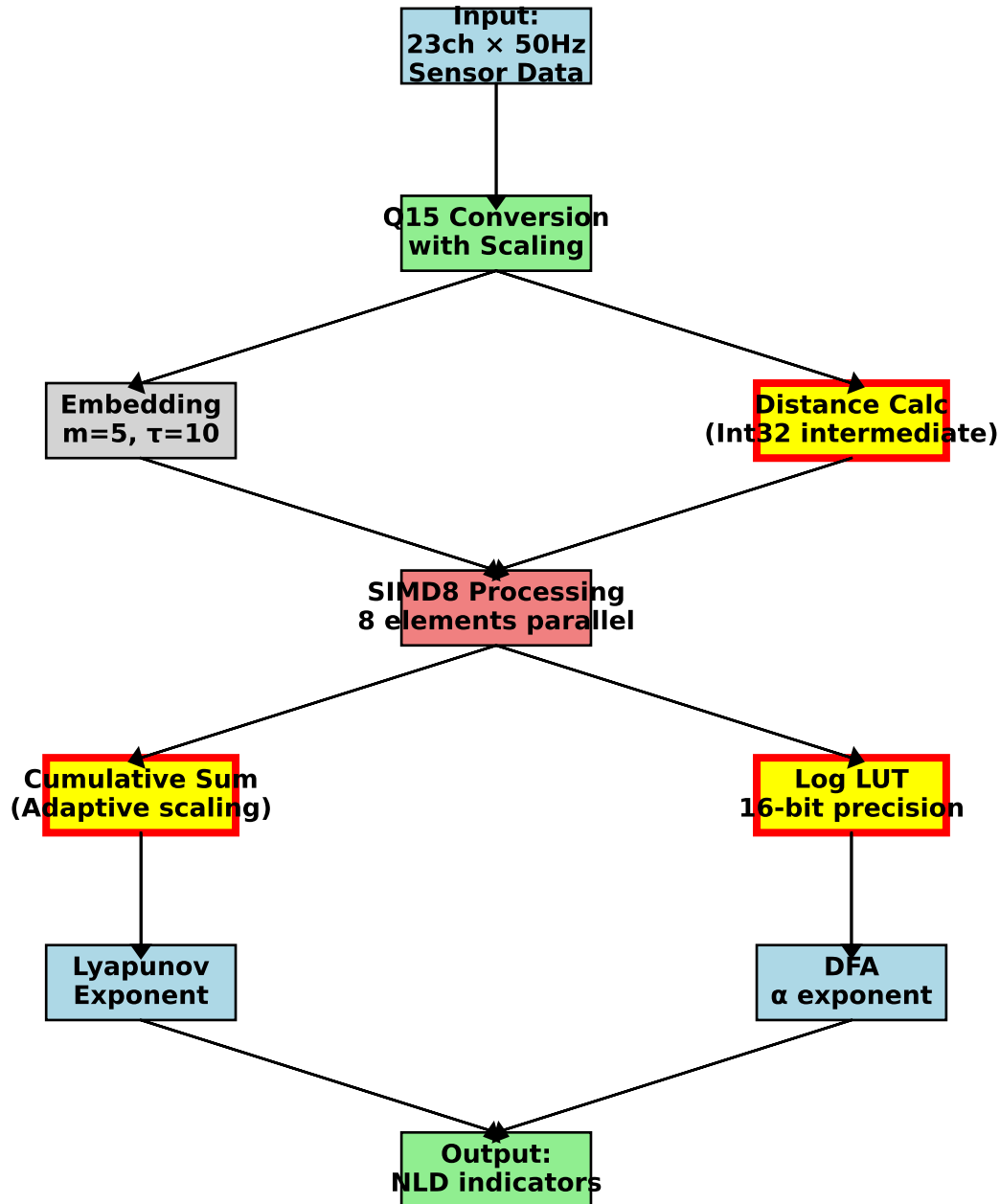


# Q15-SIMD Optimization Flow with Novel Elements



## Novel contributions (vs CMSIS-DSP):

1. Int32 intermediate arithmetic prevents saturation
2. DFA-specific memory access pattern
3. 16-bit log LUT without floating-point