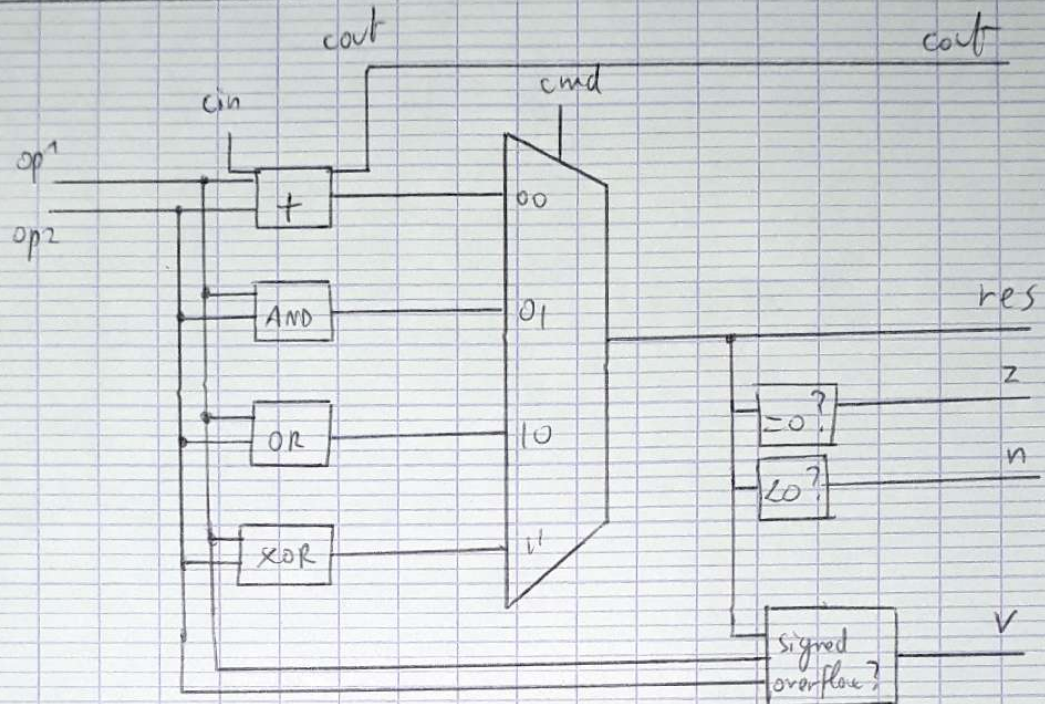
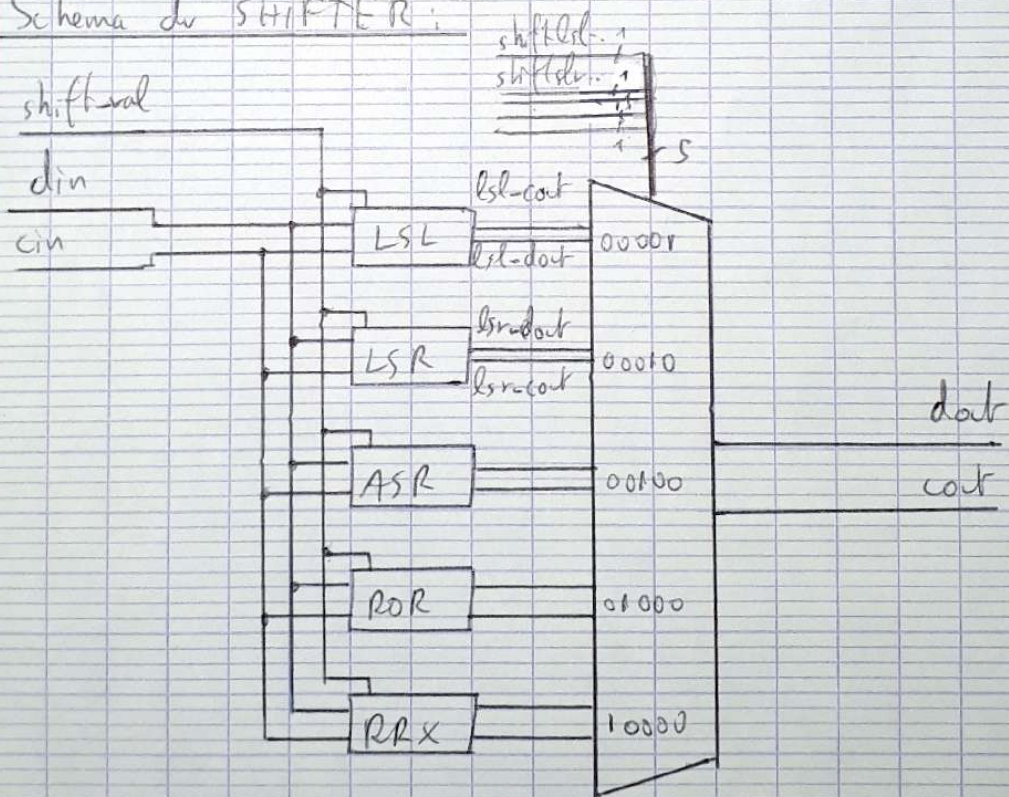


VLSI
TP 3

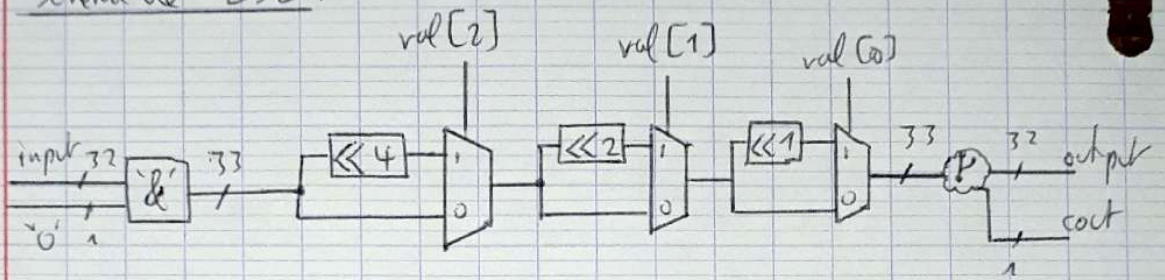
Schema de l'ALU:



Schema du SHIFTER:



Schema de LSL :



in principe pour LSR, ASR, ROR, RRL.