

15F CST8216 Processor
Architecture Term Test
Tuesday, November 17, 2015

Instructions:

1. Place your name and student number in the space provided.
2. Ensure that your copy of the test contains a total of 5 pages, including this cover page.
3. There are a total of 40 marks available.
4. Marks for each of the questions vary, and where noted you must show all of your work to be considered for full marks. Clearly indicate your answer to the questions.
5. A handout of the HCS12/9S12 Instruction Set Reference and other supplemental information is also provided with this test.
6. This is a closed book test.
7. No Scrap paper is permitted to be used during this term test – use the back of any page if you need extra space to solve problems.
8. You may take this test apart if you wish, I have a stapler at the front of the class.
9. If you have any questions, raise your hand.
10. Calculators are permitted to be used in this test.
11. Time limit: 50 minutes.

The use of electronic devices, with the sound turned on, during classes is strictly prohibited. In particular, cell phones are not to be used to communicate during a class. The use of any electronic devices during exams and mid-term tests, other than those sanctioned by the faculty in charge of the examination, is strictly prohibited.

Anyone caught using a prohibited device will be considered to have plagiarized, and will be treated as such in accordance with College Plagiarism Policy.

This test is worth 10% of your overall course mark.

Name _____

Student Number _____

Circle Your Lab Day/Time Tue 10 – 12 Wed 1 – 3 Wed 3 – 5 Fri 2 – 4

Version A
Solution

Number Conversions – (5 marks) Perform the following number conversions, placing your answers in the Value column:

Convert	To	Value
\$F9 (unsigned)	Integer	249
\$F9 (signed)	Integer	-7
\$3PFE (unsigned)	Integer	16383
72	8-bit Hexadecimal (unsigned)	\$48
-72	8-bit Hexadecimal (signed – use 2's complement)	\$B8
-6	8-bit Hexadecimal (signed – use 2's complement)	\$FA
128	Binary	721000 0000
-128	Binary (use 2's complement)	21000 0000
11111111 (signed)	Integer	-1
11111111 (unsigned)	Integer	255

BCD Arithmetic Methods – (5 marks) To confirm your understanding of BCD Arithmetic methods, represent 135₁₀ and 265₁₀ in BCD and then correctly perform BCD addition. Show all steps (including all annotations to the right of the additions) for full credit.

$$\begin{array}{r}
 135_{10} \rightarrow 0001 \ 0011 \ 0101 \leftarrow \text{BCD } 135 \\
 + 265_{10} \rightarrow 0010 \ 0110 \ 0101 \leftarrow \text{BCD } 265 \\
 \hline
 0011 \ 1001 \ 1010 \leftarrow \text{Invalid BCD} \\
 \text{Add 6} \\
 \hline
 + 0000 \ 0000 \ 0110 \\
 \hline
 0011 \ 1010 \ 0000 \leftarrow \text{Carry out} \\
 + 0000 \ 0110 \ 0000 \leftarrow \text{CAUSED INVALID} \\
 \hline
 0100 \ 0000 \ 0000 \leftarrow \text{BCD } 400 \\
 \hline
 4 \qquad \qquad \qquad 0
 \end{array}$$

Given the following program listing, trace the results (hexadecimal value of registers being asked) of each program step **after** the line of code has been executed, as per the example answer for SP. (*Instruction Set included with this Term Test*).

Version A

Given the following source code listing, indicate the LEDs' output pattern for two complete iterations of the `for` (`for` :: `forever`) loop by completing the table that follows the code listing. Note that this is fully functional code, which correctly assembles and outputs a specific pattern on Port B's LEDs.

To indicate that a specific LED is ON, place an X in the appropriate box in the table as per the following example *which you should note is NOT part of the pattern produced by the code listing*. If a specific LED is OFF, leave the appropriate box in the table blank.

```

; Walk, A.Btc, Structured by Short_Version.asm
#include C:\68k32\1710\register.inc
; Program Constants
STRK equ $2500
; Delay value (base 10) 0 - 255 ms
DELAY equ $250
; data area
org $1000
; values to display on LEDs
LEDBLE db $00, $18, $24, $42, $81

; Continually flash LEDs with values from LEDBABLE, changing values every 250 ms
; For (??)
Back ledn ledbale
; point to first element of LEDBABLE
; get first value, increment pointer
; output value to LEDs
; delay value
; delay routine
; get ans to do
; loop
; endless loop
Back

; Presetted Shortest's Policy
#include C:\68k32\1710\Contig_Swz_and_LEDs.asm
#include C:\68k32\1710\Delay_Ms.asm
end

```

Indicate the LEDs' output pattern by completing this table (1 mark per correct line):

Version A

```

1 ; Trace2_VA.asm
2
3 ; Name      David Haley
4 ; S/N      Faculty
5 ; Date      8 Nov 2015
6
7 ; Purpose   To manually trace through given Assembly Language code
8
9 Data1 db    $32, $14, $98, $9D
10 Data2 db    $CE, $D8, $53, $7C, $01, $23, $02, $98
11
12
13
14 org        $2000
15 lds        ; SP = ?
16
17 ldy        $Data1
18
19 ldaa        0,Y
20
21
22 ldba        1,Y+
23
24
25 ldx        $Data2
26
27 ldaa        4,X
28
29
30 lnx
31
32 ldd        6,X+
33
34
35
36
37
38
39 swi
40 end
41

```

Version B
Solution

Instructions

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Name

Student Number

Circle Your Lab Day/Time Tue 10 – 12 Wed 1 – 3 Wed 3 – 5 Fri 2 – 4

Number Conversions – (5 marks) Perform the following number conversions, placing your answers in the Value column:

Convert	To	Value
\$F8 (unsigned)	Integer	248
\$F8 (signed)	Integer	-8
\$JFFF (unsigned)	Integer	8191
92	8-bit Hexadecimal (unsigned)	\$5C
-92	8-bit Hexadecimal (signed – use 2's complement)	\$A4
-6	8-bit Hexadecimal (signed – use 2's complement)	\$FA
128	Binary	21000000
-128	Binary (use 2's complement)	21000000
11111110 (signed)	Integer	-2
11111110 (unsigned)	Integer	254

BCD Arithmetic Methods – (5 marks) To confirm your understanding of BCD Arithmetic methods, represent 265₁₀ and 135₁₀ in BCD and then correctly perform BCD addition. Show all steps (including all annotations to the right of the additions) for full credit.

265₁₀ + 135₁₀

0010 0110 0101 ← BCD 265
 + 0001 0011 0101 ← BCD 135

 0011 1001 1010 ← INVALID BCD
 + 0000 0000 0110

 0011 1010 0000 CAUSED INVALID
 + 0000 0110 0000 BCD, ADD 60

 0100 0000 0000 ← BCD 400

Program Tracing (10 marks)

Given the following program listing, trace the results (hexadecimal value of registers being asked) of each program step **after** the line of code has been executed, as per the example answer for SP. (Instruction Set included with this Term Test).

```

1 ; Trace1_VB.asm
2 ; Name      David Haley
3 ; S/N       Faculty
4 ; Date      7 Nov 2018
5
6 ; Purpose   To manually trace through given Assembly Language code
7
8 VALUE1 equ $5678
9 VALUE2 equ $21
10
11 org $1000
12 Source db $0011100, $82, $54, 128
13 Source2 dw $1234
14
15
16
17 org $2000
18 lds $2000
19
20 ldd #value1
21
22
23
24
25
26
27
28
29 ldaa Source+3
30
31 ldsb $1000
32
33 ldx Source2
34
35 ldx #Source2
36
37 ldaa #502
38 ldsb #501
39
40 exg a,b
41
42 ldy Source+4
43
44
45 end

```

Example for lds

17 org \$2000 ; S = \$2000
 18 lds \$2000 ; A = \$56
 19 ; B = \$78
 20 ldd #value1 ; A = \$CE
 21 ; B = \$78
 22 ; A = \$80
 23 ; B = \$3C
 24 ; A = \$1234
 25 ; B = \$1004
 26 ; A = \$1234
 27 ; B = \$1004
 28 ; A = \$1234
 29 ; B = \$1004
 30 ; A = \$1234
 31 ; B = \$1004
 32 ; A = \$1234
 33 ; B = \$1004
 34 ; A = \$1234
 35 ; B = \$1004
 36 ; A = \$1234
 37 ; B = \$1004
 38 ; A = \$1234
 39 ; B = \$1004
 40 ; A = \$1234
 41 ; B = \$1004
 42 ; A = \$1234
 43 ; B = \$1004
 44 ; A = \$1234
 45 ; B = \$1004

Given the following source code listing, indicate the LED's output pattern for two complete iterations of the for (:) [forever] loop by completing the table that follows the code listing. Note that this is only functional code, which correctly assembles and outputs a specific pattern on Port B's LEDs.

To indicate that a specific LED is ON, place an 'X' in the appropriate box in the table as per the following example which was specifically not the pattern produced by the code listing. If a specific LED is OFF, leave the appropriate box in the table blank.

Time (msec)	P87	P86	P85	P84	P83	P82	P81	P80
3000	X			X			X	

[illegible]

Indicate the LEDs' output pattern by completing this table (1 mark per correct line):

Time (msec)	P87	P86	P85	P84	P83	P82	P81	P80
0	X						X	X
250		X					X	
500			X			X		
750				X	X			
1000								
1250	X							X
1500		X					X	
1750			X			X		
2000				X	X			
2250								

080

G A	A A	A
C C	E E	D
O O	F F	-

Expanded view of
LEDTABLE

\$81, \$42, \$24, \$18, \$00

TABLE
number

1000 0001
0100 0010
0010 0100
0001 1000
0000 0000

Given the following program listing, trace the results (hexadecimal value of registers being asked) of each program step **after** the line of code has been executed, as per the example answer for SP. (Instruction Set included with this Term Test).

1	; Trace1_VB.asm
2	
3	David Haley
4	Faculty
5	8 Nov 2015
6	
7	Purpose
8	To manually trace through given Assembly Language code
9	org \$1000
10	Data1 db \$02, \$33, SEI, \$06, \$01
11	DBData2 db \$04, \$99, SEI, \$50, SEI, \$01, \$23, \$94, \$01
12	
13	; All Values must be in Hexadecimal as per the example for ids
14	
15	org \$2000
16	
17	ldy #Data1
18	
19	ldaa 1,y+
20	
21	
22	
23	ldab 0,y
24	
25	
26	ldx #Data2
27	
28	
29	ldaa 4,x
30	
31	inx
32	
33	
34	ldd 4,x+
35	
36	
37	
38	
39	swi
40	end

\$1000	
\$2000	
\$02	
\$33	
\$06	
\$01	
\$04	
\$99	
\$50	
\$01	
\$23	
\$94	
\$01	
\$100A	
\$9F	
\$100A	