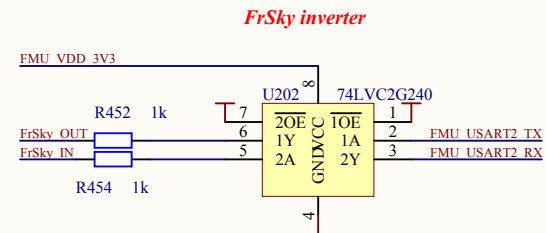
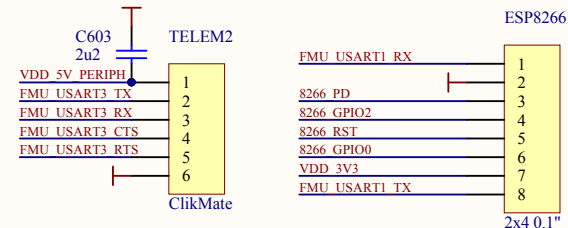
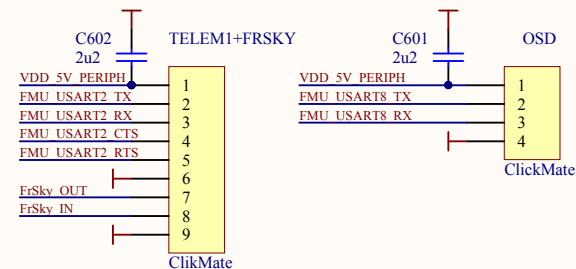
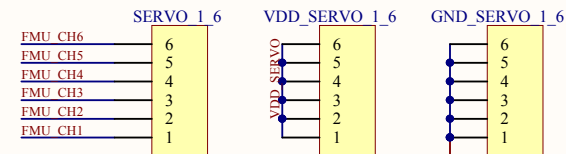
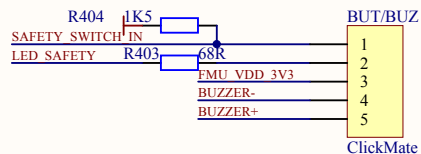
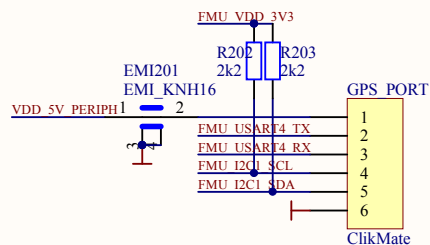


The diagram shows a 74LVC1G57 hex inverter buffer. The input of the buffer is connected to the RC\_INPUT signal through a 10k pull-up resistor. The output of the buffer is connected to the FMU\_RC\_INPUT signal. The buffer is powered by FMU\_VDD\_3V3 and its ground is connected to GND.



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