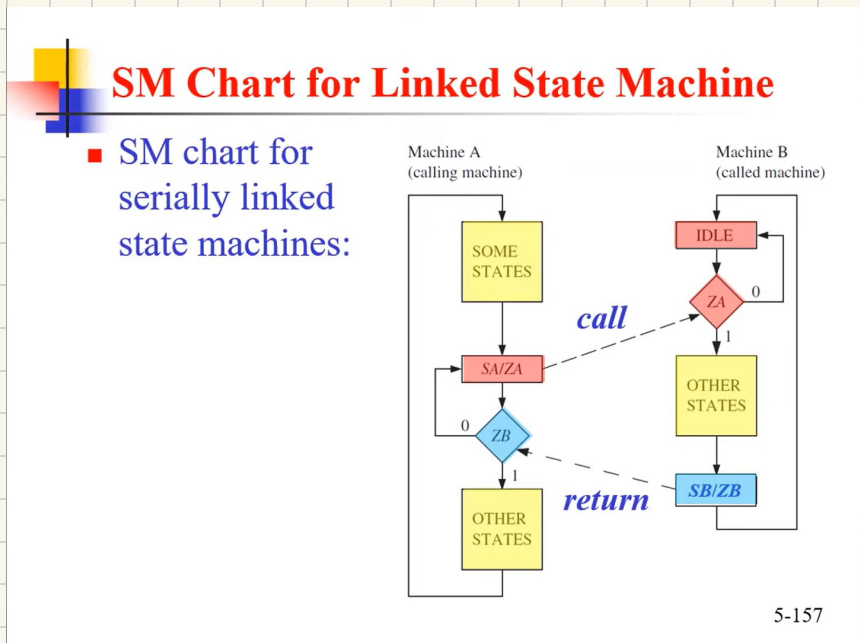


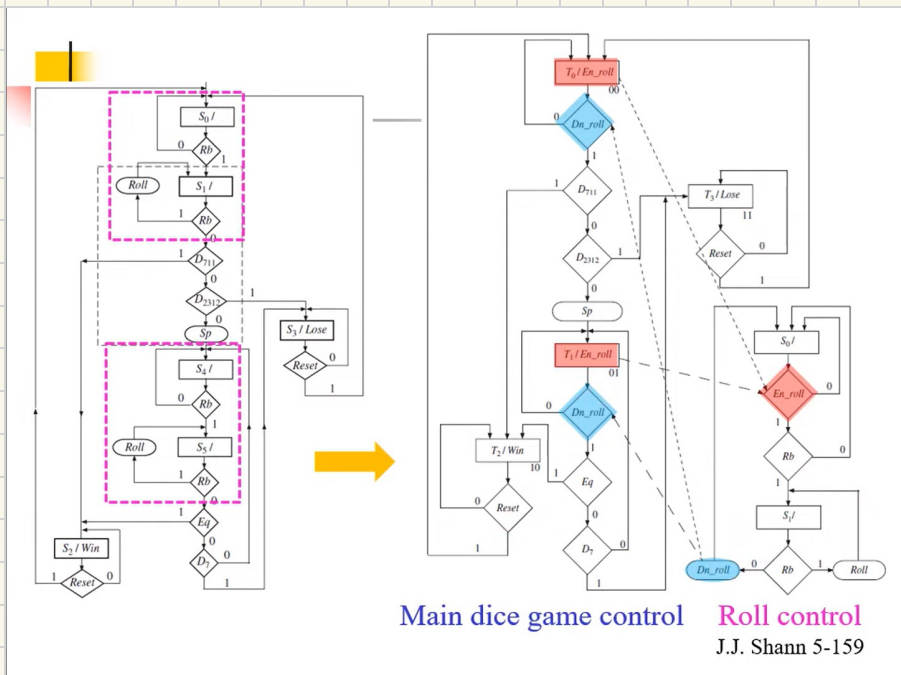
5.6 Linked State Machines



Introduction

- Divide a sequential machine into several smaller machines linked together when the machine becomes large and complex.
- Each of the smaller machines is easier to design and implement.
- One of the submachines may be "called" in several different places by the main machine.





2 Summary

Procedures for digital system design:

1. block diagram (define I/o)
2. flowchart / procedure
3. SM chart or state graph
4. behavioral Verilog description
5. test bench
6. implementation (fixed circuitry, faster or hardware or microprogramming)

read from memory, slower