

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



13 198473

KARNATAKA, INDIA

STATEMENT OF MARKS BE First Semester January 2013 Electrical & Electronics Engineering

Seat No.:

10X12EE034

Name of the College OXFORD COLLEGE OF ENGINEERING, J.P. NAGAR, BANGALORE Father's / Mother's Name :

GIRISH HEGDE

KEERTHI HEGDE G

Name of the Student

		80	7 1	6	Ø	4	ω	N	-	
		10CIV18	10CHEL17	10CPL16	10ELN15	10CED14	10CCP13	10CHE12	10MAT11	0000
		Environmental Studies	Engineering Chemistry Lab	Computer Programming Lab	Basic Electronics	Computer Aided Engineering Drawing	Computer Concepts & C Programming	Engineering Chemistry	Engineering Mathematics - I	IIIle
		ch ch	50	(II)	=	_		_	_	M
	27	50	8	8	100	8	9	9	8	Max.
		18	8	26	35	35	35	35	35	MID.
		41	39	43	48	66	46	52	60	Obtained
GRAND	,	8	23	25	13	25	8	83	R	Max.
GRAND TOTAL	1	,	12	12	•	1	1	1	1	MID.
	7	17	经	24	23	24	23	18	23	Obtained
775		75	75	75	125	125	33	125	133	Max.
314		30	32	32	8	5	50	50	50	WIII.
511		58	සු	67	2	8	68	73	83	Optained
		Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	nesuit

Registrar (Evaluation)

Result of the Semester

DATE:

MAY 14,2013

06/10/CIP/CIV18 is not considered for Grand Total and the Class Declaration

A - Absent NE - Not Eligible

Total Marks Obtained (in words) : FIVE HUNDRED AND ELEVEN ONLY

FIRST CLASS



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM ಎಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ಎಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



រ្ទ 545637

KARNATAKA, INDIA

STATEMENT OF MARKS
BE Second Semester July 2013
Electrical & Electronics Engineering

Name of the Student KEERTHI HEGDE G

Father's / Mother's Name : GIRISH HEGDE

Seat No. :

10X12EE034

Name of the College OXFORD COLLEGE OF ENGINEERING, J.P. NAGAR, BANGALORE

		80	7	თ	(J)	4	w	N	-	No.	SI.
		10CIP28	10PHYL27	10WSL26	10ELE25	10EME24	10CIV23	10PHY22	10MAT21	Code	
		Constitution of India & Professional Ethics	Engineering Physics Lab	Workshop Practice	Basic Electrical Engineering	Elements of Mechanical Engineering	Elements of Civil Engg. & Engg. Mechanics	Engineering Physics	Engineering Mathematics - II	Title	Subject
25	7.0	ජ	50	50	100	100	100	100	100	Max.	Exami
		18	8	8	ਲ	83	35	35	35	Min.	nation
		31	50	33	77	57	83	76	52	Obtained	Examination Marks
GRAN		83	25	25	25	23	25	133	R	Max.	Internal
GRAND TOTAL		ı	12	12	•	,			1	Min.	Assessi
_		19	ß	33	18	8	23	8	83	Obtained	Internal Assessment Marks
775	4	75	75	75	125	133	123	125	33	Max.	
314		30	32	32	8	8	5	50	8	Min	Total Marks
586		8	75	88	88	79	106	82	π	Obtained	rks
		Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	Hesult	Subject



Result of the Semester

DATE: OCT 10,2013

Total Marks Obtained (in words) : FIVE HUNDRED AND EIGHTY SIX ONLY

: FIRST CLASS WITH DISTINCTION

06/10/CIP/CIV28 is not considered for Grand Total and the Class Declaration

A - Absent

NE - Not Eligible





Name of the Student

Father's / Mother's Name : GIRISH HEGDE

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

KARNATAKA, INDIA

154822

STATEMENT OF MARKS

BE Third Semester January 2014

Seat No.: 10X12EE034

KEERTHI HEGDE G Electrical & Electronics Engineering

Name of the College OXFORD COLLEGE OF ENGINEERING, J.P. NAGAR, BANGALORE

Title N 33-III its Instrumentation ion	GRAND TOTAL 900 364 640	900		GRAND TOTAL	GRAND TOTA	GRAN	1	1					
Title Max. Min. Obtained Max. Min.													
Title Max. Min. Obtained Max. Min.		4							7				
Title Max. Min. Obtained Max. Min.			ing transferred										
Title Max. Min. Obtained Max. Min.	Pas	8	33	75	K	12	33	45	8	50	18 Logic Design Lab	10ESL38	œ
Max. Min. Obtained Max.	Pass	61	32	75	K	12	8	37	8	55	77 Analog Electronics Lab	10ESL37	7
le Max. Min. Obtained Max. Min. <	Pas	8	5	133	8	1	B	40	33	100	6 Electric Power Generation	10EE36	6
le Max. Min. Obtained Max. Min. <	Pass	72	g	136	×	,	R	48	쏤	100	E & E Measurements & Instrumentation	10EE35	ď
Max. Min. Obtained Max.	Pas	91	g	123	23	ì	R	8	器	100	Network Analysis	10ES34	4
lll Max. Min. Obtained Max. Mln. Obtained Max. Mln. Obtained lll Obtained Max. Mln. Obtained Max. Mln. Obtained lll Obtained lll Obtained Max. Mln. Obtained Max. Mln. Obtained Max. Mln. Obtained lll Obtained Max. Mln. Obta	Pas	107	8	133	23	ı	B	85	윉	100	3 Logic Design	10ES33	w
Max. Min. Obtained Max. Min. Obtained Max. Min. Obtained	Pas	81	ස	133	R	1	B	85	윉	100	2 Analog Electronic Circuits	10ES32	N
Title Max. Min. Obtained Max. Min. Obtained Max. Min. Obtained	Pas	83	5	123	8	1	3	74	35	3	Engineering Mathematics-III	10MAT31	-
	Resu	Obtained	Min.	1	Obtained	Min.		Obtained	Min.	Max.		Code	NO.
Examination Marks Internal Assessment Marks Total Marks	Subject	rks	otal Ma	ı	nent Marks	Assessn	Internal	Marks	nination	Exan	Subject		<u>S</u>



A - Absent NE - Not Eligible

Result of the Semester

DATE:

APR 05,2014

Total Marks Obtained (in words) : SIX HUNDRED AND FORTY ONLY

: FIRST CLASS WITH DISTINCTION





VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

KARNATAKA, INDIA

STATEMENT OF MARKS
BE Fourth Semester July 2014
Electrical & Electronics Engineering

Seat No.: 10X12EE034

529588

Name of the College OXFORD COLLEGE OF ENGINEERING, J.P. NAGAR, BANGALORE

Father's / Mother's Name :

GIRISH HEGDE

KEERTHI HEGDE G

Name of the Student

	June 194	and any other than		-			2.2	L. L			_	and the same
Total N			80	7	6	5	4	3	2	_	No.	SI.
Marks Obtained (i			10EEL48	10ESL47	10EE46	10EE45	10EE44	10ES43	10ES42	10MAT41	Code	
Total Marks Obtained (in words) : FIVE HUNDRED AND EIGHTY FIVE ONLY			Power Electronics Lab	Microcontrollers Lab	Transformers and Induction Machines	Power Electronics	Field Theory	Control Systems	Microcontrollers	Engineering Mathematics - IV	Title	Subject
FIVE ONLY			(7)	<u></u>	_	_	_	_	_	_	M	
•			8	8	8	8	8	9	90	100	Max.	Exami
	1		8	20	쓩	8	쓠	8	8	었	Min.	nation
			46	49	59	51	5	2	42	69	Obtained	Examination Marks
	GRAN	a T	8	R	8	8	8	8	3	R	Max.	Internal
	GRAND TOTAL		12	12	١	•	,	,	١	•	Min.	Assessm
			21	23	20	18	23	23	23	R	Obtained	Internal Assessment Marks
	99		75	75	125	133	125	3	128	136	Max.	
,	364		33	32	8	8	8	8	8	8	Min.	Total Marks
	585		67	71	79	8	73	77	83	22	Obtained	rks
			Pass	Pass	Pass	Pass	Pass	Pass	Pass	Pass	Result	Subject
101	E 33	ana	To be			T A		7.5		477	Q.	Mar.



A - Absent NE - Not Eligible

DATE:

SEP 21,2014

Result of the Semester

: FIRST CLASS





VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

15 091372

Seat No.: 10X12EE034

STATEMENT OF MARKS KARNATAKA, INDIA

Electrical & Electronics Engineering BE Filth Semester January 2015

Father's / Mother's Name : GIRISH HEGDE KEERTHI HEGDE G

Name of the Student

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OXFORD COL
LEGI
0
OXFORD COLLEGE OF ENGINEERING, J.P. NAGAR, BAN
J.P
NAGAR,
BANGALORE

1		GRAND TOTAL	GRA					
		100						
12		B		46	8	55	Transformers & Induction Machines Lab.	10EEL58
25 12 22		B		2	8	8	Measurements & Circuit Simulation Lab.	10EEL57
- 26	. 8	K		56	35	100	Linear IC's & Applications	10EE56
, K	B	B		77	33	ig 8	Modern Control Theory	10EE55
- 23	. 3	R		59	35	8	D. C. Machines & Synchronous Machines	10EE54
- 23	. 33	R		74	33	100	Transmission & Distribution	10EE53
- 22	. 23	33		53	33	100	Signals & Systems	10EE52
. 53	,	R		8	35	100	Management & Entrepreneurship	10AL51
Max. Min. Obtained	Min.	Max		Min. Obtained	Min.	Max.	Title	Code
Internal Assessment Marks	nternal Assessment	nterna	_	Examination Marks	ninatio	Exan	Subject	

A - Absent

NE - Not Eligible

DATE:

APR 13,2015

Result of the Semester

Total Marks Obtained (in words) : SIX HUNDRED AND SIXTY THREE ONLY

: FIRST CLASS WITH DISTINCTION





KARNATAKA, INDIA

15 856156

STATEMENT OF MARKS

BE Sixth Semester July 2015

Electrical & Electronics Engineering

Seat No. : 10X12EE034

Name of the College OXFORD COLLEGE OF ENGINEERING, J.P. NAGAR, BANGALORE

Father's / Mother's Name :

GIRISH HEGDE

KEERTHI HEGDE G

Name of the Student



A - Absent NE - Not Eligible

Result of the Semester

DATE:

NOV 01,2015

Total Marks Obtained (in words) : SIX HUNDRED AND EIGHTY FOUR ONLY

FIRST CLASS WITH DISTINCTION





KARNATAKA, INDIA

16 019157

BE Seventh Semester January 2016 Electrical & Electronics Engineering STATEMENT OF MARKS

Seat No.: 10X12EE034

Name of the Student KEERTHI HEGDE G

Father's / Mother's Name : GIRISH HEGDE

Name of the College OXFORD COLLEGE OF ENGINEERING, J.P. NAGAR, BANGALORE

Total Marks Obtained (in words): SIX III INDBED AND SEVENTY FOLIR ONLY							- 1
			GRAND	GRANDTOTAL	GRANDTOTAL	GRAND TOTAL 900	al - 10 - 10 - 10
8	50 20		8	20 45	20 45 25	20 45 25 12	20 45 25 12 25
g	50 20		8	39	39 25	20 39 25 12	20 39 25 12 25
6	100 35		쓩	35 76	35 76 25 -	35 76 25 - 25	35 76 25 - 25
1 00	100 35	_	쓩	35 82	35 82	35 62 25	35 62 25 - 24
100	100 35		8 8	36 65	36 65	35 65 25 - 19	35 65 25 - 19
100	100 35		용	35 72	35 72	35 72 25 - 19	35 72 25 - 19
100	100 35		35 59	35 59	35 59 25 -	35 59 25 -	36 59 26 - 26 126
Computer Techniques in Power System Analysis 100		8	100 35	100 35 72	100 35 72	100 35 72 25 -	100 35 72 25 - 22
Max.		Max. Min. Obtained		Min. Obtained	Min. Obtained Max.	Min. Obtained Max. Min.	Min. Obtained Max. Min. Obtained
Exam	Examination	Examination Marks			Examination Marks Illiellid Assessificit Marks	III COLINE TO COCCUITO III III III	

FIRST CLASS WITH DISTINCTION

A - Absent NE - Not Eligible

DATE :

JUL 22 2016

Result of the Semester







VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

KARNATAKA, INDIA

F 16 021243

Seat No.: 10X12EE034



: KEERTHI HEGDE G

Name of the College OXFORD COLLEGE OF ENGINEERING, J.P. NAGAR, BANGALORE

Father's / Mother's Name: GIRISH HEGDE

N - No.	Code 10EE81 10EE82	Subject Title Electrical Design, Estimating & Costing Power System Operation and Control	Exam Max. 100	Examination Marks lax. Min. Obtain 100 35 53 100 35 51	Marks Obtained 53	Internal Max. 25	N. S.	Min.	Max. Min. Obtained 25 - 23 25 - 25	125	125	Tot. d Max. 125
WW	10EE82	Power System Operation and Control Renewable Energy Sources	1 1 2 3 3 3 3 3 3 3 3 3 3		8 8	1	6 51 64	6 51 64	51 25 -	51 25 - 25 1 26 - 23 1	64 25 - 25 125 64 26 - 23 125	51 25 - 25 125 50 64 26 - 23 125 50
4	10EE842	Energy Auditing & Demand Side Management	100		35	35 63	_ =	ස	ස	63 25 -	63 25 - 24	63 25 - 24 125
5	10EEP85	Project Work	100		40	40 92		93	92 100	92 100 50	92 100 50 96	92 100 50 96 200
6	10EES86 Seminar	Seminar	•		1	1	- 50	- 50 20	-	8	20 48	20 48 50

*First Attempt Total (in words) : FIVE HUNDRED AND SIXTY TWO ONLY

FIRST CLASS WITH DISTINCTION

Results of V to VIII Semesters:

Result of the Semester

					1
Semesters	<	≤	٧I	YII	MADIA
Marks at First Attempt	663	684	674	562	2583
Total Marks (Max.)	900	900	900	750	3450
Result	FCD	FCD	FCD	FCD	FCD

Result of the Degree First Attempt Total (Y to VIII Sem) in words: TWO THOUSAND FIVE HUNDRED AND EIGHTY THREE ONLY

FIRST CLASS WITH DISTINCTION

Date: SEP 24,2016

GRAND TOTAL

750

310

562

Registrar (Evaluation)