Padstack Usage Report Page 1 of 1

$\frac{Design\ Name}{Date}\ D: /JAKS\ jaya/Propagater/orcad\ lib/sensorboard final.brd\\ \underline{Date}\ Thu\ Jun\ 06\ 21:34:02\ 2019$

Total Padstack Instances

Padstack Name	Total Design Instances
110X110DUAL	2
60S85C35D	46
60X45DUAL	6
70S85C35D	42
PAD125	2
VIA	10

Detailed Padstack Usage

I Symbol Nama I	Padstack Name		X	Pin Numbers
JACK	110X110DUAL	2	2	1;2;
RESISTOR	60S85C35D	2	26	1;2;
REGULATOR	60S85C35D	3	3	1;2;3;
CONN3	60S85C35D	3	9	1;2;3;
CONN4	60S85C35D	4	4	1;2;3;4;
CONN2	60S85C35D	2	4	1;2;
BC547	60X45DUAL	3	6	1;2;3;
ICSOCKET	70S85C35D	40	40	1; 2; 3; 4; 5; 6; 7; 8; 9; 10; 11; 12; 13; 14; 15; 16; 17; 18; 19; 20; 21; 22; 23; 24; 25; 26; 27; 28; 29; 30; 31; 32; 33; 34; 35; 36; 37; 38; 39; 40;
SCREWCONN	70S85C35D	2	2	1;2;
MTG125	PAD125	1	2	· ,