VENKATESH G

Lead – Functional & Test Automation with 15+ years of experience

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Accomplished professional with rich experience in **Test Automation & DevOps, Pre & Post Silicon Validation Tools Development, Validation & Automation for Firmware & Software**

*Targeting leadership assignments as **Automation & CI** with an organization of repute*

***Location Preference:** Open to relocate to worldwide locations*

Executive Profile

- Extensively worked on **Test Automation & Continues Integration strategy** for Cisco Intersight, Cisco Unified Computing System
- Hands-on experience in **development of customer deployment and silicon validation tools** for **pre and post silicon validation**
- Insightful knowledge of Code Repository Tools like **Git/GitHub, Continues Integration** tools like **Jenkins** & **Containerization** tools like Docker & Containers
- Extensively worked on:
  - CLI and GUI test automation & qualification of storage features of **Cisco Rack Servers** and **Cisco Hyperflex**
  - Validation tools of **Intel Display subsystem;** designed & developed test strategy for firmware & library validation of **Intel Manageability Engine**
- Worked on development **Automation & Continues Integration strategy** for various programs
- Established & re-enforced standard test processes and deliverables (including defect management and quality metric reporting) within the gated process to streamline testing & support automation and testing efforts
- Possess in-depth knowledge of **Infrastructure Consulting & Server Virtualization**
- Expertise in **Automation Software** & **Silicon Validation Firmware** for Software Development Platforms, Evaluation Modules, PoC and Consumer Electronic Products from Tier1 OEMs
- Skilled in **Software Testing and team management** involving setting-up of test environment, preparation of test scripts/cases, execution of testing and defect tracking & closure

Career Timeline

SAMSUNG ELECTRONICS — Jun'00 – Nov'04

intel — Nov'04 – Nov'15

CISCO — Nov'15 till date

Key Impact Areas

- **Continuous Integration**
- **Test Automation & Validation**
- **Firmware Validation**
- **Tools Development**
- **Device Drivers Test Automation**
- **Pre & Post Silicon GFX Validation Tools**
- **Virtualization (ESXI)**
- **Stakeholder Management**
- **Team Building & Leadership**

Soft Skills

- **Analytical**
- **Communicator**
- **Innovator**

Education & Credentials

- **Accelerated General Management Program** from Indian Institute of Management, Ahmedabad in 2017-18
- **B.S. (Information Systems)** from Birla Institute of Technology and Science, Pilani, Rajasthan in 2003
- **Diploma in Computer Science & Engineering** from Sandur Polytechnic, Yeshwanthnagar, Karnataka in 1996

Technical Skills

Operating Systems: Windows, Linux Programming Languages: C/C++, Python Framework: QALi
IDE: Visual Studio, Eclipse, PyCharm
Tools: Visual Studio, Jenkins, JIRA, Docker & Containers, Git, GitHub
Others: Intel Integrated Display Architecture, Cisco Unified Computing

Notable Accomplishments Across the Career

At Cisco Systems India Private Limited, Bengaluru

- Steered efforts in current program for setting up **Continuous Integration Pipeline** in Cisco; defined Test Automation strategy; worked with stakeholders and DevOps Team to define Continues Integration and Continues deployment pipeline for Cisco UCSM
- Exhibited excellence in enabling Central Regression Team in Bengaluru for Cisco Intersight, a Cloud-Based Management Tool for managing all Cisco Data Centre Servers & Management Devices
- Effectively **enabled Test Automation of Cisco Unified Computing System Manager** Software to improve validation efficiency; enabled and lead validation engineers to ramp up of test automation & QALi framework

At Intel Technology India Private Limited, Bengaluru, Karnataka

- Spearheaded validation to support Display Validation in tool chain which enabled Intel Display validation for Intel SoC; rewarded with **Divisional Recognition Award**
- Worked extensively with Validation Engineers to automate Audio Output Verification resulting in saving test execution time in Intel
- Actively driven validation of module in tools stack to dump and parse firmware debug data of Graphics MicroController which helped Validation Engineers to debug hardware failures faster and save time during critical debugs
- Successfully **designed architecture of new tool set to replace legacy tools resulting in improving performance** and debugging capabilities; drove methodologies for coverage and concurrency tool development

Organizational Experience

Cisco Systems India Private Limited, Bengaluru **Nov'15 till date**
Software Engineer (QA Functional & Automation Lead)
Key Result Areas:

- Heading Rapid Regression Team of Cisco Intersight; performing functional test scripts development reviews, daily/weekly regression, failure analysis and reporting
- Leading the effort to build new test frameworks and or extend existing frameworks
- Preparing reusable functions, which improve the robustness, re-usability, and maintainability of their test scripts
- Steering complete Working for UCSM Rack QA group from Nov 2015
- Driving test automation effort for validation of Cisco UCSM, Cisco UCS Server Management Software and Cisco Security development life cycle tools; leading automation of UCSM tests to increase validation and debug efficiency
- Assisting Validation Engineers to build expertise in Test Automation using Python
- Coordinating with multiple stakeholders across organizations like Architects, HW and SW design teams and emulation teams to comprehend the requirements and validate and debug the test failures in automation environment
- Guiding & mentoring teams during critical debugs and road blocks during automation

Previous Experience

Intel Technology India Private Limited, Bengaluru, Karnataka **Nov'04 – Nov'15**
Growth Path:
Nov'04 – Nov'10: Senior Software Engineer
Nov'10 - Nov'15: Graphics Software Engineer (Automation Lead)

Role:

- Led design, debug and delivery of Intel Graphics Silicon Validation Tools; managed Scrum backlog and assisted development during roadblocks in the sprint cycle
- Designed test plan & strategy of software & firmware modules of Intel Manageability Engine & Quite System Technology
- Enabled toolset for new Intel chipset for emulation environments like QuickTurn and Zebu
- Supported tools across multiple chipset and scale-up the tool set for future products; enhanced during silicon power-on and board bring-up
- Drove automation & execution to validate the Intel Display Drivers and Intel Display Subsystem

Samsung Electronics Company Limited, India Software Operations, Bengaluru, Karnataka **Jun'00 – Nov'04**
Growth Path:
2000 – 2002: Software Engineer
2002 – 2004: Senior Software Engineer

Role:

- Administered complete software engineering activities including design and development of various applications
- Interfaced with the clients for requirement gathering, understanding features required and implementing same
- Developed various applications for configuration of Samsung Network Devices
- Designed & implemented Agent server and Central server modules managing gateway & user connections, managing multiple agent server connections respectively
- Worked on network management application for desktop PC's to manage devices over power line network

Personal Details

Date of Birth: **01-Sep-1977**
Address: 6, 1st Main, 1st Cross, Kathriguppe East, BSK 3rd Stage, 4th Phase, Bangalore - 560085,

*~Refer to the **Annexure** for **Projects Managed***

Annexure

Projects Managed

~ At Intel Technology India Pvt. Ltd.

Title: **PAVE - Platform Automation Validation Engine**
Role:

- Led project and end-to-end delivery operations for Graphics Drivers Test Automation & Customer Defects Analysis
- Debugged test failures on automation environment by coordinating with the validation engineers and offering them a solution by through root cause & driving improvements to the tool
- Followed-up for major & critical audit findings to ensure appropriate actions and resolved the issues identified
- Executed test plan & validation of new features in PAVE Automation subsystem and validation software tools stack used for pre & post silicon validation of Intel Graphics chipsets; includes validation tools for emulation
- Supported tests scripts across multiple chipset and scaled-up the tool set for future products
- Developed new features and drove methodologies for coverage and concurrency tool validation

Highlights:

- Leveraged skills in developing critical & important **Flash and Intel® AMT customization tools** for Intel chipsets

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Title: Intel Manageability Engine – Software Tools
Description: Worked on software tools to configure Intel Active Management Technology
Role:

- Designed & developed test plan, test suite and test cases for critical and important Flash and AMT customization tools for Intel chipsets required for enabling OEMs
  - Flash image and programming tools are critical tools required for configuration of chipset firmware
- Interfaced India tools team with program management and reported updates.
  - Flash and AMT tools helped OEMs to make use of any ME/AMT features and capabilities of Intel chipsets and assisted in configuration and validation of Intel AMT
- Coordinated with various stakeholders of program like 8*Customer enabling team, debug team, program management and validation team during development, validation and support phase

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Title: Intel® Quite System Technology (IQST) Software Validation Plan and Test Development for Inte Chipsets
Description: This is module in Intel Manageability Engine (ME) subsystem in Intel® chipset
Role:

- Formulated detailed plan for validation of software and firmware modules of IQST technology including:
  - Owned validation plans and test development for a firmware and software library of Intel's Quite System Technology
  - Prepared detailed plan for validation of firmware and software library subsystem; developed test applications and test cases for various modules
  - Coordinated with product architects and developers to understand the implementation details and acquired validation plan and test reviewed by them
  - Designed test suit for validation of various firmware drivers
- Supported team members in ramping up on subsystem's architecture and test suite

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~ At Samsung Electronics, India Software Operations (SISO)

Title: **SAViNA**
Description: The project aimed at streaming media files present in personal computer on to television The

architecture consisted of two services, AV Node and AV Media server. AV Node is a Linux based set-up box capable of playing media resources located in a centralized location (AV Media Server) or from local storage device. A windows based PC will act as AV Media Server, contains media resources, which will be shared among all AV Nodes in the network. On user demand these media resources will be streamed to the AV Node over a wired or wireless LAN

Role:

- Worked on home networking domain and headed design and development of Media Server Applications with 4 member team; engaged in SEI-CMM level 5 assessments as Functional Area Representative (FAR) member

Title:	**PNMS**
Description:	PNMS is a network management application to manage the Samsung home appliances from remote place. This application also manages the gateway and user connectivity.
Title:	**PLC Network Designer**
Description:	It was a network management application for desktop PC's to manage devices over power line network. It was used by field engineers to do the configuration of power line network