

(9)

- thread priorities:

typedef enum osPriority {

from <cmsis_os.h>

osPriority Idle = -3,

osPriority Low = -2,

osPriority BelowNormal = -1,

osPriority Normal = 0,

osPriority AboveNormal = +1,

osPriority High = +2,

osPriority Realtime = +3,

} osPriority;

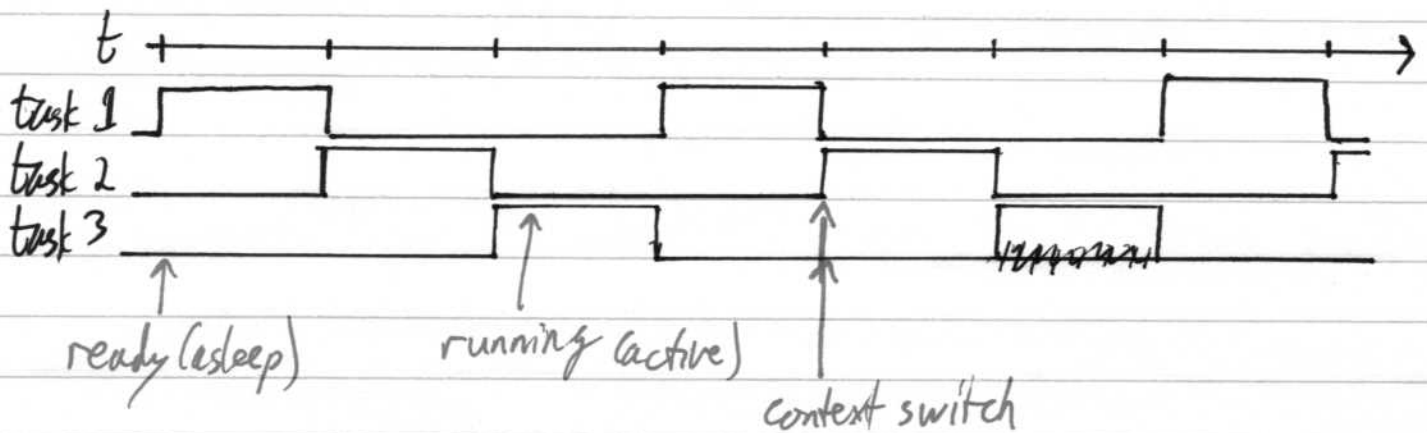
- the scheduler runs the threads that are ready and have highest priority
- when neither t1, t2, nor t3 are ready, the scheduler runs the os_idle_demon(): (osPriority Idle)

for (;;) ; ← os_idle_demon() body

3) Concurrent Execution

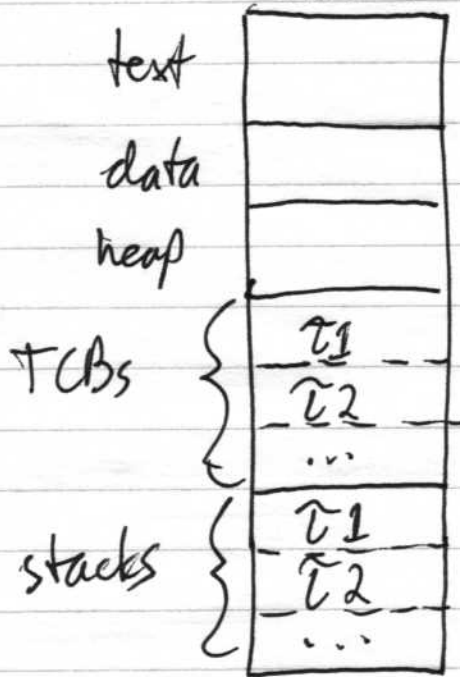
- task executions are interleaved on one (or more) processors with indeterminate ordering

e.g. 1 processor, 3 tasks of equal priority, round-robin scheduling



- a real-time application consists of a set of concurrent tasks (e.g. threads in CMSIS-RTOS)
 - all tasks share the text and data sections and heap
 - each task has its own stack and Task Control Block (TCB)

RTOS Memory Map



- the OS uses the TCB to manage a task

- TCB contents:

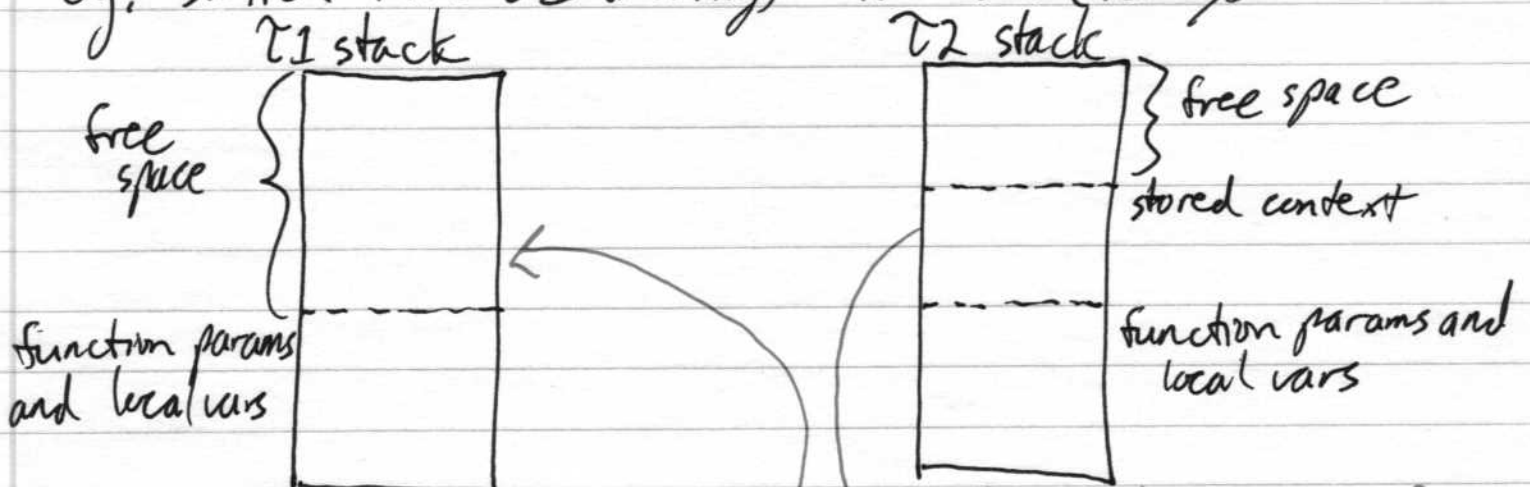
- task ID
- stack pointer
- state = { Ready, Running, Waiting, Inactive }
- priority
- pointers to wait lists (linked lists)
- delay time
- event flags
- ..

3.1) Context Switch

- to switch between running tasks the OS must:

- (1) store the execution context of the running task
- (2) restore the execution context of the ready task

e.g. switch from T1 (running) to T2 (ready)



- push general-purpose regs, PC, status reg
- record stack pointer in T1's TCB

- update stack pointer from T2's TCB
- pop into gen.-purpose regs, PC, status reg.

4) Scheduling

[7.3]

4.1) Task States

