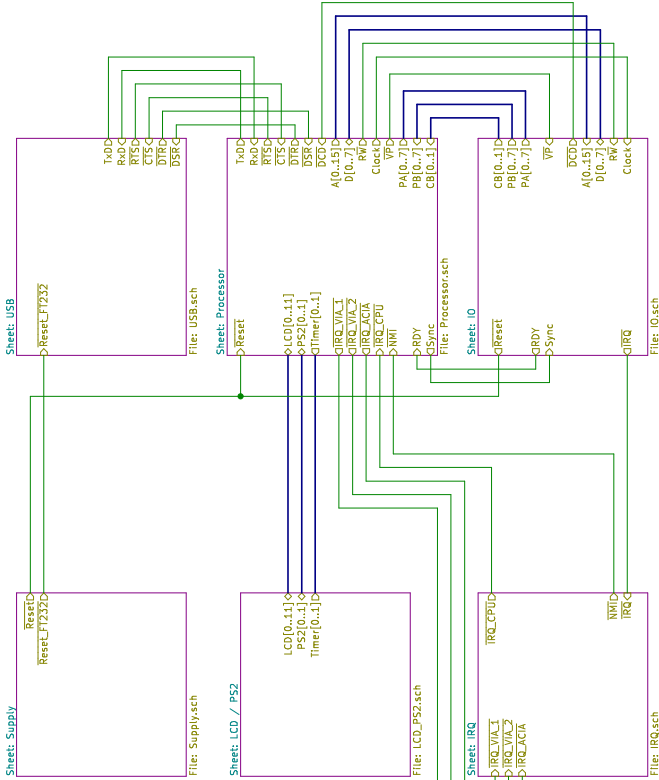


# Memory map

Address ranges:	Size:	Description:
\$0000 – \$FFFF	64K	Available memory
\$0000 – \$7FFF	32K	SRAM
\$8000 – \$83FF	1K	VIA 1
\$8400 – \$87FF	1K	VIA 2
\$8800 – \$8BFF	1K	ACIA
\$A000 – \$FFFF	24K	Program memory



System architecture

Author: Daniel Kampert  
<https://www.kampis-elektroacte.de>

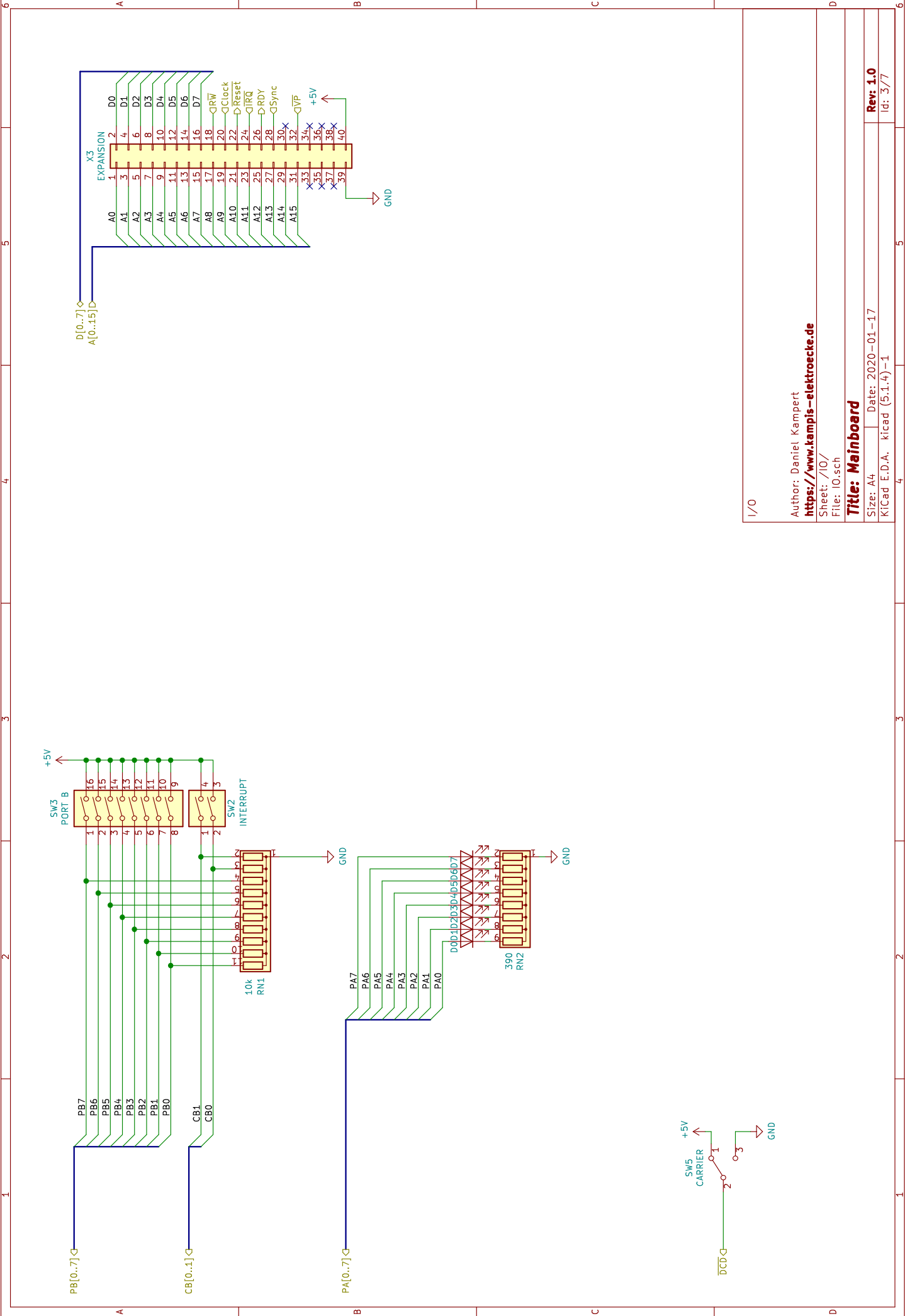
Sheet: /  
File: Mainboard.sch

**Title: Mainboard**

Size: A3 | Date: 2020-01-17  
Kicad E.D.A. | kicad (5.1.4)-1

Rev. 1.0  
Id: 1/7





I/O

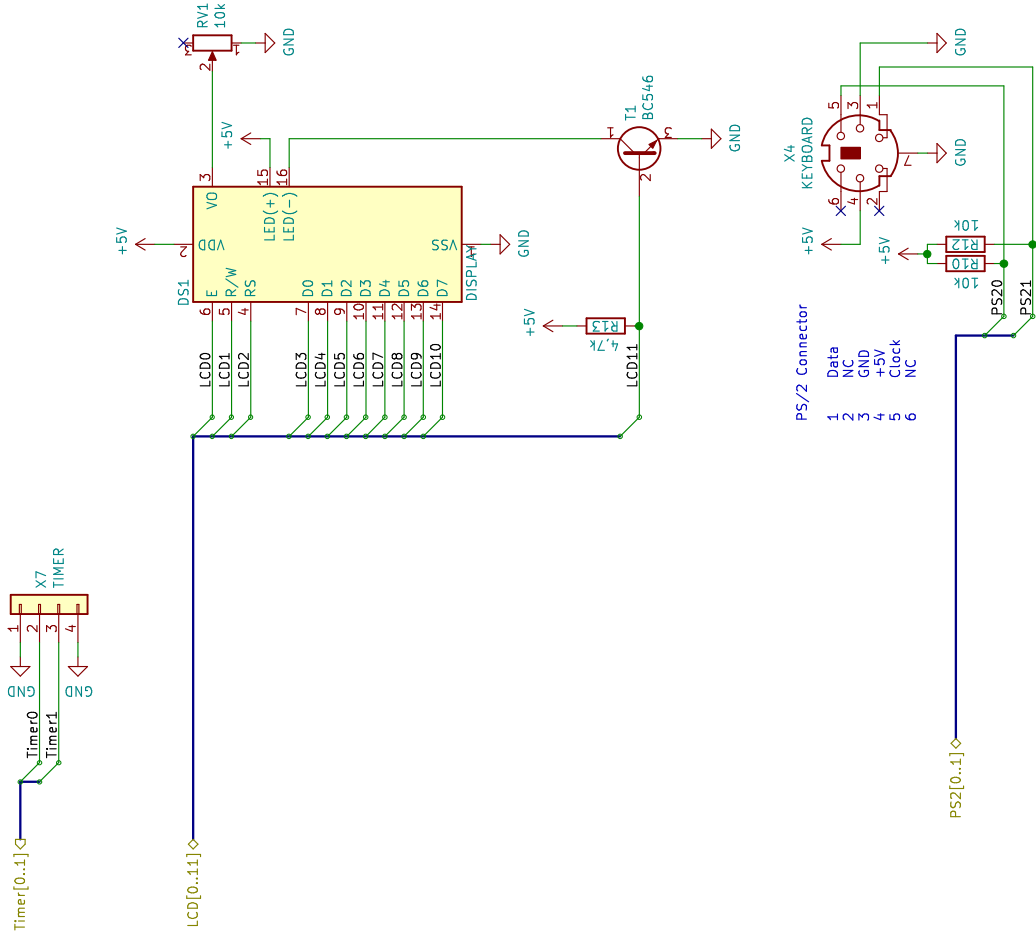
Author: Daniel Kampert  
<https://www.kampis-elektroecke.de>

Sheet: /I/O/  
File: IO.sch

**Title: Mainboard**

Size: A4 | Date: 2020-01-17  
KiCad E.D.A. kicad (5.1.4) - 1

Rev: 1.0  
Id: 3/7



LCD, P2/2 & Timer

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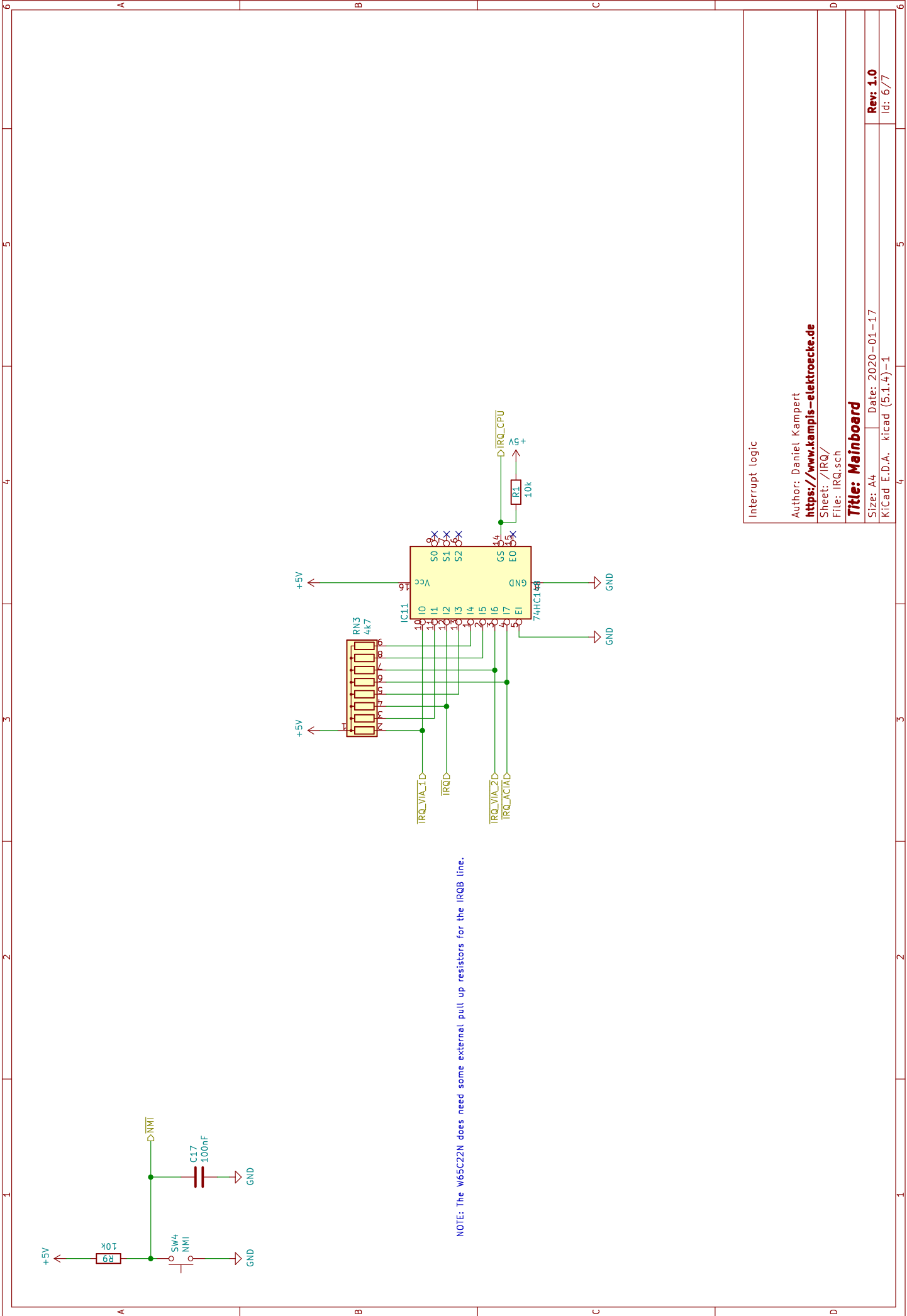
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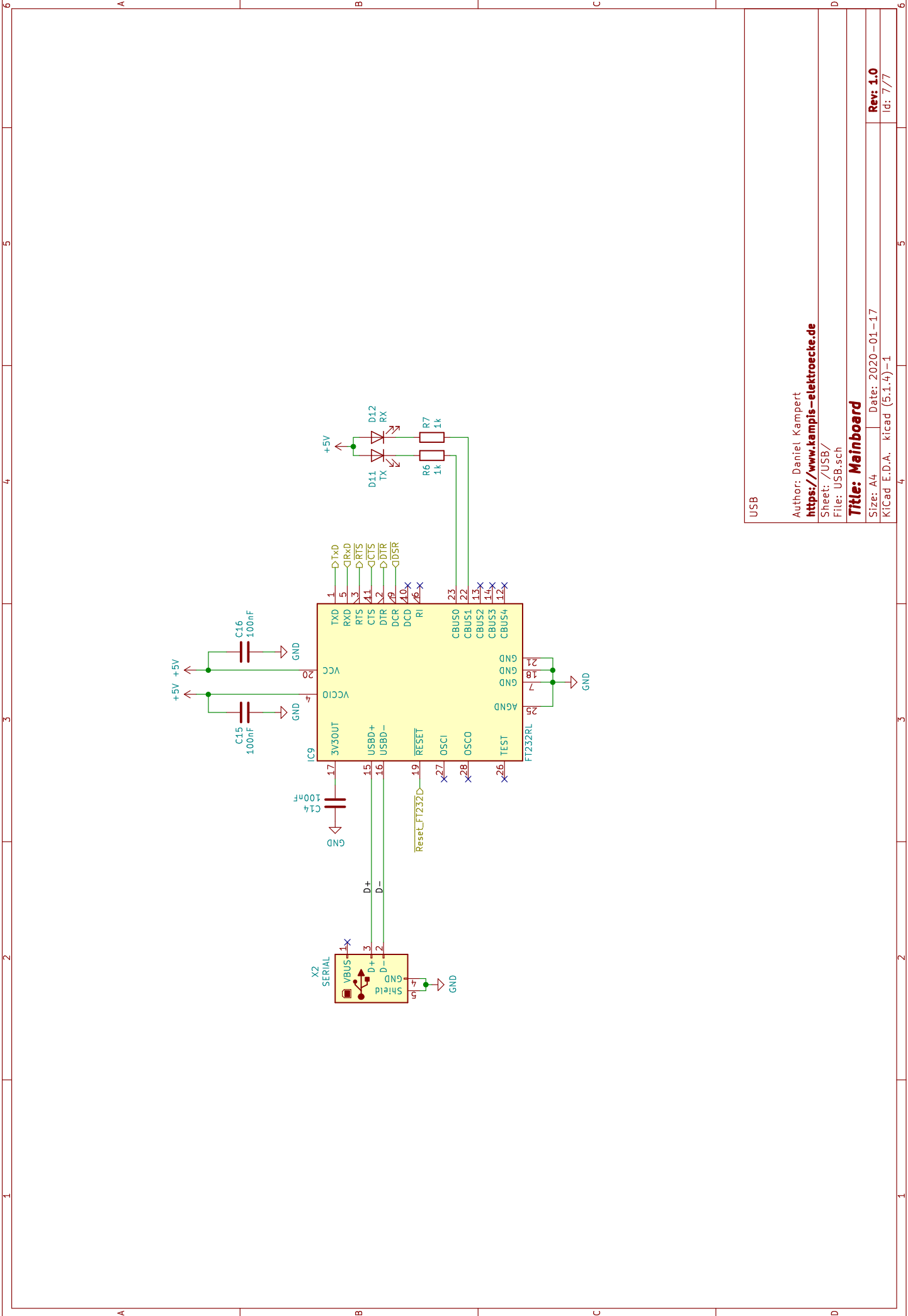
**Title: Mainboard**

Size: A4 | Date: 2020-01-17  
 KiCad E.D.A. kicad (5.1.4) - 1

**Rev: 1.0**  
 Id: 4/7







USB

Author: Daniel Kampert  
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Sheet: /USB/  
File: USB.sch

**Title: Mainboard**

Size: A4 | Date: 2020-01-17  
KiCad E.D.A. kicad (5.1.4) - 1

**Rev: 1.0**  
Id: 7/77