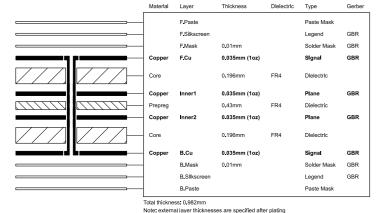
# **Heartrate-DevKit-HW Fabrication Document**

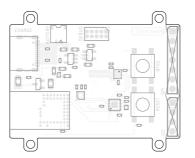
#### **Layer Stack Legend**



### **Impedance Table**

Transmission Line | Impedance [ohms] | Tolerance [%] | Layer | Trace Width [mm] | Gap [mm] | Ref. Layers

## **Top Fabrication (Scale 1:1)**



FABRICATION NOTES (UNLESS OTHERWISE SPECIFIED)

- 1) FABRICATE PER IPC-6012A CLASS 2.
- OUTLINE DEFINED IN SEPARATE GERBER FILE WITH "Edge\_Cuts.GBR" SUFFIX.

DIMENSIONS OF CIRCUMSIZED RECTANGLE SHOWN ON THIS DRAWING FOR REFERENCE ONLY.

3) SEE SEPARATE DRILL FILES WITH ".DRL" SUFFIX FOR HOLE LOCATIONS.

SELECTED HOLE LOCATIONS SHOWN ON THIS DRAWING FOR REFERENCE ONLY.

- 4) SURFACE FINISH: HAL SNPB
- 5) SOLDERMASK ON BOTH SIDES OF THE BOARD SHALL BE LPI, COLOR GREEN.
- SILK SCREEN LEGEND TO BE APPLIED PER LAYER STACKUP USING BLACK NON-CONDUCTIVE EPOXY INK.
- 7) ALL VIAS ARE TENTED ON BOTH SIDES UNLESS SOLDERMASK OPENED IN GERBER.
- 8) VENDOR SHOULD FOLLOW ROHS COMPLIANT PROCESS AND Pb FREE FOR MANUFACTURING
- 9) PCB MATERIAL REQUIREMENTS:

- FLAMMABILITY RATING MUST MEET OR EXCEED UL94V-0 REQUIREMENTS.
   Tg 150 C OR EQUIVALENT.
   EQUIVALENT MATERIAL SHALL BE ROHS COMPLIANT, HALOGEN FREE AND APPROVED BY ZSWATCH.

#### 10) DESIGN GEOMETRY MINIMUM FEATURE SIZES:

BOARD SIZE
BOARD THICKNESS
TRACE WIDTH
TRACE TO TRACE
MIN. HOLE (PTH)
MIN. HOLE (IPTH)
ANNULAR RING
COPPER TO HOLE
COPPER TO EDGE
HOLE TO HOLE 0.982 mm 0.150 mm 0.150 mm 0.150 mm 0.650 mm 0.100 mm 0.100 mm 0.100 mm

Variant: Git Hash: Company: ZSWatch ZSWatch CHECKED Board Name: Heartrate-DevKit-HW ZSWatch Heartrate-DevKit Sheet Title: File Name: Designer: Date: Revision: ZSWatch-Heartrate-DevKit.kicad\_pcb Top Fabrication (Scale 1:1) 2025-07-13 Daniel Kampert + (Unreleased) Sheet Path: Size: **A3** of 10

