Column1	Column2	Column3	Column4	Column5	Column6	Column7	Column8	Column9	Column10	Column11
	Sn	Student name	Father name	dob	Gender	Course	Model	Email id	adress	

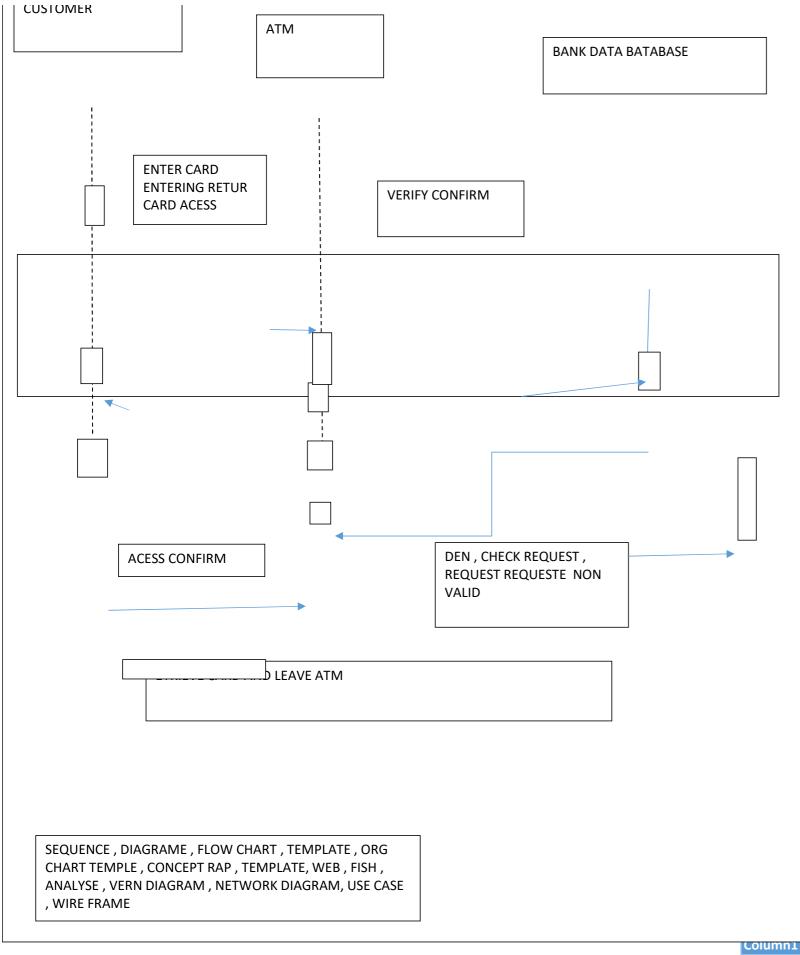
**Macro1 Macro Column2 Column3 Column4 Column5 Column6 Column7 Column8 Column9 Column10 Column11 Column12 Column13 Column14 Column15

**Code resetb and initialize form with default form . Sub reset_form () Dim I row as long With .txt student name.text=" " Txt student name backcolor +vb white Txt father name itext ="" Txt father name back color + vb white Txt father name itext ="" Txt father name back color + vb white Txt father name itext ="" Txt father name itext = "" Txt father

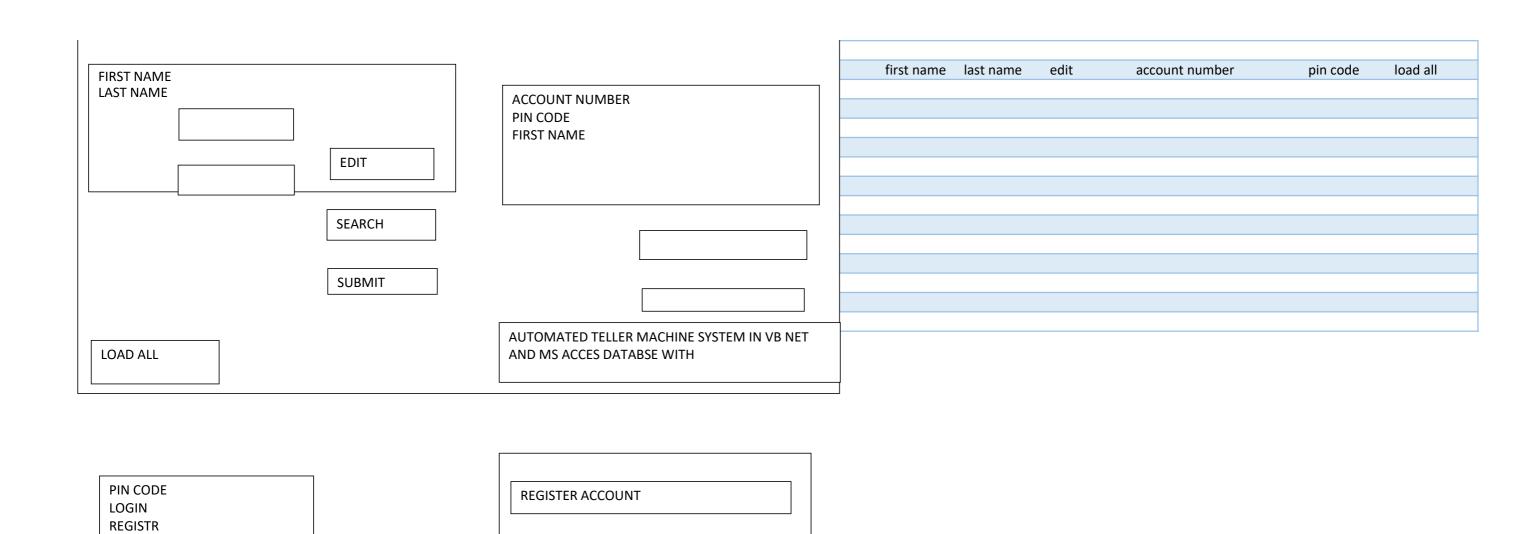
ActiveWindow.ScrollRow = 30
ActiveWindow.ScrollRow = 29
ActiveWindow.ScrollRow = 28
ActiveWindow.ScrollRow = 27
ActiveWindow.ScrollRow = 26
Range("O32").Select
ActiveWindow.ScrollColumn = 1
Range("B38").Select
ActiveCell.FormulaR1C1 = "7"
Range("C38").Select
ActiveCell.FormulaR1C1 = "8"
Range("D38").Select
ActiveCell.FormulaR1C1 = "9"
Range("E38").Select
ActiveWorkbook.Save
End Sub
Sub Macro2()
'Macro2 Macro
'text field buttons for the number operator button for the result off, clear, back space option explicit public class form 1 dim operand 1 as double dim operad 2 as double dim {operator} as string dim has decimal boolean
Application.Run _
"'Copy of PROJECT DRAWING WORKSHET TSHINGOMBE DESIGN ANALYSE ENGIN Book12.xlsx'!Macro2"
End Sub
Sub Macro3()
' Macro3 Macro
"500000fff03ff000018000a0410000d00095000001" string .cmd=""; cmd=cmd+"5000";//sub head (not) cmd=cmd+"00"//network number cmd+cmd+"ff";//plc number cmd+""03ff";//demand object module i/o.number cmd

End Sub

Input	output	Column1	Column2	Column3	Column4	Column5	Column6	Column7	Column8	Column9	Column10	Column11
X1		X2	Х3	X4	X5	X6	X7	S1	S2	S0	Reg select	Hardware virtual
	C	0	0	0	0	0	0	0	0	0	0	0



Column1 Column2 Column3 Column4 Column5 Column6 Column7 Column8



DEPOSIT

TRANSFER

LOGIN

WITH DRAWIN

CLOSE

Column1	Column2	Column3	Column4	Column5	Column6	Column7	Column8	Column9	Column10	Column11	Column12	Column13	Column14	
input	KX										output			
X1	X2	Х3	Х3	X4	X5	X6	X7	X8	S1	S2	S3	S4	S5	
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	1	1
	0	0	0	0	0	0	0	0	0	0	0	1	1	1
	0	0	0	0	0	0	0	0	0	0	1	1	1	1
	0	0	0	0	0	0	0	0	0	1	1	1	1	1

0	0	0	0	0	0	0	0	1	1	1	1	1	1
0	0	0	0	0	0	0	1	1	1	1	1	1	1
0	0	0	0	0	0	1	1	1	1	1	1	1	1
0	0	0	0	0	1	1	1	1	1	1	1	1	1
0	0	0	0	1	1	1	1	1	1	1	1	1	1
0	0		1	1	1	1	1	1	1	1	1	1	1
0	0	1	1	1	1	1	1	1	1	1	1	1	
0	1	1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1

BASIC SOFTWARE ENGINEERING CONCEPT SOLVE ELECTRICAL AND ELECTRONICS ENGINEERING PROBLEM .. TESTING DOCUMENTING I/O PROGRAMME BASIC STRUCTURE DESIGN

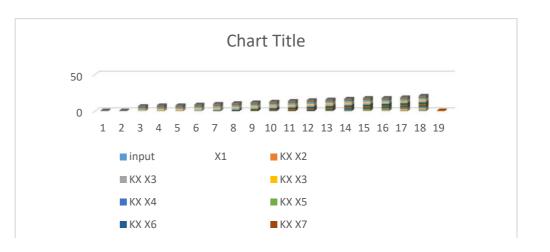
Column1	Column2 Column3 Column4 Column5 Column6 Column7 Column8 Column9 Column10 Column11									
	Int sensor = Ao									
	Red adc pin 0, bo int led bank =7									
	If bo<50 then level 1 void set up () {									
	If b0<100then level 2 pin mode (led bank ,output)									
	If bo 150 the level 3									
	et mark=0 void loop (){									
	t space =10 int reading +analogie read (sensor)									
	Go pulser int value = map (reding ,01023,0,255									
	Level1:									
	Lel mark =10 delay (10)									
	Let spacde =0 digital write (led bank,low)ay (3)									
	Go pulser ,,									
	-level 2: if value >49 et value <100){									
	Let space =4 delay (6)									
	Got pulser digital writer (led bank low , delay									
	Level .									
	Let mark=3 if (value > 99 et value <150{									
	Let space =7 digital write (led bank , hgh									
	Pulser high 7, digital written led low, pause,, goto main, main write (led bank,low delay (10									
	Pause mark									

Column16	Column17	Column18	Column19	Column20	Column21	Column22	Column23	Column24
te . Txt dob t	ext =""""							

1 dim tmp nalue as double

d=cmd+"001c";length demand data cmd=cmd+cmd+000A";cpu in

Column15	Column16	Column17	Column18	Column19	Column20	Column21	Column22
S6	CPU	SELECT	POROCESS	REGISTER	ARITHM	LOGIC	
30	0	0	0	0	0	0	1
	0	0	0	0	0	0	1
	1	1	1	1	1	1	1
	1	1	1	1	1	1	1
	1	1	1	1	1	1	1
	1	1	1	1	1	1	1
	1	1	1	1	1	1	1
	1	1	1	1	1	1	1
	1	1	1	1	1	1	1



1	1	1	1	1	1	1	
1	1	1	1	1	1	1	
1	1	1	1	1	1	1	
1	1	1	1	1	1	1	
1	1	1	1	1	1	1	
1	1	1	1	1	1	1	
1	1	1	1	1	1	1	
	1	1	1	1	1	1	
1	1	1	1	1	1	1	
					1		



N USING FLOW CHART PROGRAMME DESIGN LANGUAGE, WRITE TEST DOCUMENT LINEAR PROGRAM USING LANGUAGE, HEXADECIMAL, SEQUENCE, table logic, convenrsion, logic gate, and or in sum, sequence logic, bloc diagram, ansyc

