Column1	Column2	Column3	Column4	Column5	Column6	Column7	Column8	Column9	Column10	Column11	Column12	Column13	Column14	Column15	Column16	Column17
input	KX										output					
X1	X2	X3	X3	X4	X5	X6	X7	X8	S1	S2	S3	S4	S5	S6	CPU	SELECT
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1
0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1
0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1
0	0		1	1	1	1	1	1	1	1	1	1	1	1	1	1
0	0	1	1	1	1	1	1	1	1	1	1	1	1		1	1
0	1	1	1	1	1	1	1	1	1	1	1	1	1		1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

Column18	Column19	Column20	Column21	Column22
POROCESS REGISTER		ARITHM	LOGIC	
	0	0	0	1
	0	0	0	1
	1	1	1	1
	1	1	1	1
	1	1	1	1
	1	1	1	1
	1	1	1	1
	1	1	1	1
	1	1	1	1
	1	1	1	1
	1	1	1	1
	1	1	1	1
	1	1	1	1
	1	1	1	1
	1	1	1	1
	1	1	1	1
	1	1	1	1
	1	1	1	1
			1	