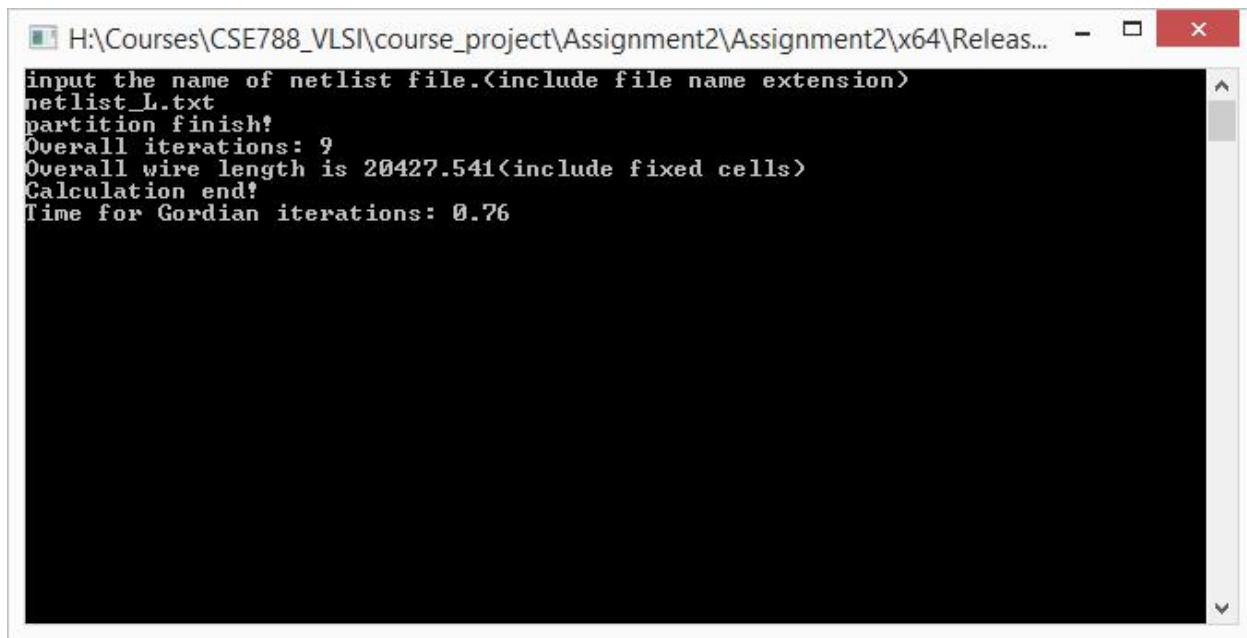


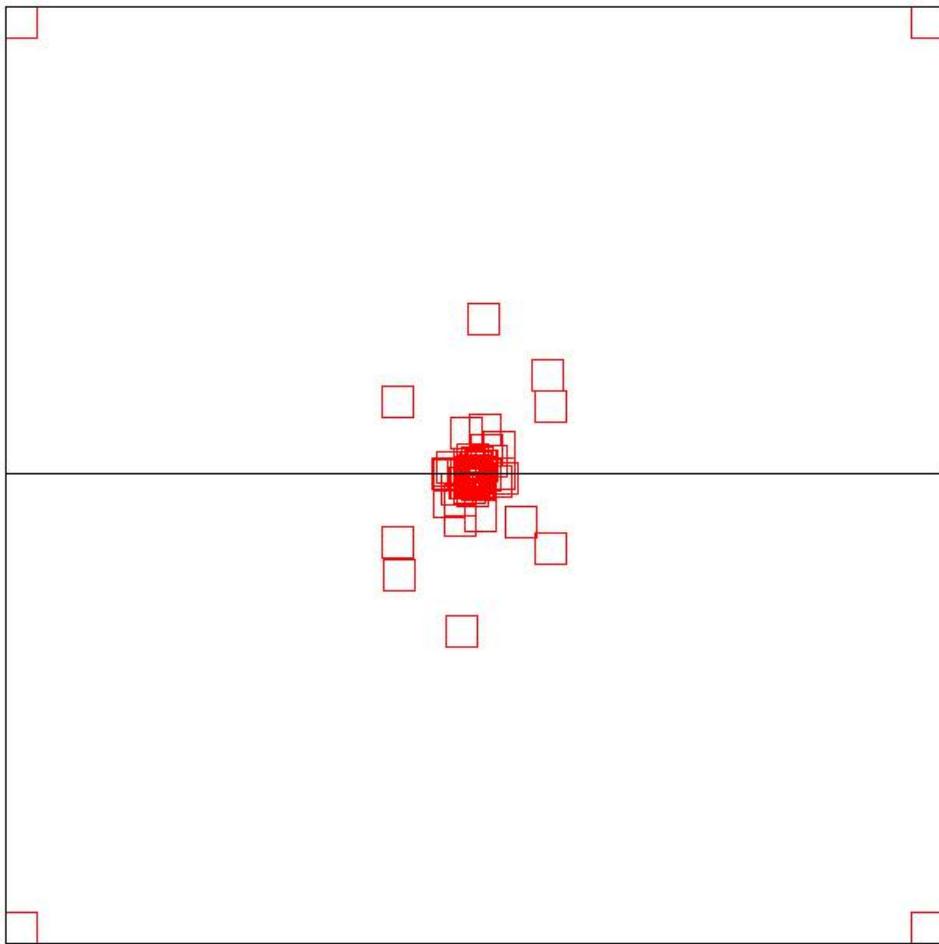
The strategy I used to do partition is trying to set the
(sum of cell size in region A)/region A = (sum of cell size in region B)/region B.

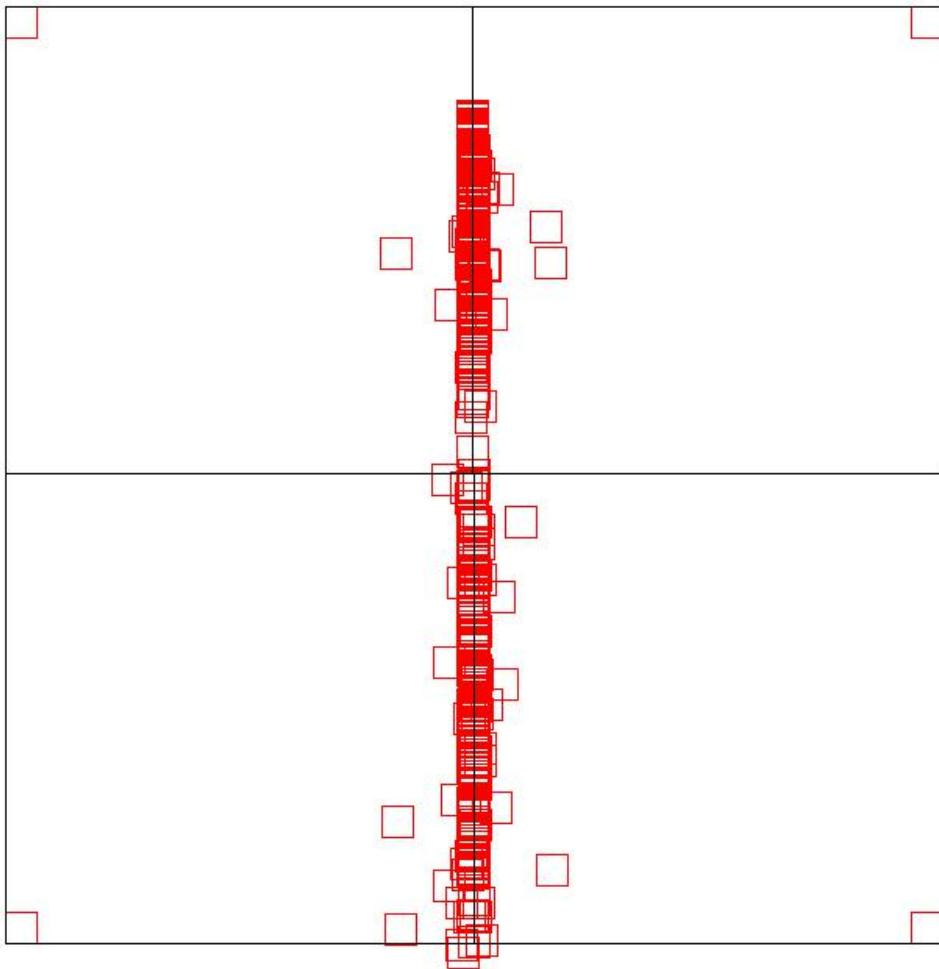


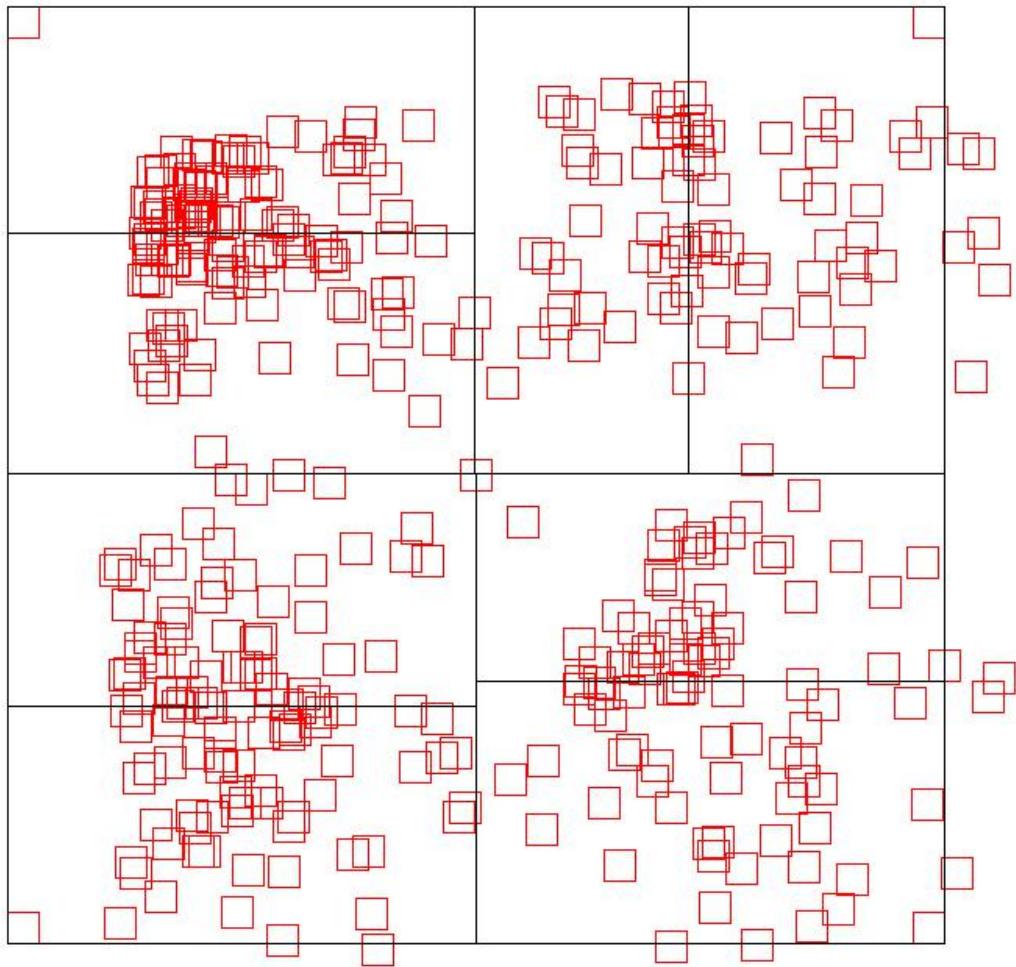
A screenshot of a Windows-style terminal window. The title bar reads "H:\Courses\CSE788_VLSI\course_project\Assignment2\Assignment2\x64\Releas...". The window contains the following text output:

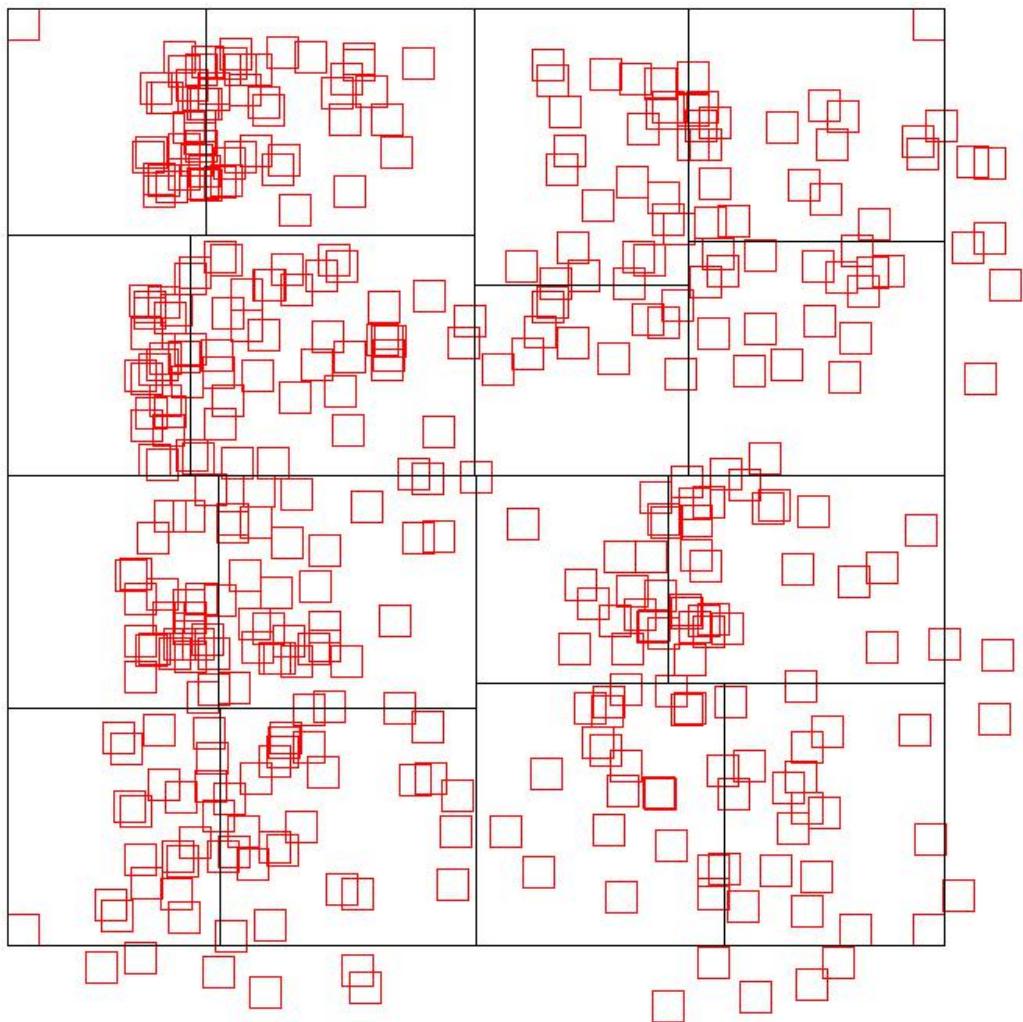
```
input the name of netlist file.<include file name extension>
netlist_L.txt
partition finish!
Overall iterations: 9
Overall wire length is 20427.541<include fixed cells>
Calculation end!
Time for Gordian iterations: 0.76
```

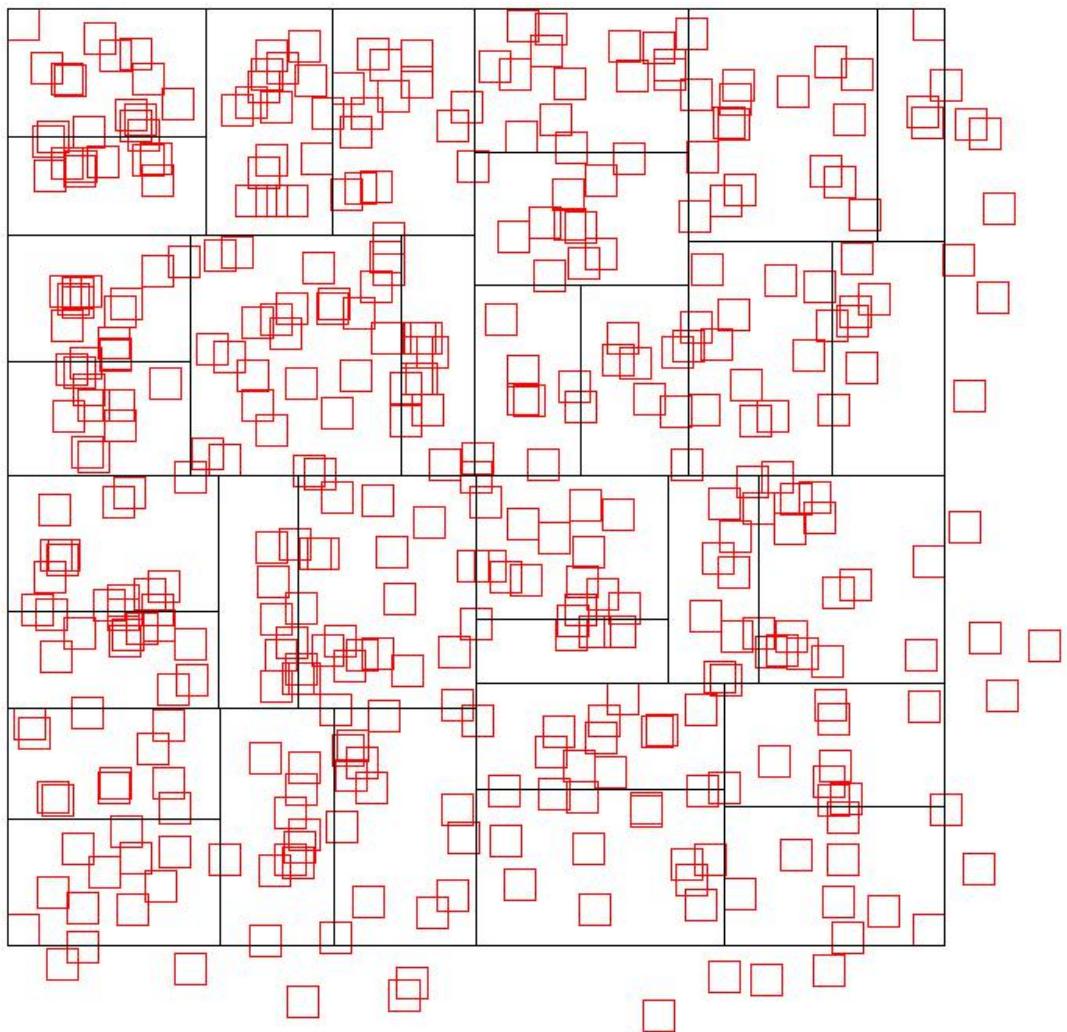
Result for netlist_L.txt

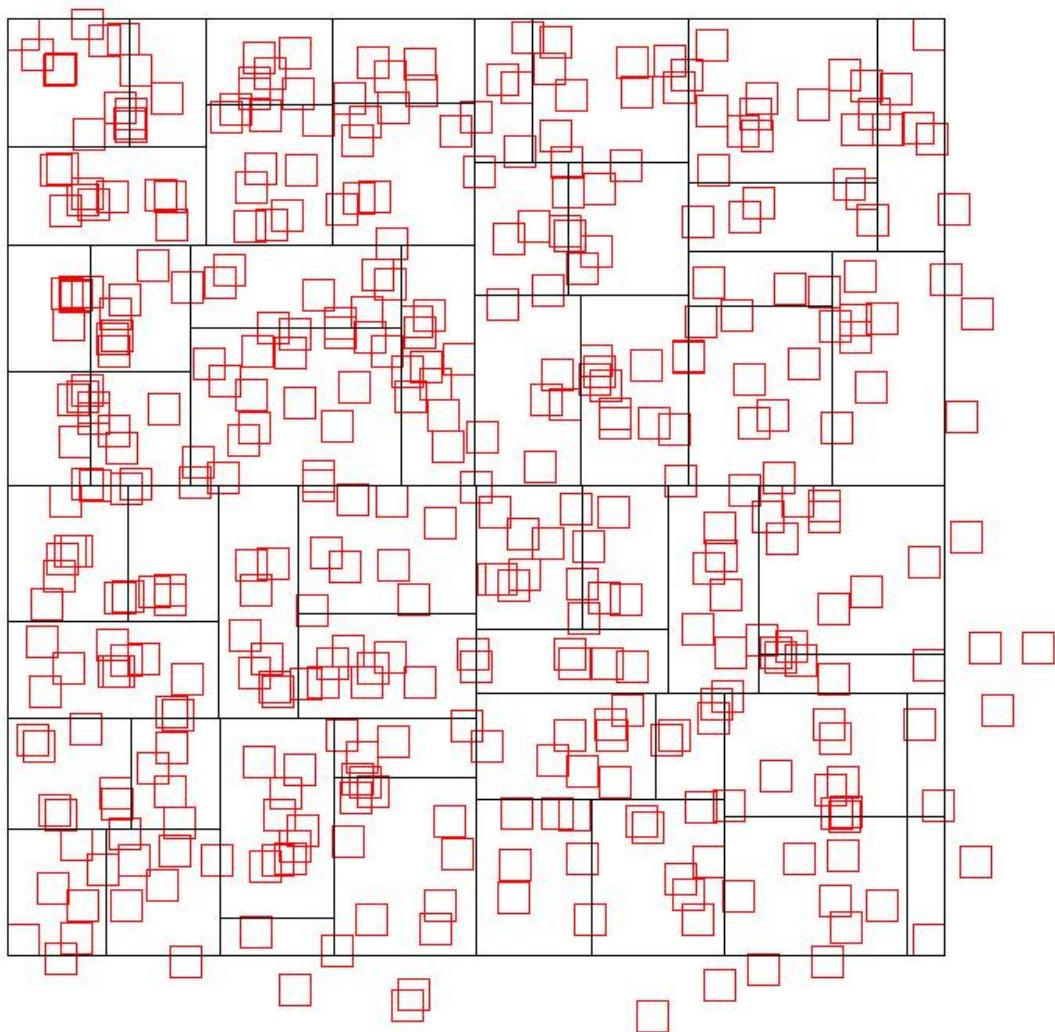


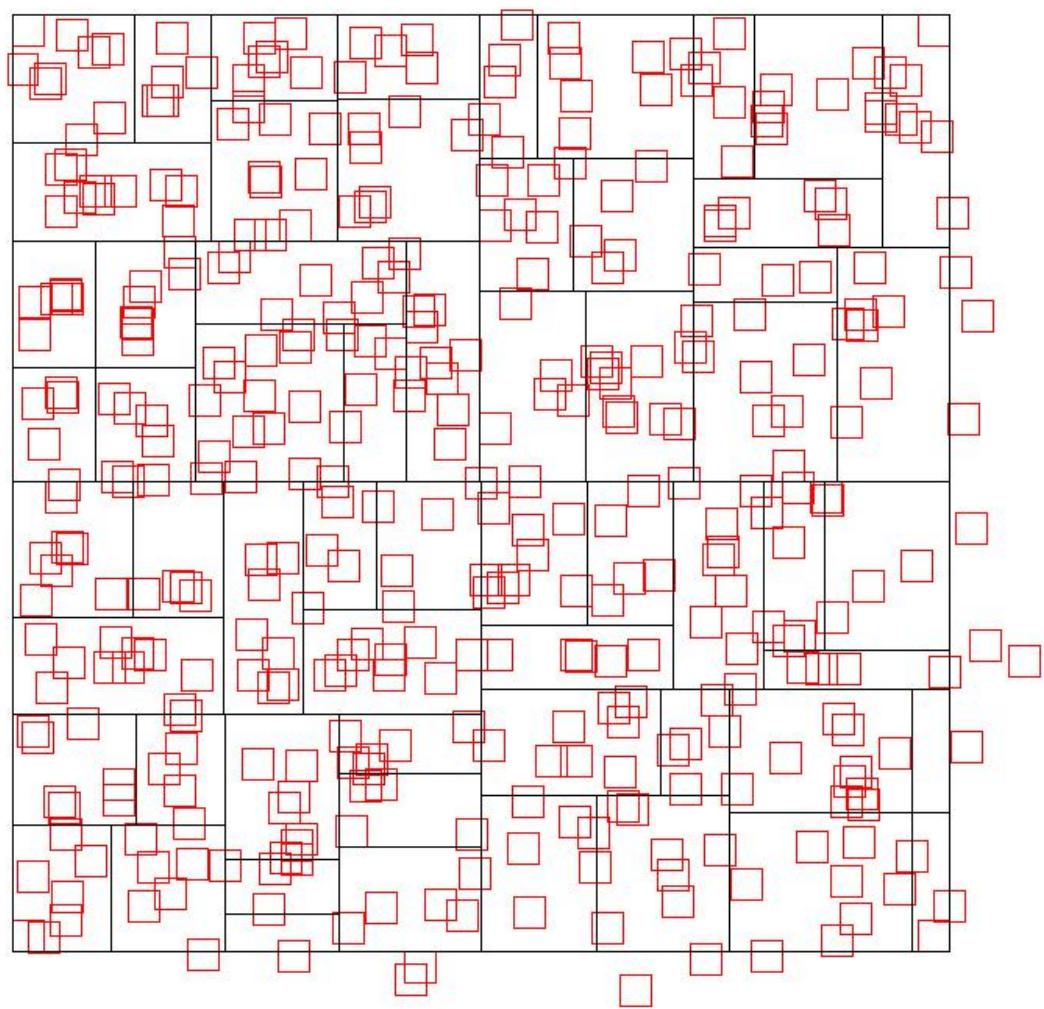


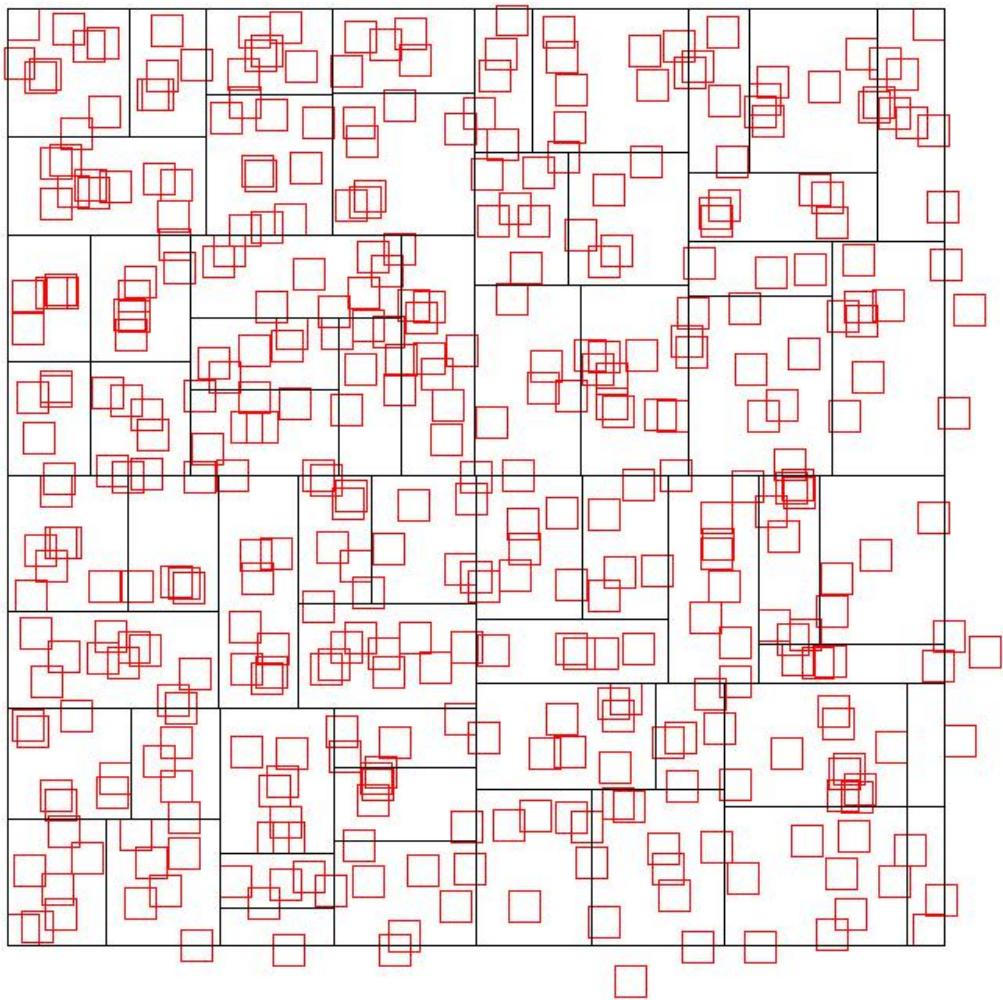


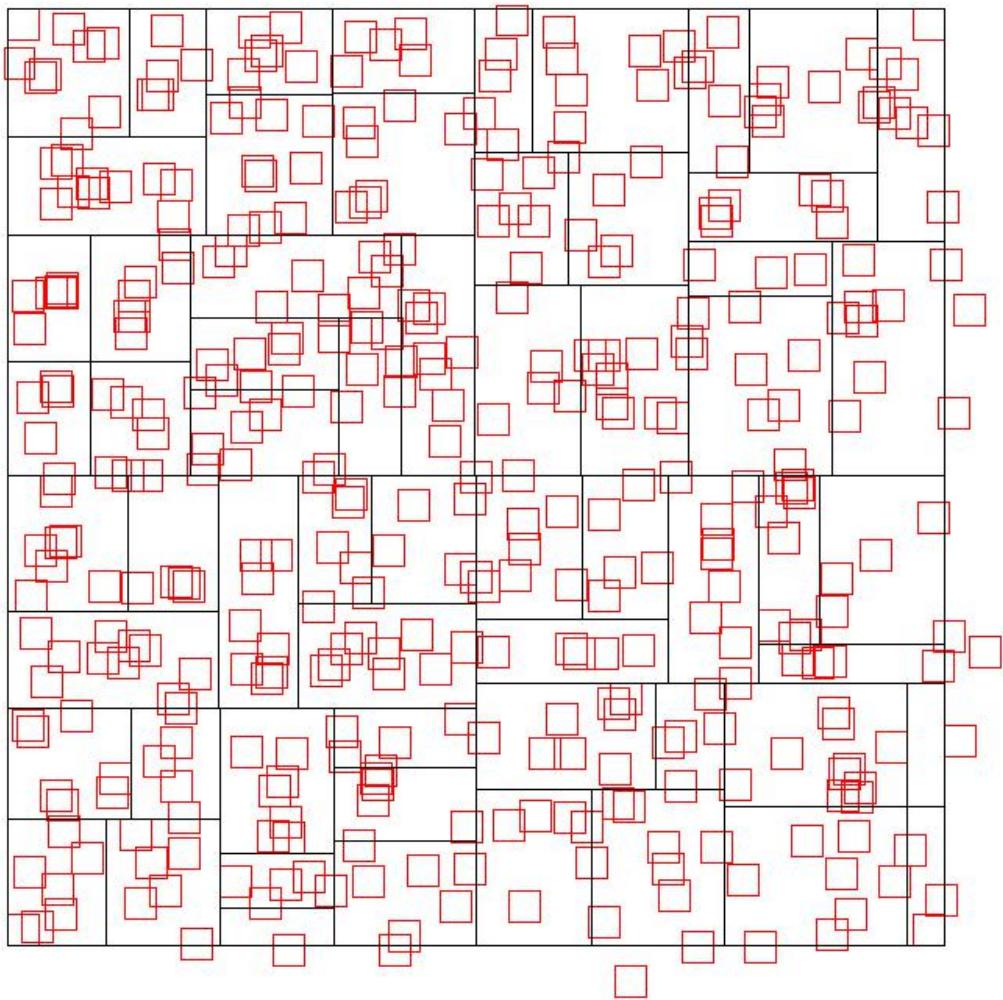


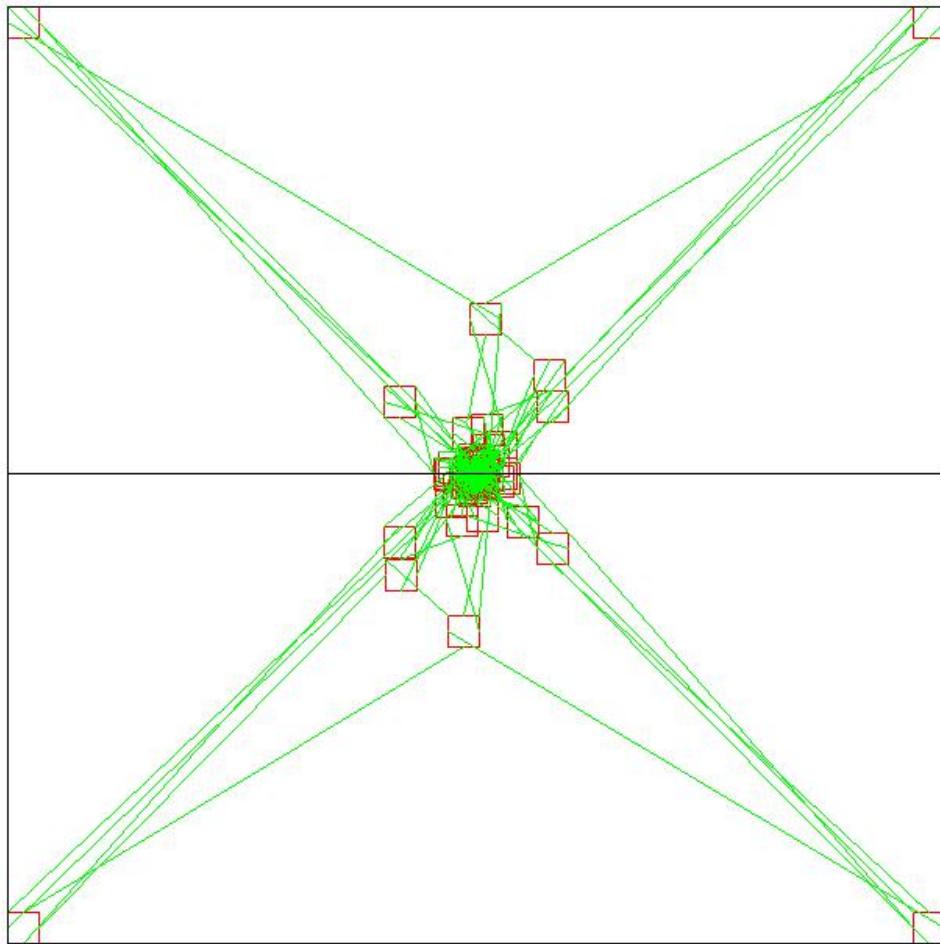


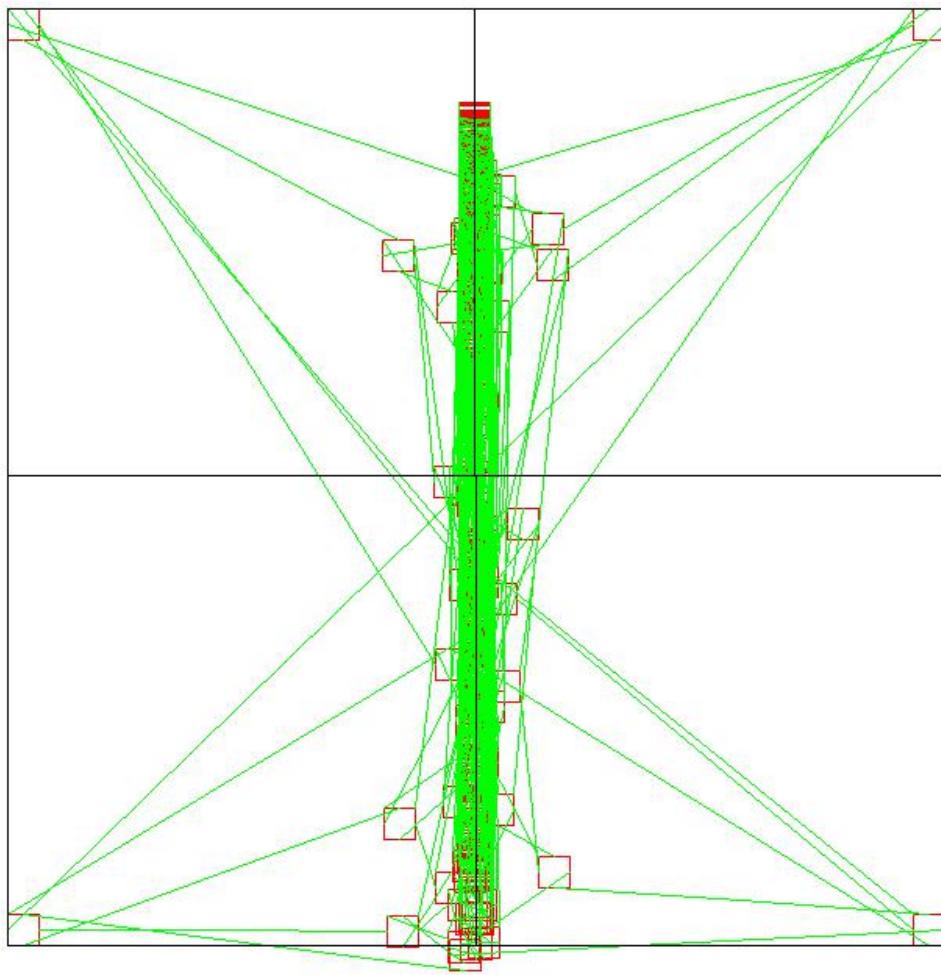


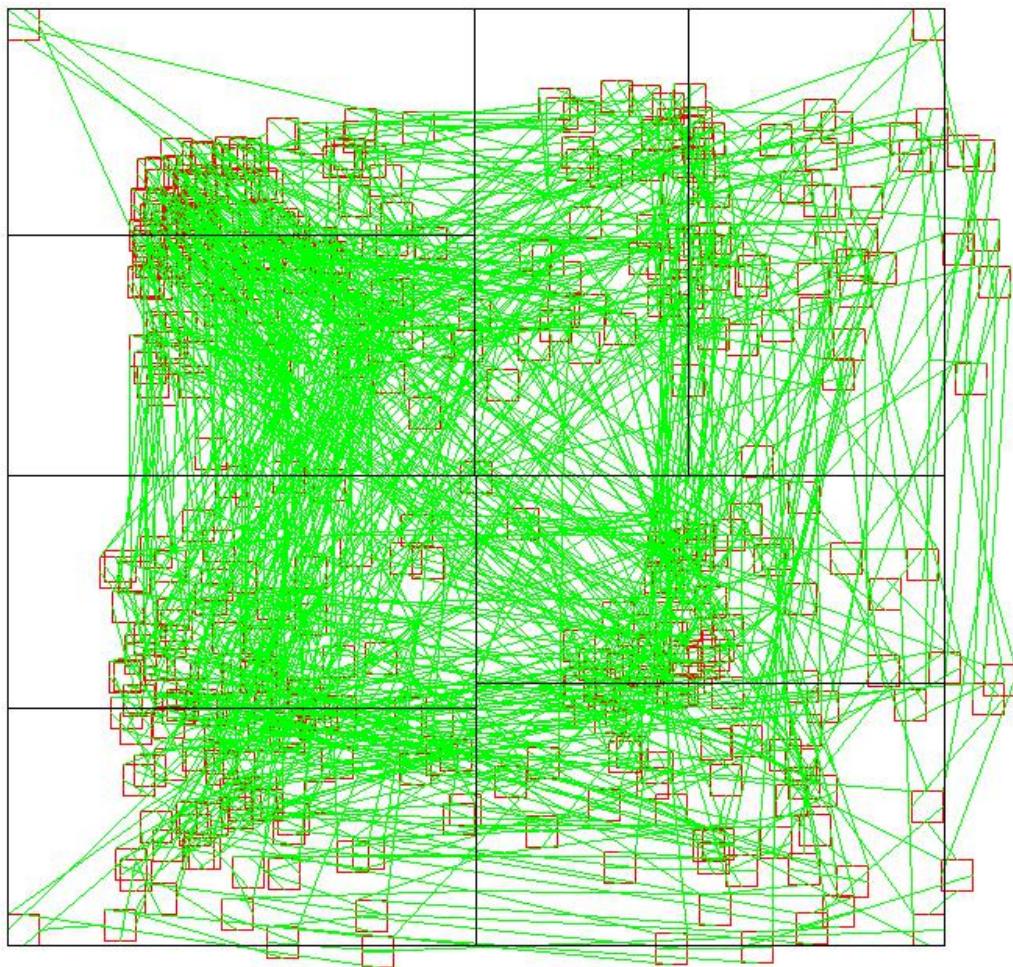


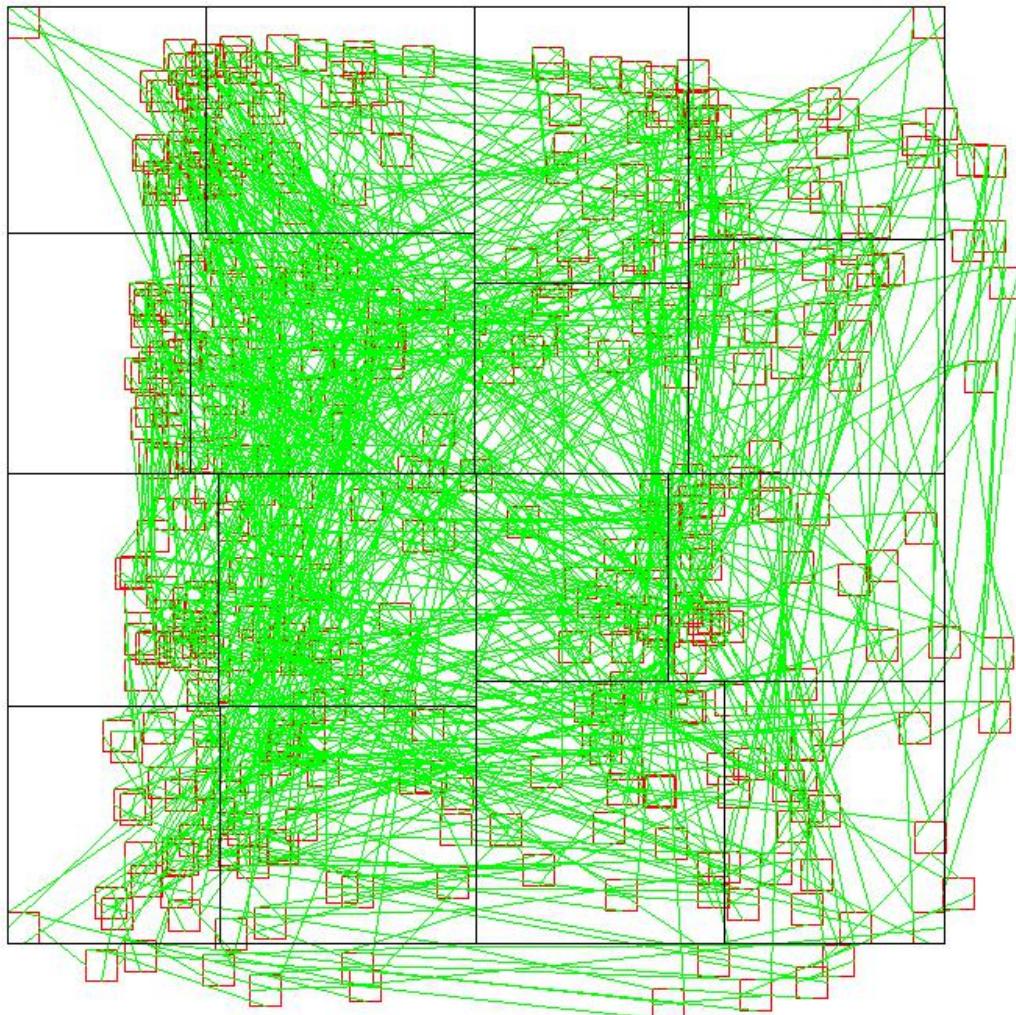


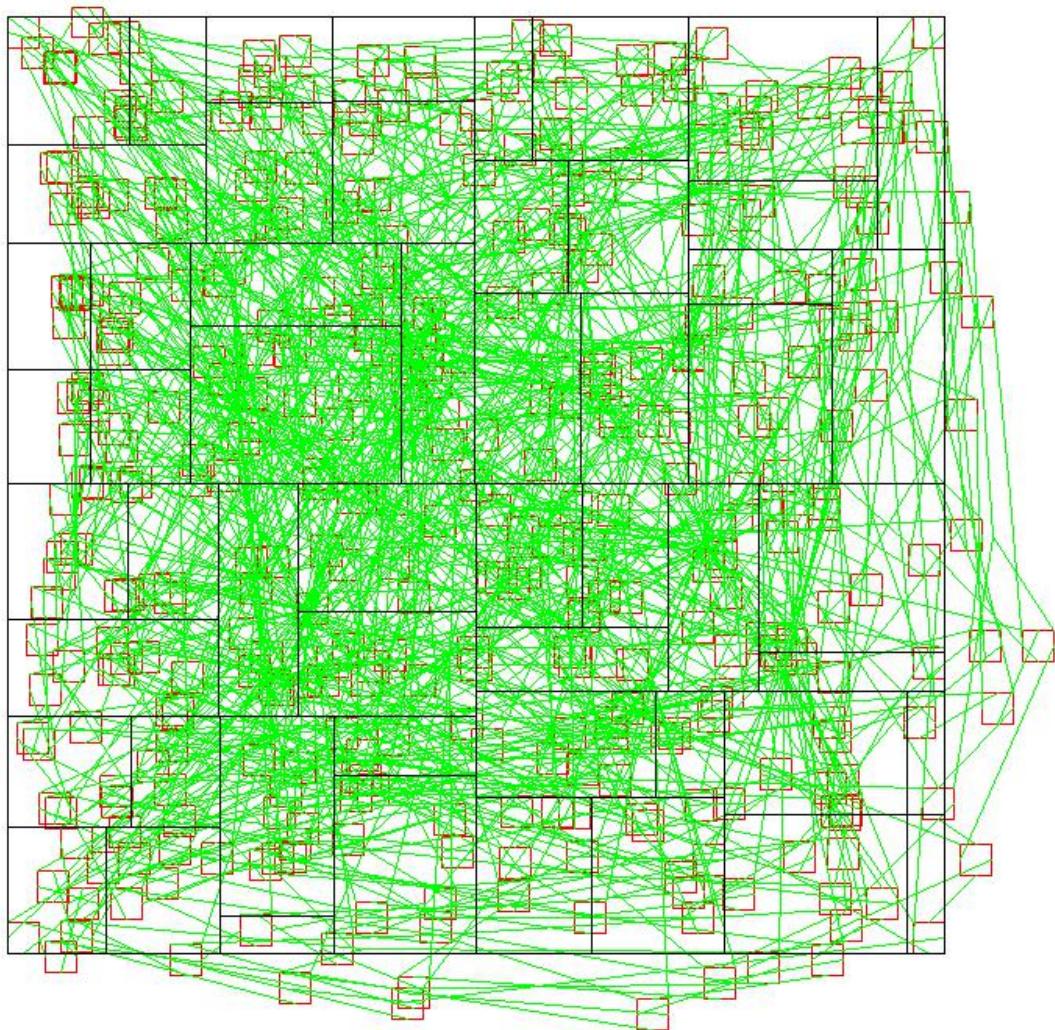


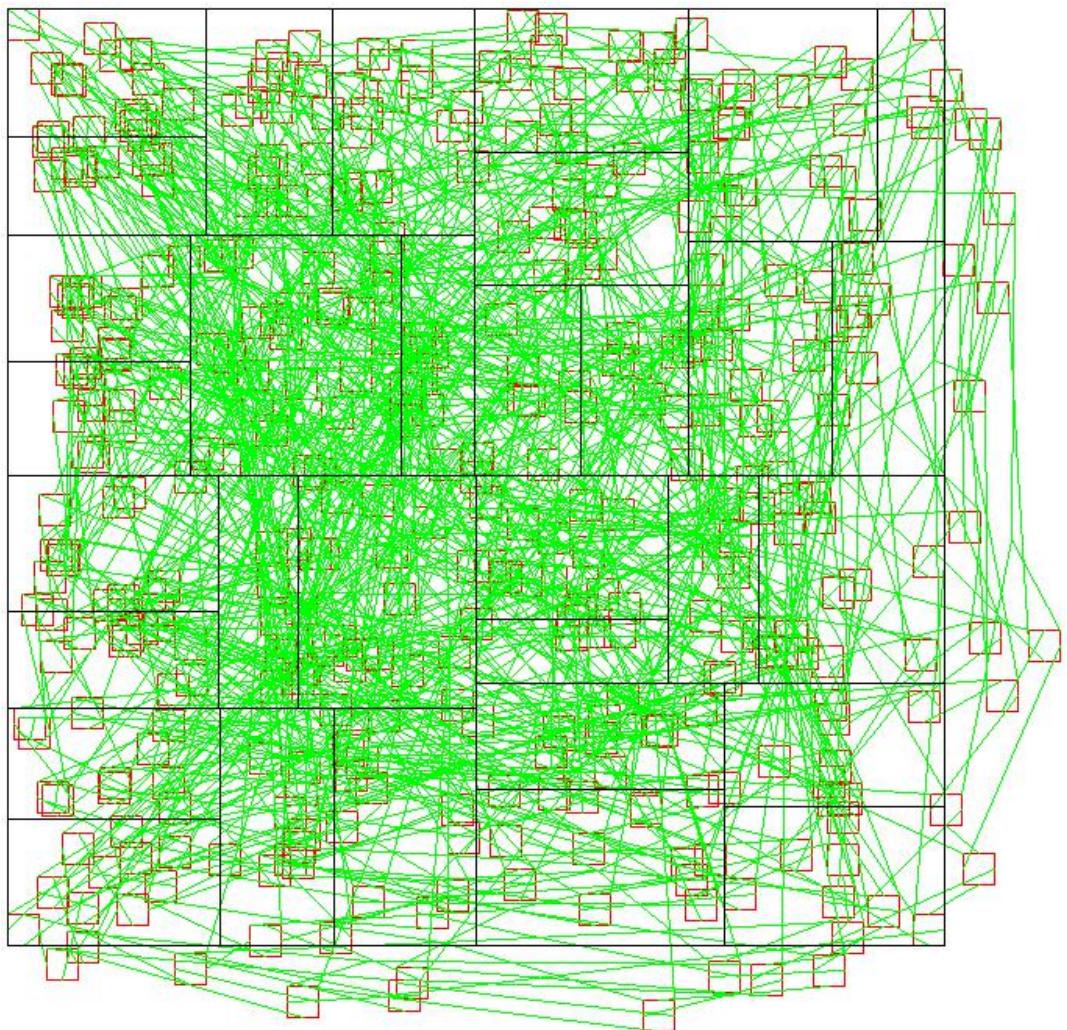


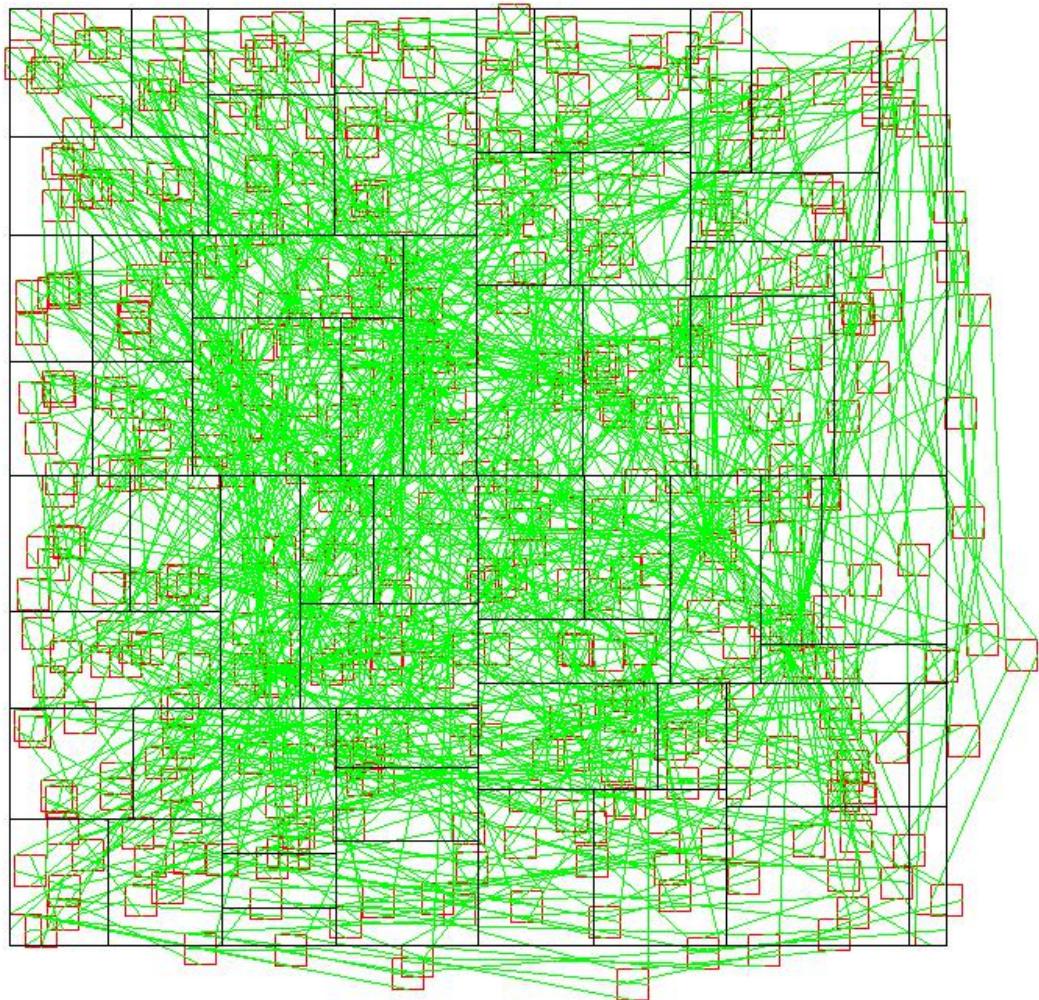


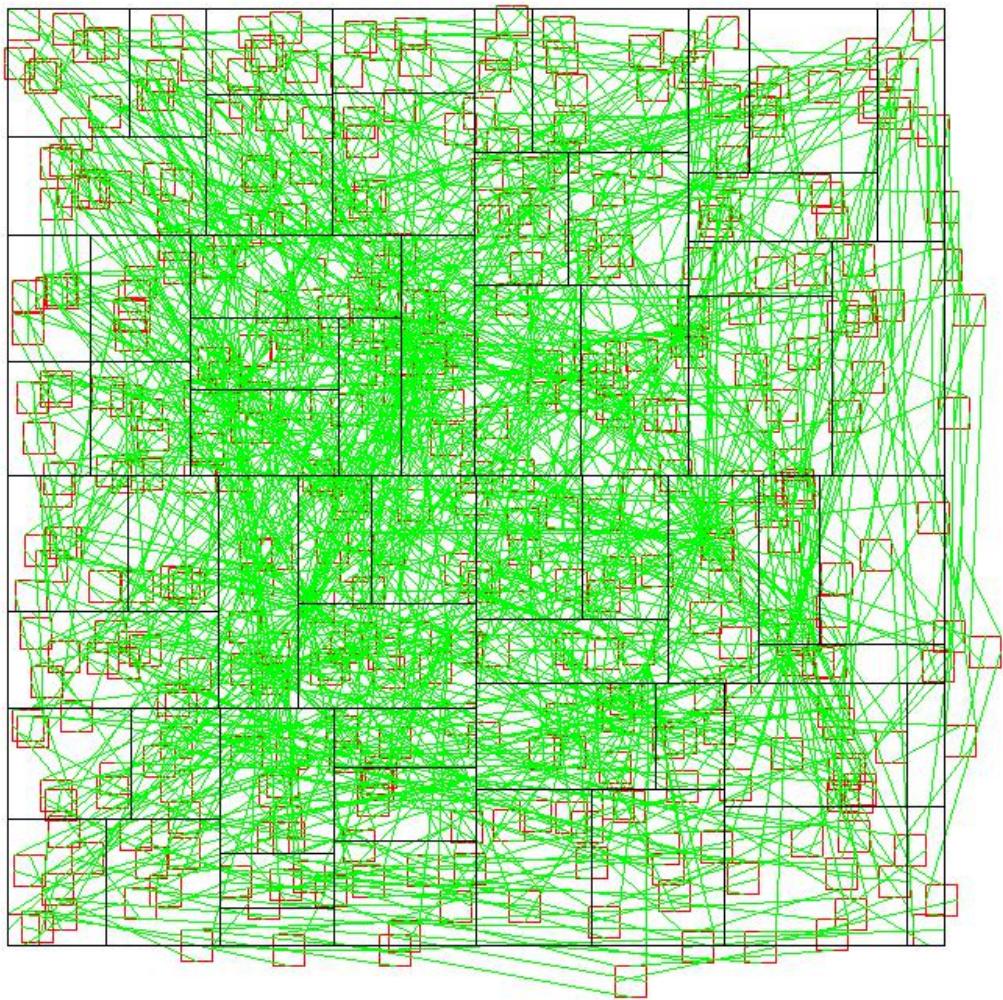


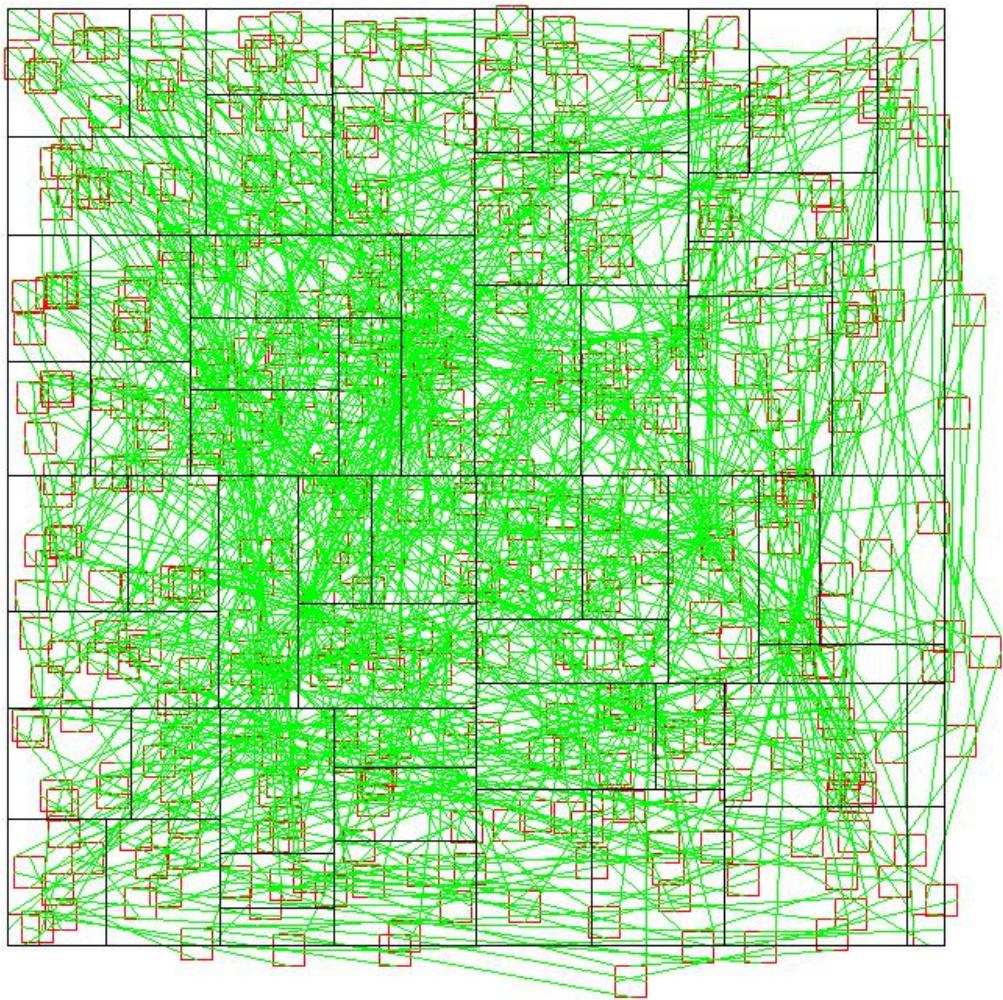












```
H:\Courses\CSE788_VLSI\course_project\Assignment2\Assignment2\x64\Releas... - □ ×  
input the name of netlist file.<include file name extension>  
netlist_H.txt  
partition finish!  
Overall iterations: 13  
Overall wire length is 150339.625<include fixed cells>  
Calculation end!  
Time for Gordian iterations: 58.94
```

Result for netlist_H.txt

