

Ref. no: EICT/2021-22/JFDP/SD&IoT/549

Electronics & ICT Academy
Supported by Ministry of Electronics and Information Technology (MeitY), Govt. of India
Indian Institute of Technology Guwahati

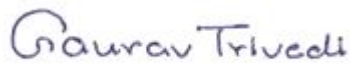
Participation Certificate



Mr./Ms./Dr.

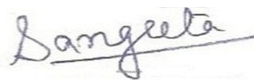
of

has participated in a two-week Online Faculty Development Programme on “System Design Methodologies for Embedded, IoT, AI, & HPC using Intel FPGA” jointly organised by Electronics and ICT Academies held from 19 - 30 April, 2021 under the “Scheme of financial assistance for setting up of Electronics and ICT Academies” of the Ministry of Electronics and Information Technology (MeitY), Government of India. He/she has also taken part in the hands on sessions during the program. We wish him/her a very good luck for his/her future endeavour. This Faculty Development Programme is at par with other Quality Improvement Programmes*.



Dr. Gaurav Trivedi

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* As per minutes of the 3rd PRSG.