/tb_mux_2x1_nbits/bits 4

/tb_mux_2x1_nbits/xin_0 1010 0001 0101

/tb_mux_2x1_nbits/xin_1 1100 1110 1001

/tb_mux_2x1_nbits/sel

/tb_mux_2x1_nbits/xout 1010 1100 0001 1001 0000

0 ps   50000 ps   100000 ps   150000 ps   200000 p

Entity:tb_mux_2x1_nbits Architecture: Date: Tue Sep 02 20:08:01 IST 2025 Row: 1 Page: 1