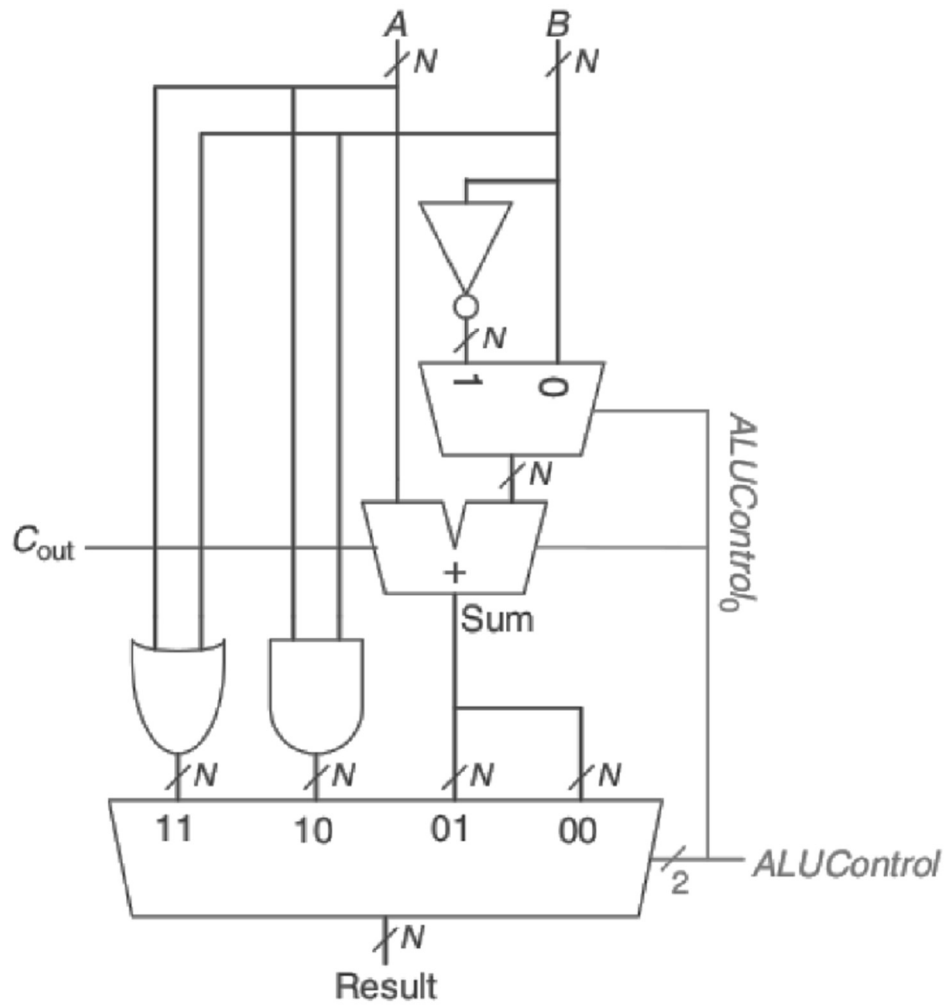


## Logic Diagram



## Simulation Result

		100.000 ns									
Name	Value	0.000 ns	10.000 ns	20.000 ns	30.000 ns	40.000 ns	50.000 ns	60.000 ns	70.000 ns	80.000 ns	90.000 ns
> A[31:0]	00000001	00000003	000000e8	00000005	f00000ff	0000000e	6a4563e8	0000000e	01111111	fffffffe	00000001
> B[31:0]	ffffff	00000001	000000a3	00000003	f00000ff	00000008	000006be	00000008	00000001	fffffffe	fffffffe
> ALUControl[2:0]	5	0	0	1	1	2	2	3	4	5	5
> Result[31:0]	00000002	00000004	0000019b	00000002	00000000	00000008	000002a8	0000000e	01111119	00000000	00000002