Datapath With Jumps Added

Ensure that no operations on registers nor memory
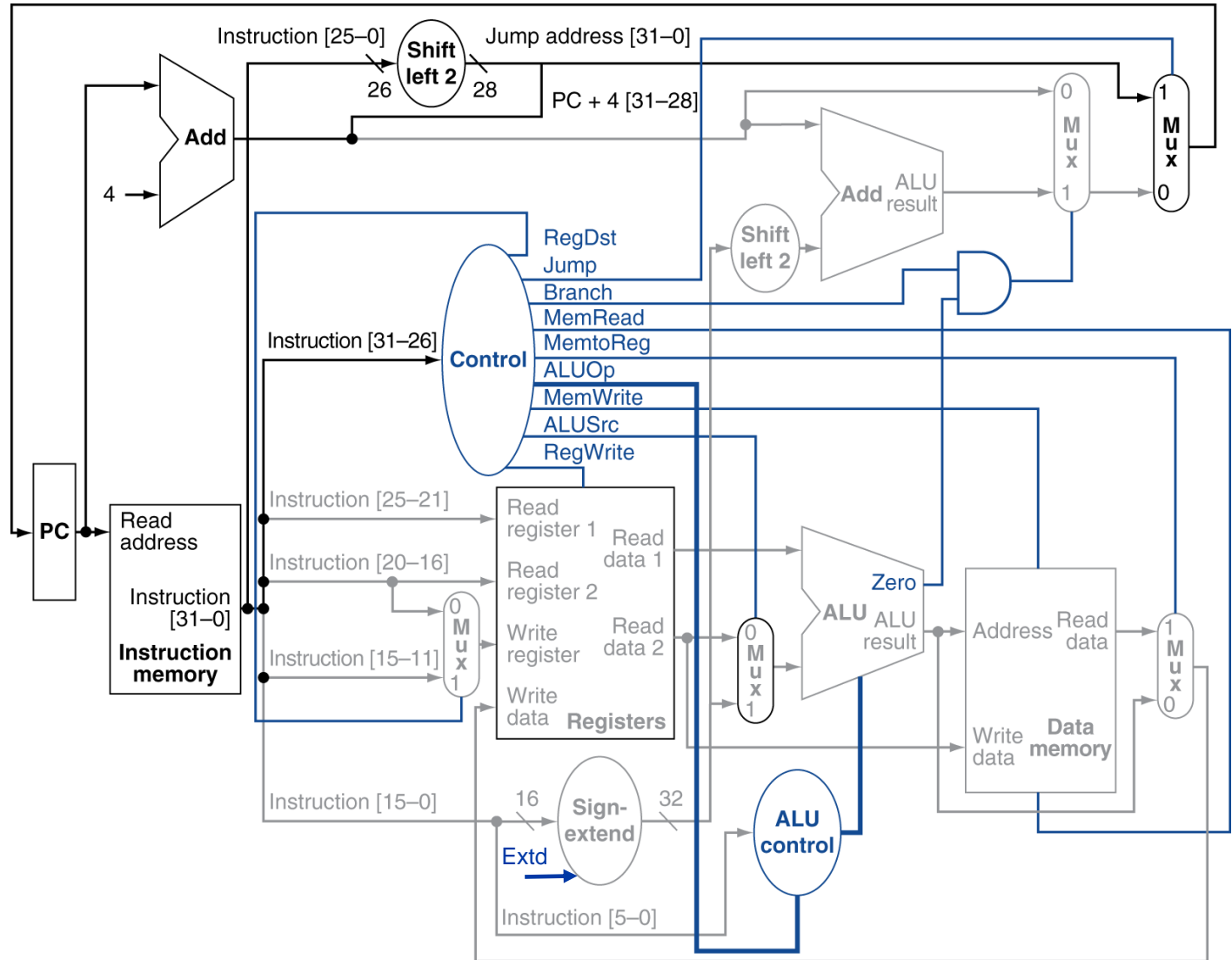
MemRead = 0
MemWrite = 0
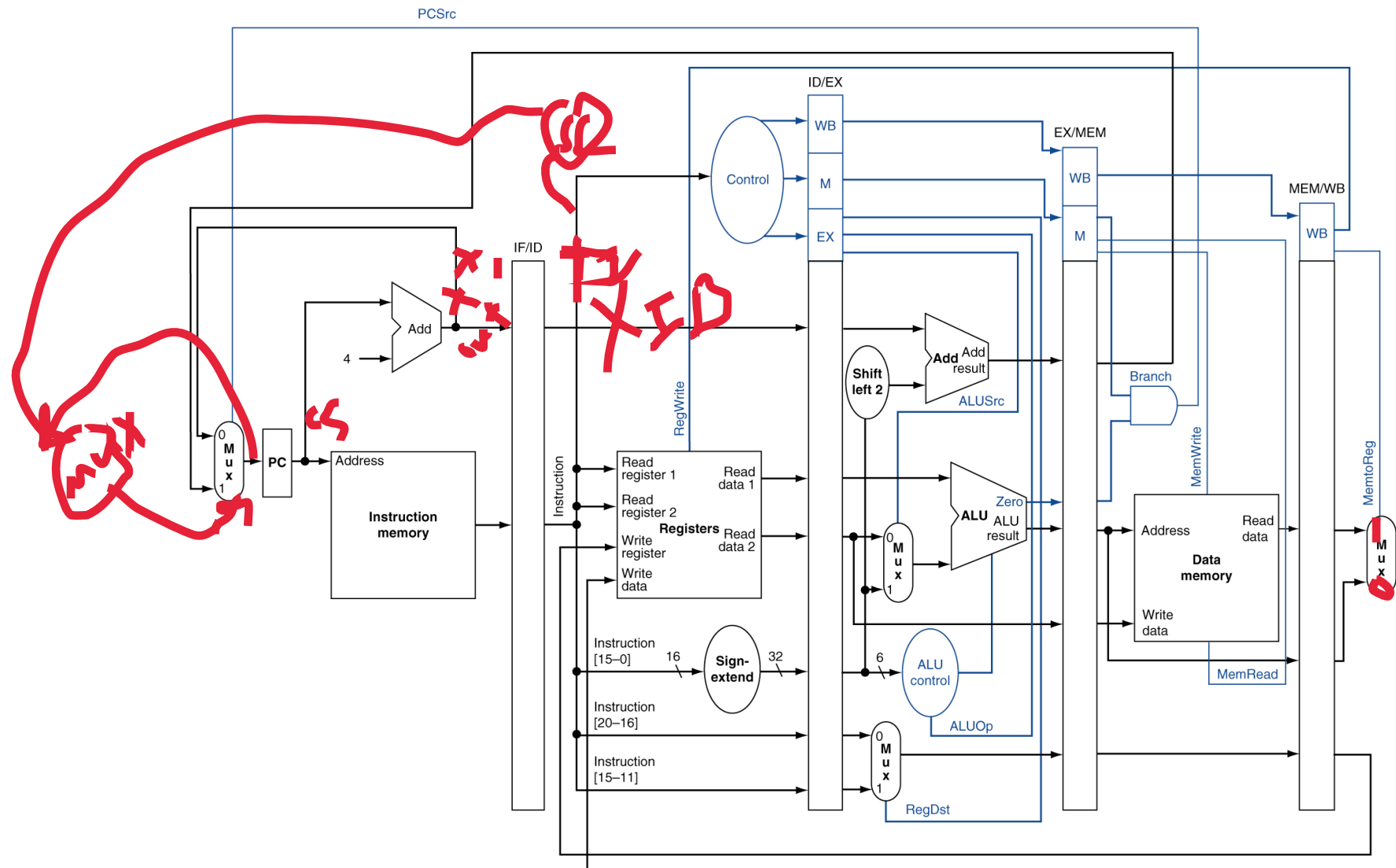**RegWrite = 0**

All other control signals do not care

Jump Target = 4 bits of old PC
: 26-bit jump address : 00

Jump = 1

PC is updated with Clock edge

MIPS Pipelined Control (FULL)

Main Control Signals

Operation	RegDst	RegWrite	Extd	ALUSrc	MemRead	MemWrite	MemtoReg
R-type	1	1	X	0	0	0	0
ADDI	0	1	1	1	0	0	0
SLTI	0	1	1	1	0	0	0
ANDI	0	1	0	1	0	0	0
ORI	0	1	0	1	0	0	0
XORI	0	1	0	1	0	0	0
LW	0	1	1	1	1	0	1
SW	X	0	1	1	0	1	X
BEQ	X	0	1	0	0	0	X
BNE	X	0	1	0	0	0	X