|  |  |
| --- | --- |
| 0\_ add s0 t1 t2 //ok | **0x012A8020** |
| 4\_addi s0 t1 0x00000001 //ok | 0x21300001 |
| 8\_sub s0 t1 t2 //ok | 0x012A8022 |
| 12\_and s0 t1 t2 //ok | 0x012A8024 |
| 16\_or s0 t1 t2 //ok | 0x012A8025 |
| 20\_slt t0 s1 s2 //ok | 0x0232402A |
| 24\_lw t0 0x00000000(s2) //ok | **0x8E480000** |
| 28\_sw t0 0x00000000(s1) // ok | 0xAE280000 |
| 32\_beq t2 , t3 , 0x00000003 //ok | **0x114B0003** |
| 36\_Add s3,t1,t2 | **0x012A9820** |
| 40\_Or s5 ,t0 ,t6 | **0x010EA825** |
| 44\_And s4,t0,t6 | **0x010EA024** |
| 48\_j 0x00000002 // ok | **0x08000002** |