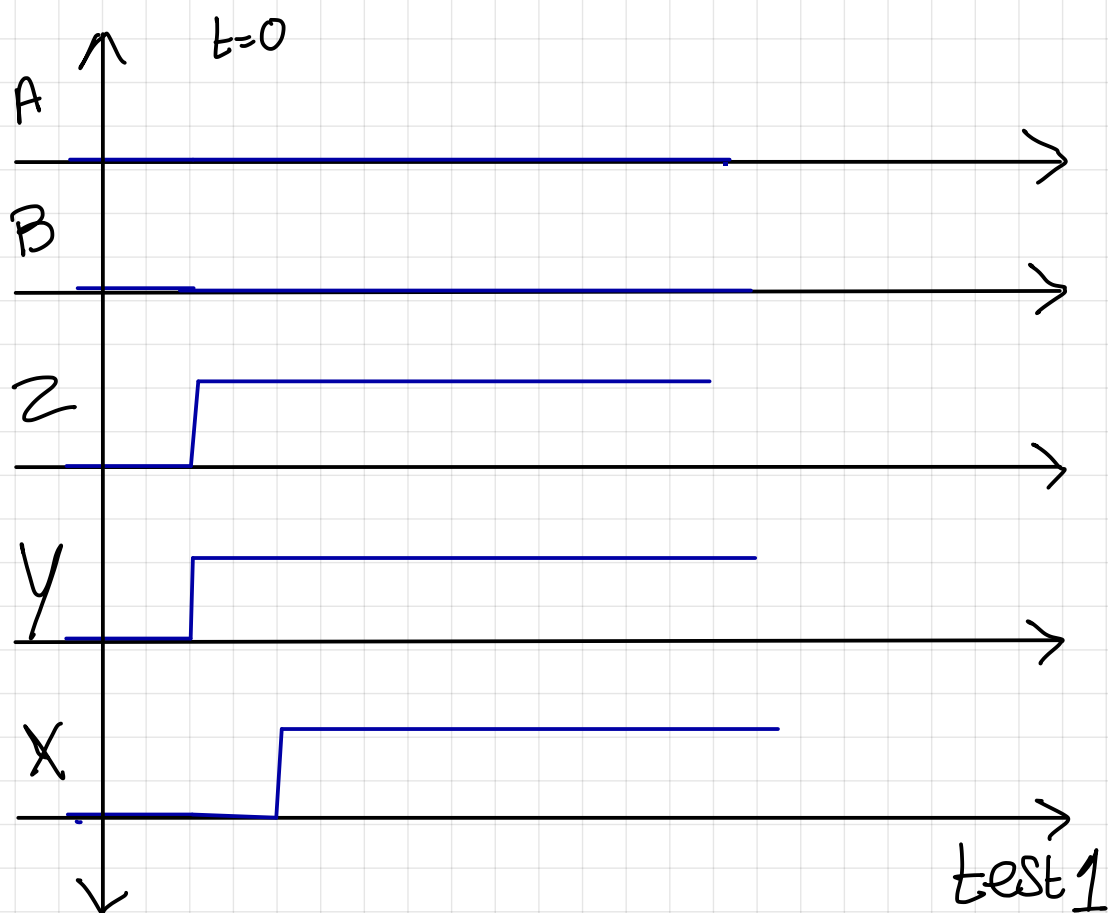
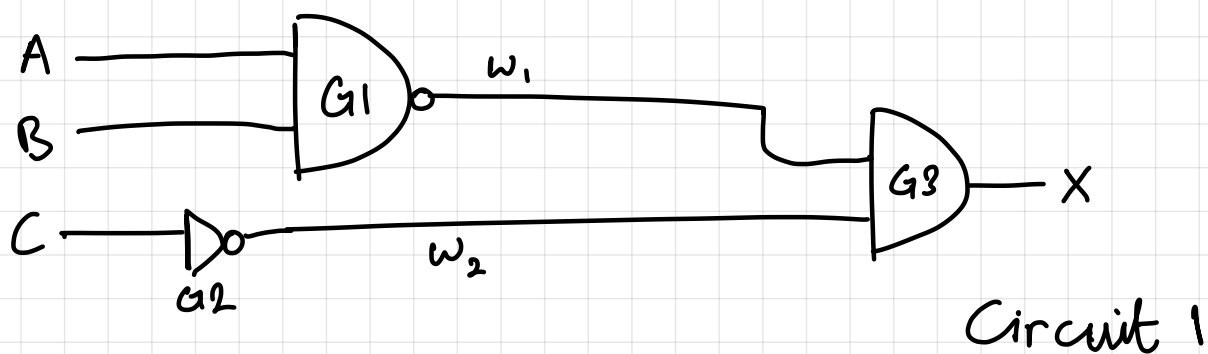
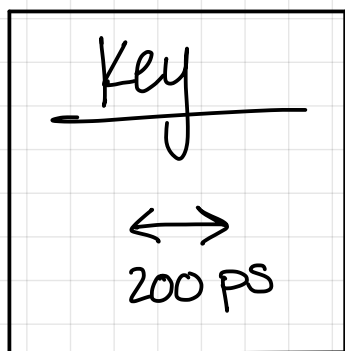
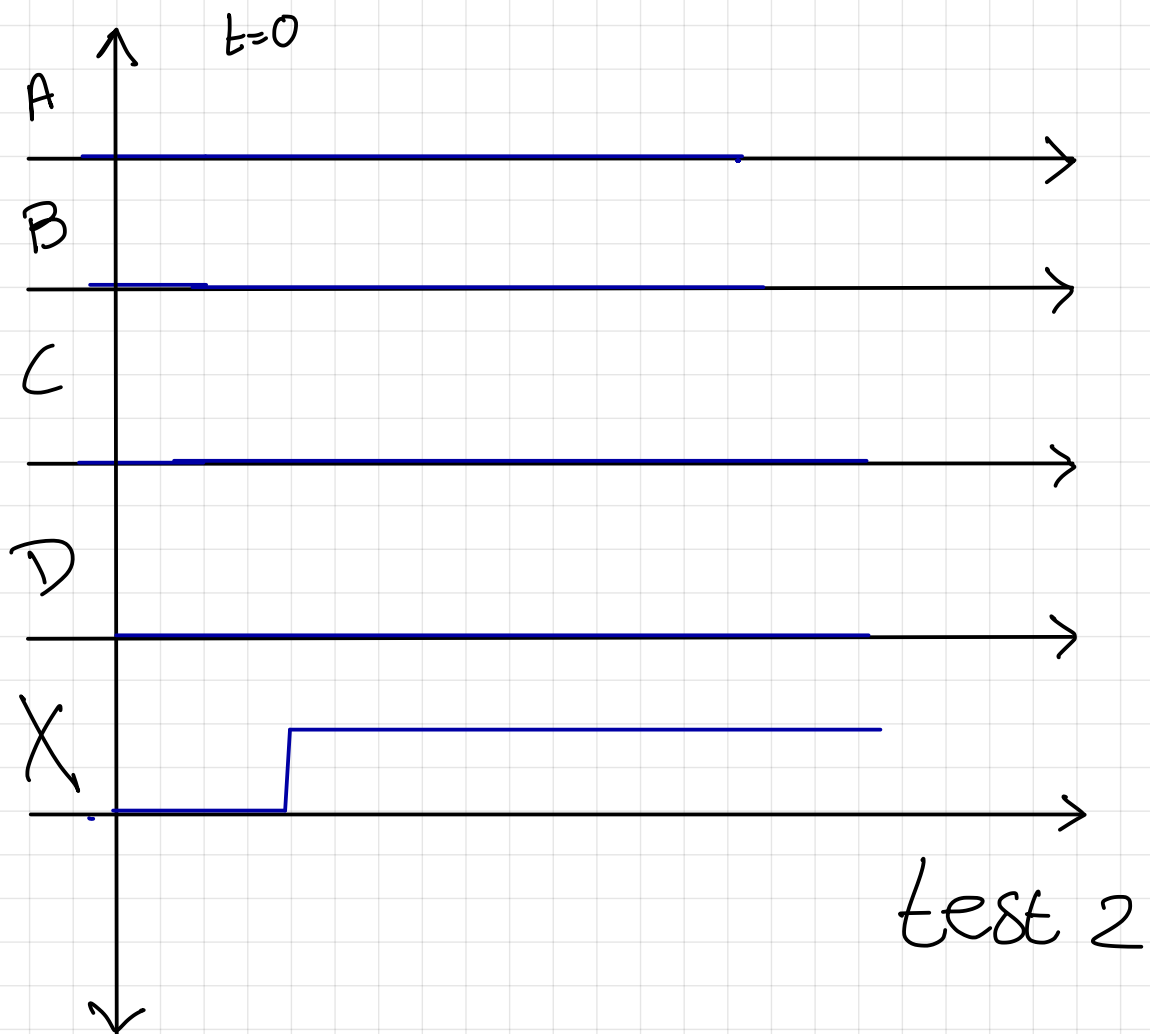
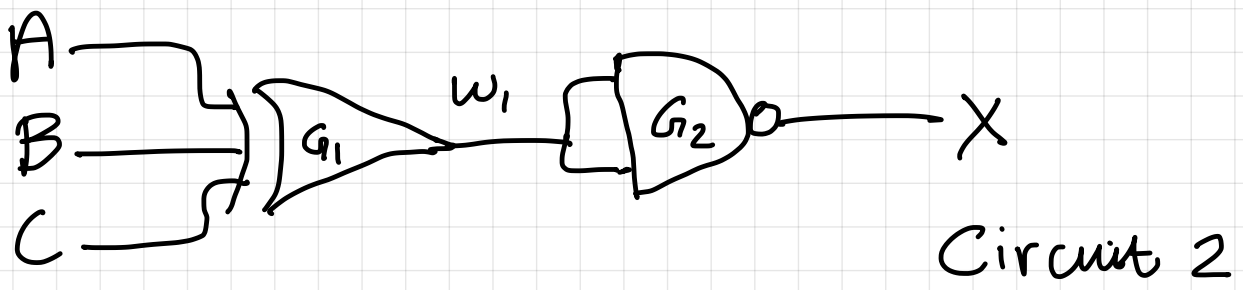
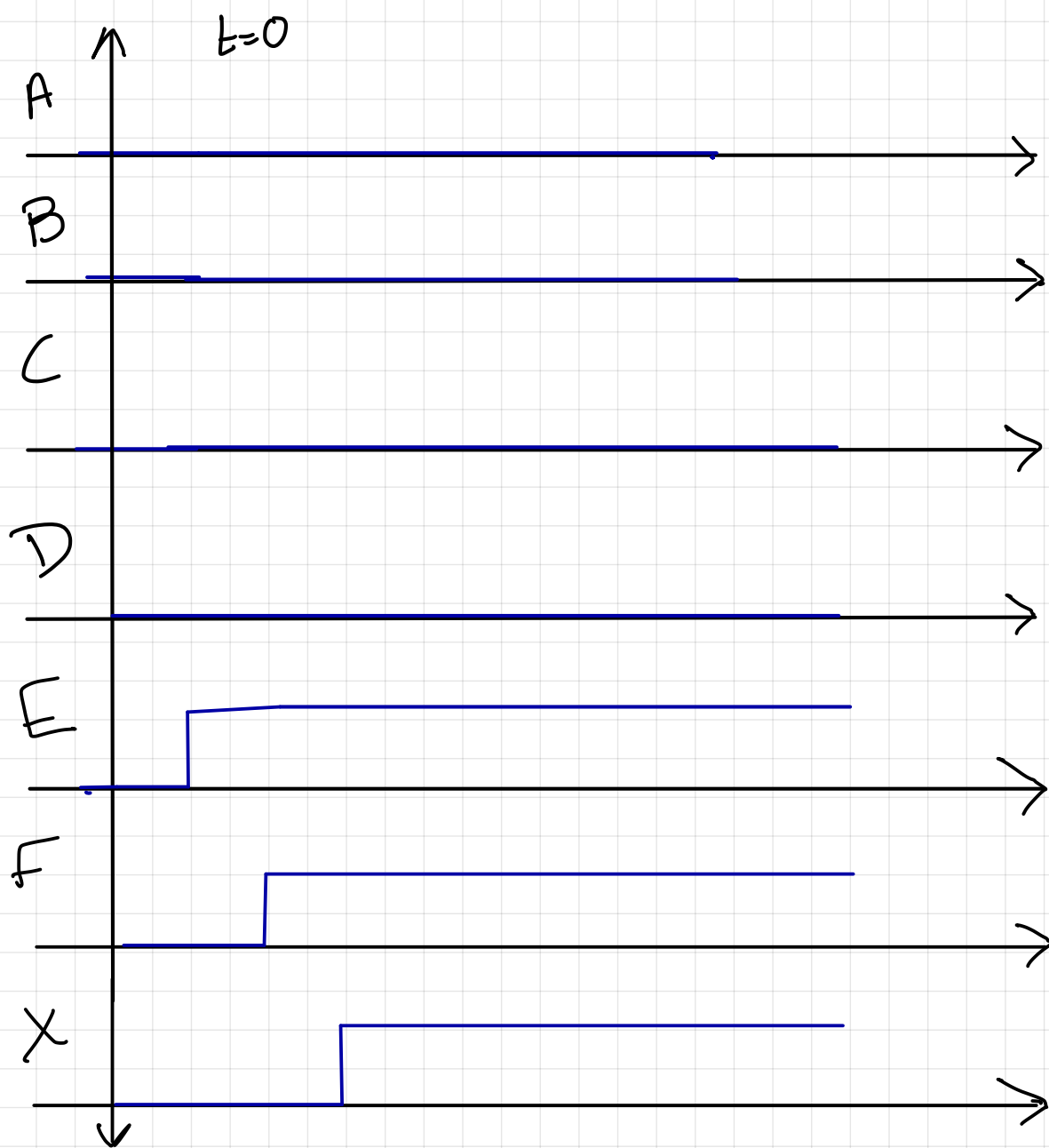
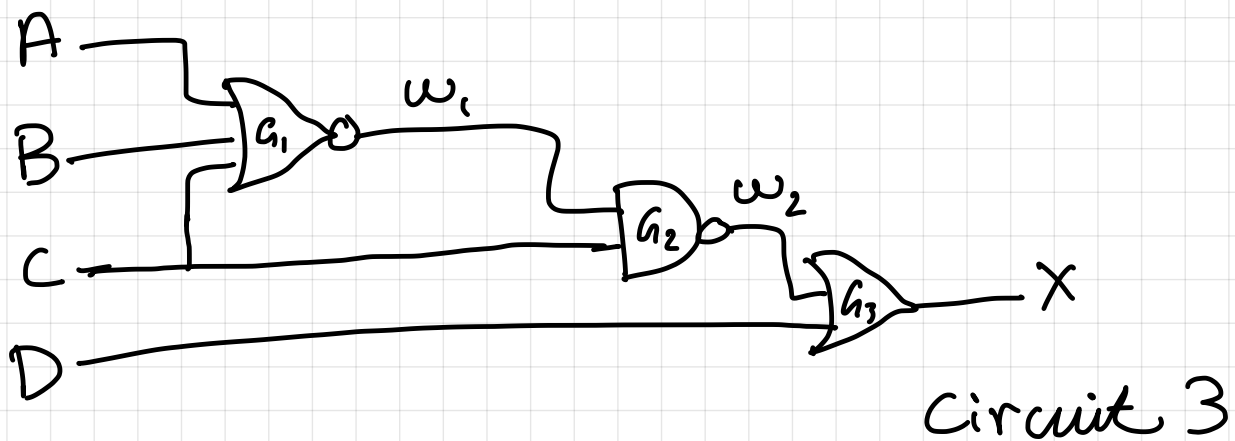


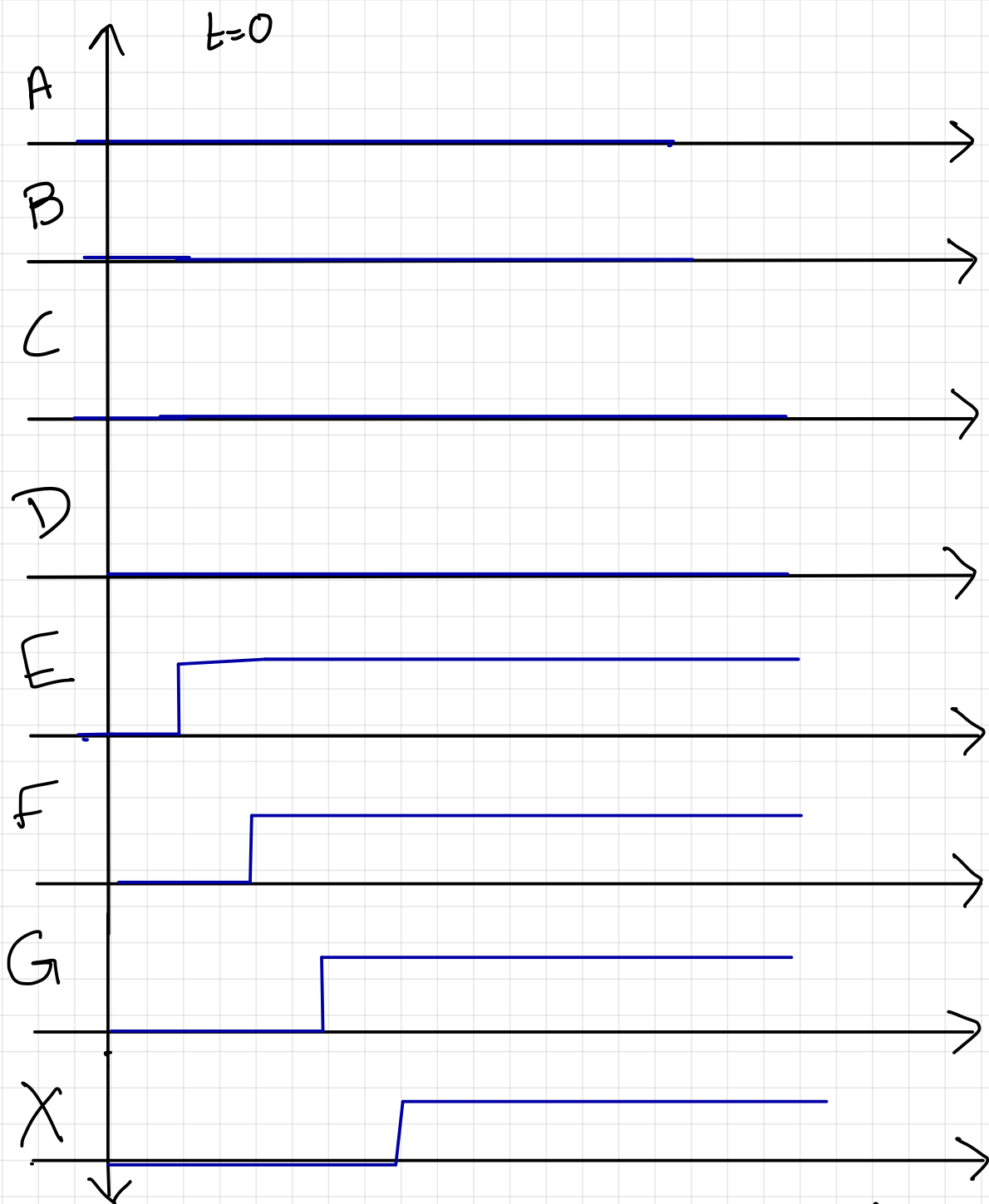
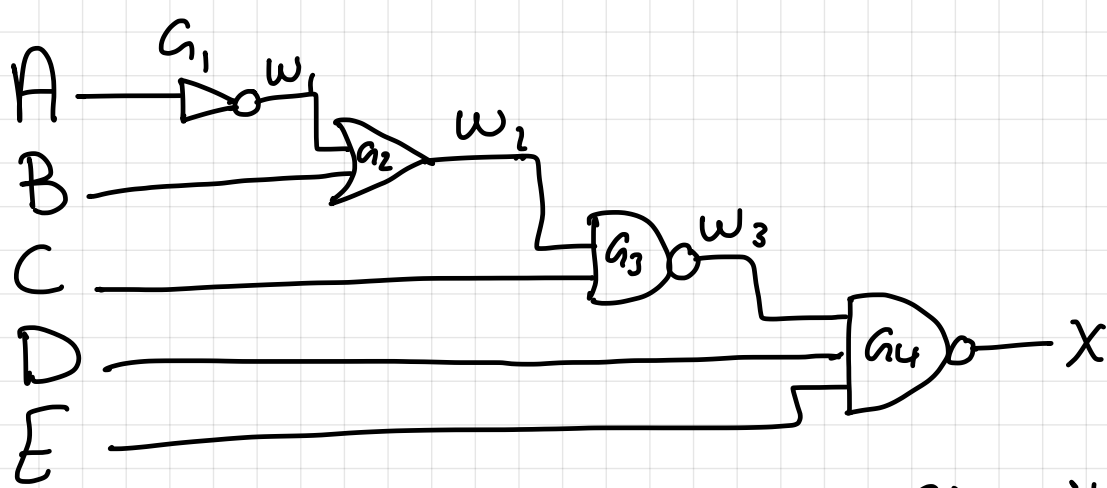
Group 3 5 test circuits + timing diagrams



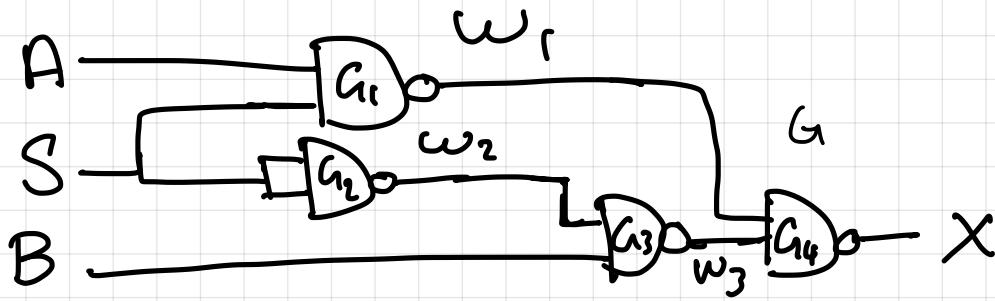




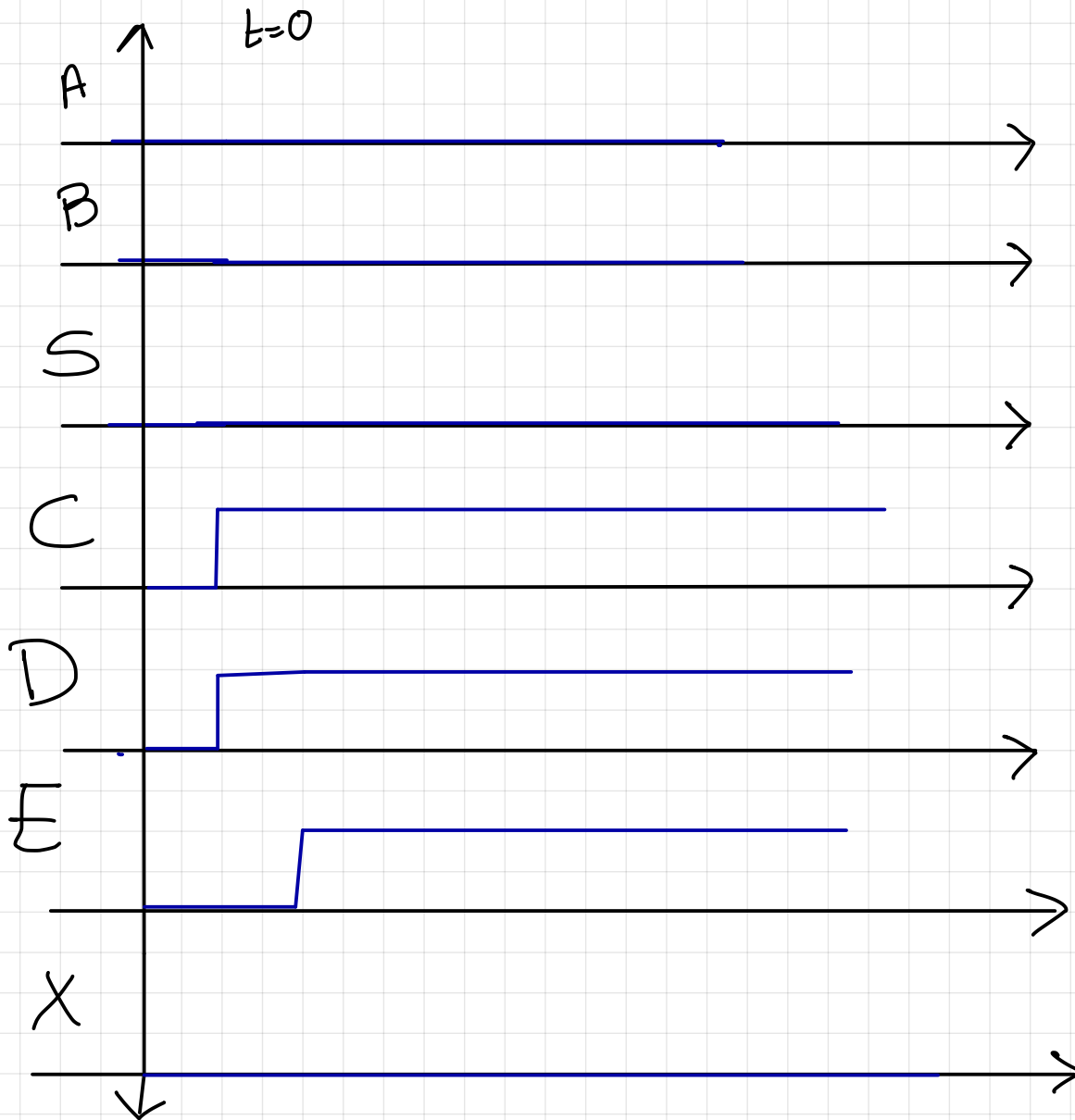
test 3



test 4



Circuit 8



test 5