project 5 Data Flow Diagram physical memony. output file (Inputfile.txt) Addrasses Party Party 9 output logic addresses distilue. address-value 1 Logical Address oader List physical memory an address-value (ogic addresses byte value record one bute Read physical logic Address list & store a logic address an address-talue memory a physical address a logic address offset physical Extract a logic address address page# TLB entries bForm physical 3 Check TLB address Search from(A) TLB replacement frame# TLB Whit trame# pagetable page# Check page table which entry ? (paget) page 1? paget Backingone 256-byte page rame# landle page# frame# 10. load a fine ► A to TLB replacement