UTAustinX: UT.6.01x Embedded Systems - Shape the World

KarenWest (/dashboard)

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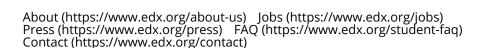
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Why does it sound so ugly?

Middle C is 261.6 Hz. How would you change the system so a middle C is generated?

What gets pushed on the stack after the hardware trigger and before the ISR is run? Why doesn't it push all the registers?

Assume switch bounce is less than 1 ms. If you check the status of the input switch in your SysTick ISR, and your SysTick runs every 1.1ms, what effect will switch bounce have on your system. What if the switch bounce lasts longer than 1.1 ms?





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