UTAustinX: UT.6.01x Embedded Systems - Shape the World

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Help

The simulation grader checked **accuracy** and the real board grader checked **linearity**. How are they similar and how are they different.

How does the **Nyquist Theorem** apply to this lab?

What factors affect **accuracy**?

What will happen if you increase your sampling rate 100 times faster?

It is bad style to perform display output in the SysTick ISR. Think of reasons why this is bad style.

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