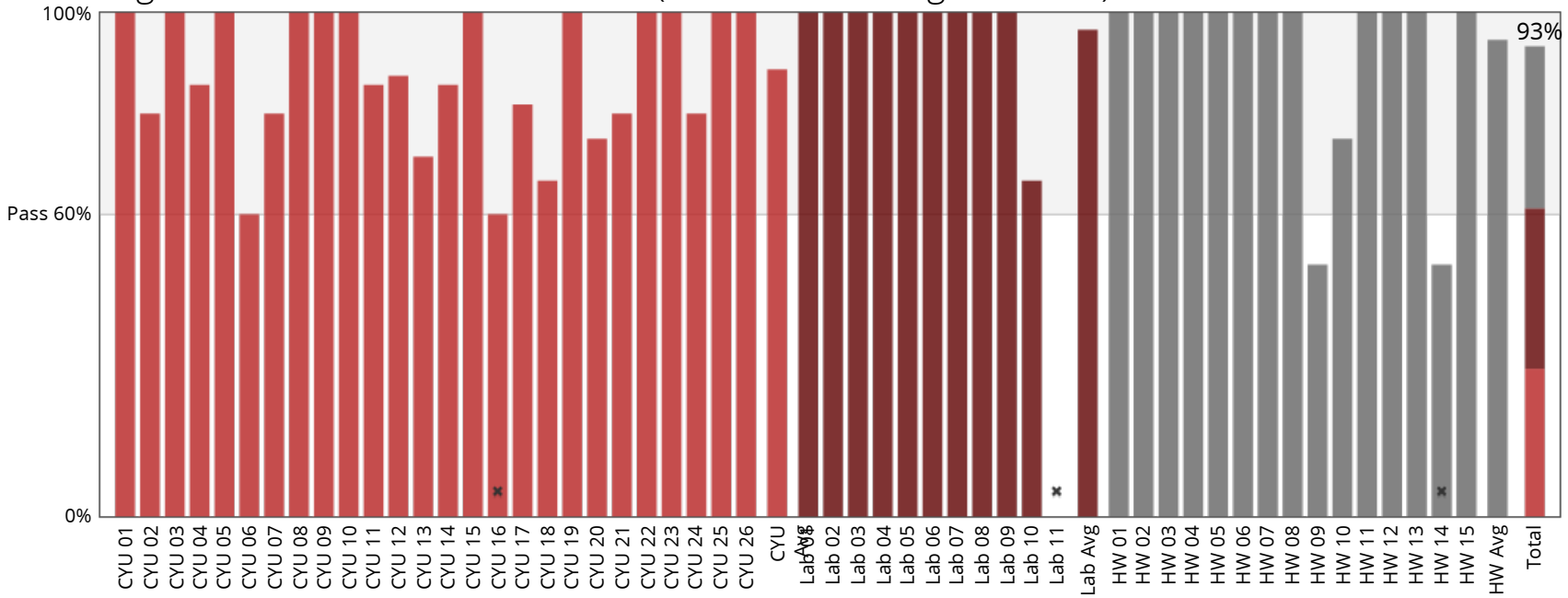
Help

Course Progress for Student 'KarenWest' (KarenWest15@gmail.com)



Introduction

Welcome to the Course

due May 12, 2015 at 18:00 UTC

No problem scores in this section

Pre-Course Survey

Help

No problem scores in this section

How the Course Works

No problem scores in this section

Inside Your Smartphone

No problem scores in this section

Big Ideas

No problem scores in this section

Please Introduce Yourself Here

No problem scores in this section

Digital Logic Weeks 1-3

Numbers (17/17) 100%

Check Your Understanding

Problem Scores: 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1

Numbers Homework (9/9) 100%

Problems

Problem Scores: 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1

Help

Operations (4/5) 80%

Check Your Understanding

Problem Scores: 1/1 1/1 0/1 1/1 1/1

Operations Homework (9/9) 100%

Problems

Problem Scores: 1/1 1/1 4/4 2/2 1/1

Transistors and Gates (5/5) 100%

Check Your Understanding

Problem Scores: 1/1 1/1 1/1 1/1 1/1

Transistors and Gates Homework (7/7) 100%

Problems

Problem Scores: 4/4 1/1 1/1 1/1

Transistors and Gates Labs (4/4) 100%

Lab

Problem Scores: 1/1 1/1 1/1 1/1

Muxes and Decoders (6/7) 86%

Check Your Understanding

Problem Scores: 2/2 1/1 1/1 1/2 1/1

Help

Muxes and Decoders Labs (5/5) 100%

Lab

Problem Scores: 1/1 1/1 1/1 1/1 1/1

Adders (1/1) 100%

Check Your Understanding

Problem Scores: 1/1

Adders Labs (6/6) 100%

Lab

Problem Scores: 1/1 1/1 1/1 1/1 1/1 1/1

Storage (3/5) 60%

Check Your Understanding

Problem Scores: 1/1 0/1 0/1 1/1 1/1

Storage Homework (4/4) 100%

Problems

Problem Scores: 1/1 1/1 1/1 1/1

Storage Labs (4/4) 100%

Lab

Problem Scores: 1/1 1/1 1/1 1/1

Help

FSM (4/5) 80%
Check Your Understanding
Problem Scores: 1/1 1/1 0/1 1/1 1/1

FSM Homework (11/11) 100%
Problems
Problem Scores: 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1

FSM Labs (1/1) 100%
Lab
Problem Scores: 1/1

Computer Organization Weeks 4-5

Stored Program Computer (6/6) 100%
Check Your Understanding
Problem Scores: 1/1 1/1 1/1 1/1 1/1 1/1

LC-3 Instructions (4/4) 100%
Check Your Understanding
Problem Scores: 1/1 1/1 1/1 1/1

LC-3 Instructions Homework (2/2) 100%
Problems
Problem Scores: 1/1 1/1

LC-3 (3/3) 100%

Help

Check Your Understanding

Problem Scores: 1/1 1/1 1/1

LC-3 Labs (5/5) 100%

Lab

Problem Scores: 1/1 1/1 1/1 1/1 1/1

Instruction Set Architecture (6/7) 86%

Check Your Understanding

Problem Scores: 1/1 1/1 1/1 1/1 1/1 1/1 0/1

Instruction Set Architecture Homework (2/2) 100%

Problems

Problem Scores: 1/1 1/1

Instruction Set Architecture Labs (2/2) 100%

Lab

Problem Scores: 1/1 1/1

ISA 2 (7/8) 88%

Check Your Understanding

Problem Scores: 1/1 1/1 0/1 1/1 1/1 1/1 1/1 1/1

ISA 2 Homework (2/2) 100%

Problems

Problem Scores: 1/1 1/1

Help

ISA 2 Labs (1/1) 100%
Lab
Problem Scores: 1/1

ARM ISA
No problem scores in this section

LC-3 Control (5/7) 71%
Check Your Understanding
Problem Scores: 1/1 1/1 1/1 0/1 1/1 1/1 0/1

LC-3 Control Labs (2/2) 100%
Lab
Problem Scores: 1/1 1/1

Programming Week 6

Finishing the LC-3 Lite Lab (2/3) 67%
Lab
Problem Scores: 1/1 1/1 0/1 0/0 0/0

Programming to Solve Problems (6/7) 86%
Check Your Understanding
Problem Scores: 1/1 1/1 1/1 0/1 1/1 1/1 1/1

Assembly Language (9/9) 100%
Check Your Understanding

Problem Scores: 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1

Programming the LC-3 Lite Lab (0/2)

Lab

Problem Scores: 0/0 0/0 0/0 0/0 0/1 0/0 0/0 0/1 0/0 0/0 0/0 0/0 0/0 0/0

Input/Output (I/O) (3/5) 60%

Check Your Understanding

Problem Scores: 1/1 1/1 1/1 0/1 0/1

C Programming (9/11) 82%

Check Your Understanding

Problem Scores: 1/1 0/1 0/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1 1/1

Performance Weeks 7-8

Performance (2/3) 67%

Check Your Understanding

Problem Scores: 0/1 1/1 1/1

Pipelining (7/7) 100%

Check Your Understanding

Problem Scores: 1/1 1/1 1/1 1/1 1/1 1/1 1/1

Help

Pipelining Homework (1/2) 50%
Problems

Problem Scores: 1/1 0/1

Hazards (3/4) 75%
Check Your Understanding

Problem Scores: 1/1 1/1 0/1 1/1

Hazards Homework (3/4) 75%
Problems

Problem Scores: 1/1 0/1 1/1 1/1

Instruction Level Parallelism (4/5) 80%
Check Your Understanding

Problem Scores: 1/1 1/1 0/1 1/1 1/1

Instruction Level Parallelism Homework (3/3) 100%
Problems

Problem Scores: 1/1 1/1 1/1

Thread Level Parallelism (1/1) 100%
Check Your Understanding

Problem Scores: 1/1

Help

Thread Level Parallelism Homework (2/2) 100%

Problems

Problem Scores: 1/1 1/1

Data Level Parallelism (1/1) 100%

Check Your Understanding

Problem Scores: 1/1

Data Level Parallelism Homework (2/2) 100%

Problems

Problem Scores: 1/1 1/1

Pipelined LC-3 Lite Hackathon (Optional, Ungraded)

Practice Scores: 0/0 0/0 0/0 0/0 0/0 0/0 0/0 0/0

Caches (8/10) 80%

Check Your Understanding

Problem Scores: 0/1 1/1 5/5 1/1 0/1 1/1

Caches Homework (1/2) 50%

Problems

Problem Scores: 0/1 1/1

Multicore (3/3) 100%

Check Your Understanding

Problem Scores: 1/1 1/1 1/1

Help

Multicore Homework (5/5) 100%

Problems

Problem Scores: 1/1 1/1 1/1 1/1 1/1

Permanent Storage (1/1) 100%

Check Your Understanding

Problem Scores: 1/1

Cache Hackathon (optional, ungraded)

Practice Scores: 0/0 0/0

Conclusion and Post-Survey

Snapdragon

due May 12, 2015 at 18:00 UTC

No problem scores in this section

Conclusion

No problem scores in this section

Post Survey

No problem scores in this section

Help

Jade Tutorial and Sandbox	Jade Sandbox Practice Scores: 0/0 Jade Tutorial Practice Scores: 0/0
Guest Interviews	Yale Patt (University of Texas at Austin Professor) No problem scores in this section Chris Batten (Cornell Professor) No problem scores in this section Rajit Manohar (Cornell Professor) No problem scores in this section Adam Kerin (Qualcomm) No problem scores in this section Ava Tan and Gulnar Mirza (Cornell students) No problem scores in this section

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