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Help

HOMEWORK 1. LC-3 INSTRUCTIONS (1/1 point)

An LC-3 ADD instruction has an Opcode of 0001 and the following format:

An LC-3 LDR instruction has an Opcode of 0110 and the following format:

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
	LI	OR		]	Dst		В	as	e		(	Of	fse	t	

The values in decimal of the LC-3 register file are as follows:

R0: 0

R1: 10

R2: 22

R3: 2

R4: 3

R5: 17

R6: 1

R7: 14

The first four locations in memory have the following values:

0:16

1:10

2: 5

3: 7

The following three instructions are executed one after the other:

0110110011000000

0110111011000001

00011111111000110

nework 1. Lo R3 will c	hange to 1	0 and R7 will ch	ange to 24.			
R3 will c	hange to 1	0 and R6 will cha	ange to 24.			
R6 will o	hange to 5	and R7 will cha	nge to 7.			
• Ke Will C	nange to 5	and R7 will cha	nge to 12.			
EXPLANATIO	J					
The first LDR	instruction	n adds the offset	t of 0 to the contents of R3	(2) to form the memory	address of 2. The v	value at memory location
			instruction adds the offset	•		<del>-</del>
value at men	ory locatio	on 3 (7) is loaded	l into R7. The last ADD inst	ruction performs R7 = R	7 + R6, which chan	ges the value of R7 to 12.
Final Check	Save	Hide Answer	You have used 1 of 2 subi	nissions		
Show Disc	ussion					<b>☑</b> New Post
LC-3 STR ins	truction st se register		ONS (1/1 point) m a register into a memory loading (reading) data from			
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