edX **CornellX: ENGRI1210x The Computing Technology Inside Your Smartphone**   🏠 **KarenWest**   ▼

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This module introduces a commonly used ISA, the ARM ISA. This ISA is used in the vast majority of smartphones today. We will discuss a little about its design heritage as a Reduced Instruction Set Computing (RISC) architecture, and compare its majors features to those of the LC-3.

At the end of this module you will be able to:

- Contribute, in brief, to the debate regarding RISC versus CISC architectures.

- Describe the high-level features of the ARM ISA, and compare those features against those found in the LC-3 ISA.

INTRODUCING THE ARM ISA

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