edX   **CornellX: ENGRI1210x The Computing Technology Inside Your Smartphone**   👤 KarenWest   ▼

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How to Use Jade

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INSTRUCTION PROCESSING PHASES

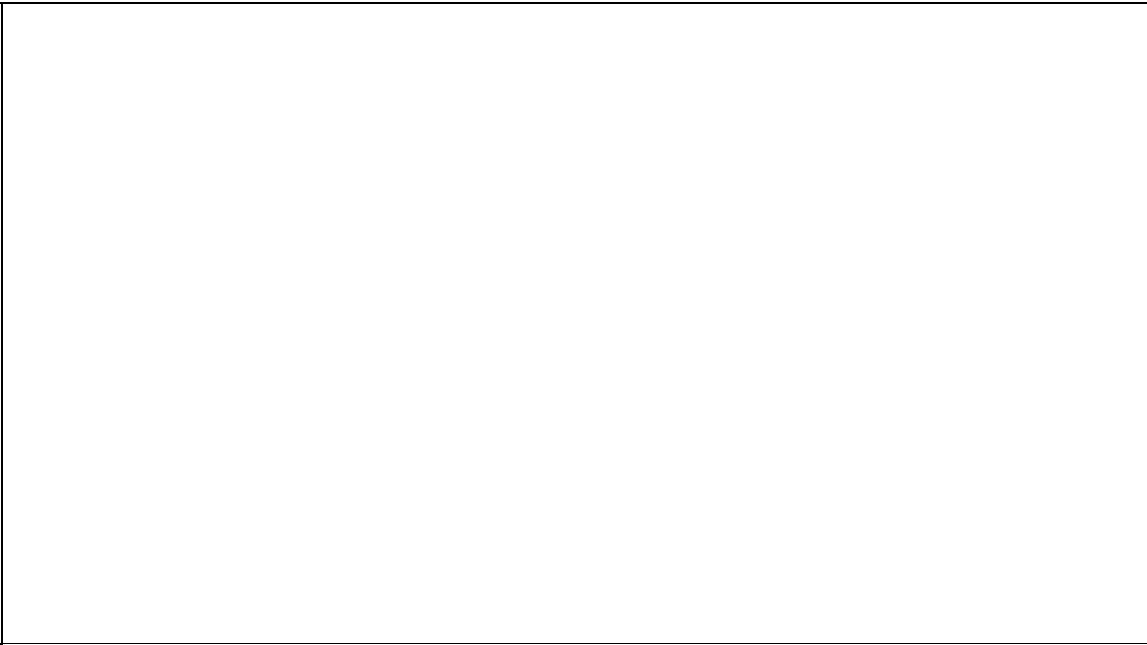**4:35 / 4:35**   **1.0x**

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1. CHECK YOUR UNDERSTANDING (1/1 point)

Which of the following statements properly describe the operation of different instructions during the phases of instruction processing? [Check all that apply]

- ☑ Instruction fetch and decode are performed for all instructions.   ✔

- ☐ No action is performed during Evaluation Address for LDR.

- ☐ No action is performed during Fetch Operands for ADD.

- ☑ No action is performed during Execute for LDR.   ✔

EXPLANATION   04/23/2015 05:06 PM

All instructions are fetched from memory and decoded. For LDR during Evaluate Address, the offset is added to the base register to form the memory address. For ADD during Fetch Operands, data is read from the register file. For LDR during Execute, no action is performed since the LDR does not perform any arithmetic or logical operation.

Final Check   **Save**   Hide Answer   *You have used 1 of 2 submissions*

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