

Help

In this lab we will implement the FSM described in the "Pattern Detectors" unit.

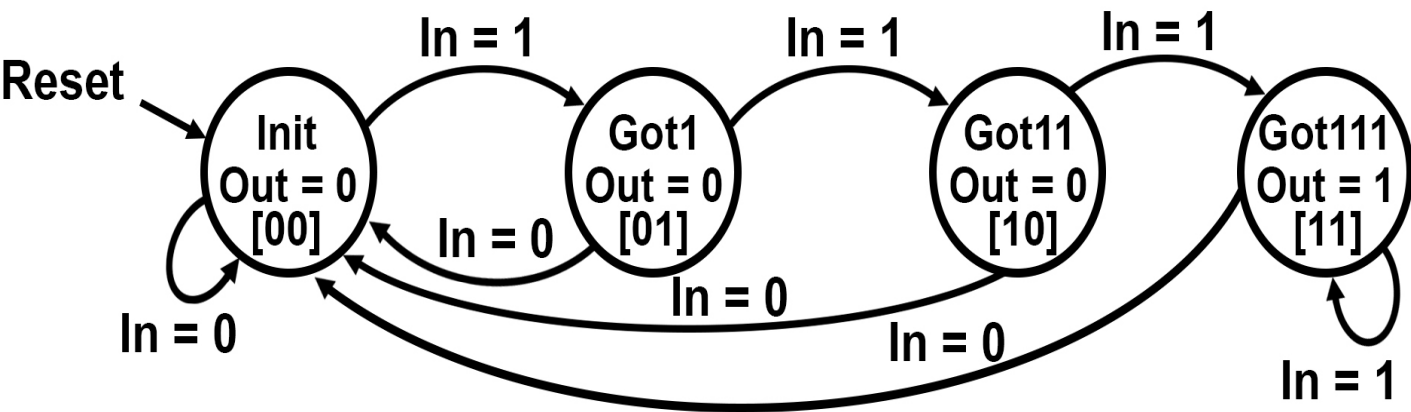
In the video, most of the FSM is actually built for us. We want to finish it up in this lab.

The state transition diagram and transition table is shown below for your reference.

You will NOT need to load anything, nor save the lab to the module clipboard.

Build the FSM and test it using the provided test file, and press "Check" to submit it to edX, as in previous labs.

You will need to label your State outputs as "S1_next" and "S0_next". The test checks for these signals!




S ₁ S ₀	S ₁ ' S ₀ '		Out
	In = 0	In = 1	
0 0	0 0	0 1	0
0 1	0 0	1 0	0
1 0	0 0	1 1	0
1 1	0 0	1 1	1

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