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LC3 Register File Lab Question

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discussion posted about 17 hours ago by KarenWest



Hi,

I've almost completed the LC3 Register File lab, and I have an error at 79.5 ns in the tests. I'll be back later, but I was wondering if given I hit the check button, if any of the teaching assistants or the professor can see what my mistake is there. I know you can see my design once I hit the check button. Thanks.

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DavidAlbonesi STAFF

about 15 hours ago

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05/05/2015 11:26 AM

R4 should be treated the same as the other registers, except for the connection to Reset. Connect the DATA

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☐
input to Data and use an AND gate just like the others.

<https://courses.edx.org/courses/CornellX/ENGR1210x/1T2015/discussion/foru...>

Thank you - I will make that change and test it again!

posted about 14 hours ago by

KarenWest

That worked - thank you! Now perhaps the lab that I had trouble with last week (Instruction Set Architecture) will make better sense - somehow I missed parts of the LC3 labs without noticing. I'm still trying to finish on time since weeks 7-8 are the parts I wanted to learn/review the most. I also started the class 2 weeks late (which never helps!) But I may make it - so thank you for your help.

posted about 14 hours ago by

KarenWest

I have done some of this before (weeks 1-6) but many years ago, and never applied to a smart phone, so I've enjoyed relearning it and using the new Jade tool. I think I learned it with Mentor Graphics in the mid-1990's, and not applied to a smart phone in those days.

posted about 14 hours ago by

KarenWest

If my memory serves me right, at Prime Computer we initially did designs using tools by Daisy Systems and eventually moved to Cadence.

posted about 13 hours ago by

DavidAlbonesi

STAFF

My early days were at Digital Equipment Corporation (1987-1993, <https://courses.edx.org/courses/CornellX/ENGRI1210x/1T2015/discussion/foru...>) my first job before going to grad. school in the mid 1990's, where I

did this chip design). DEC had their own in-house cad tools, since in those days everything was proprietary. I did not do chip design there though and instead wrote embedded software. I worked for someone once in DEC's former chip design group though, when he branched out to form his own start up in the mid to late 1990s, and I joined in the early 2000's. He said what put DEC out of business in the late 90's (after 50 years) were companies (like Dell he said) that did not build all the parts themselves. The idea of his start up business was to take a vertical market, a market such DEC, a proprietary market where the company made all the chips, software, and boards for computers for themselves, and cut across this vertical organization into a more horizontal market. For example, Dell Computer is more of a horizontal market, in that they contracted chips, boards, and software which allowed them to take advantage of the "economies of scale", and consequently their costs as a company were lower than DEC's. So anyway, sometimes it makes sense to make your own, and other times perhaps it does not. This was what my former start up boss (2002-2004) told me - I'm no expert about these things! ;-)

posted less than a minute ago by **KarenWest**

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