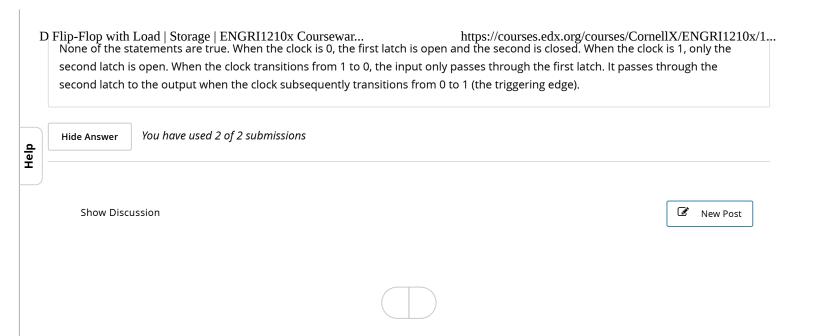
Course Info Discussion Wiki **Discussion Guidelines Exploring Engineering** Courseware **Progress** Resources **Syllabus** How to Use Jade Help D FLIP-FLOP WITH LOAD 2:24 / 2:24 1.0x Download transcript .txt **Show Discussion** New Post 1. CHECK YOUR UNDERSTANDING (1 point possible) Which of the following statements properly describe the operation of a positive edge-triggered D flip-flop designed using back-to-back D latches? [Check all that apply] When the clock is 0, the first latch is closed and the second is open. When the clock is 1, both latches are open. When the clock transitions from 1 to 0, the input value appears at the flip-flop output.

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None of the above. 💙





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