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ypes (of Transistors T	Transistors and Gate	<u>s ENGRI1210</u>	https://cours	ses.edx.org/cours	es/Cornell	X/ENGRI1	1210x/1
	5:52 / 5:52					1.0x		
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Dov	vnload transcript	.txt						
	Show Discussion	ı					☑ New F	Post
		JNDERSTANDING						
What	is the value of th	he gate input of a tra	ansistor when the switch	is open?				
	0 for n-ty	pe and 0 for p-type						
	0 for n-ty	pe and 1 for p-type	✓					
	1 for n-ty	pe and 0 for p-type						
	1 for n-ty	pe and 1 for p-type						
EXF	PLANATION							
For	an n-type transi	istor, a 0 on the gate	input opens the switch a	and a 1 closes it. The oppo	osite is true for a p	o-type tran	sistor	
			1					
Fina	al Check Sav	e Hide Answer	You have used 1 of 2 sui	bmissions				

2. CHECK YOUR UNDERSTANDING (1/1 point)

In a static CMOS logic gate, the ______ transistors are connected to the supply voltage and the ______ transistors are connected to ground.

Help

n-type, n-type
p-type, p-type

n-type, p-type

• p-type, n-type 💙

EXPLANATION

The p-type transistors are connected to the supply voltage and pull the output voltage up to a 1 for particular inputs. The n-type transistors are connected to ground and pull the output voltage down to a 0 for particular inputs.

Final Check

Save

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