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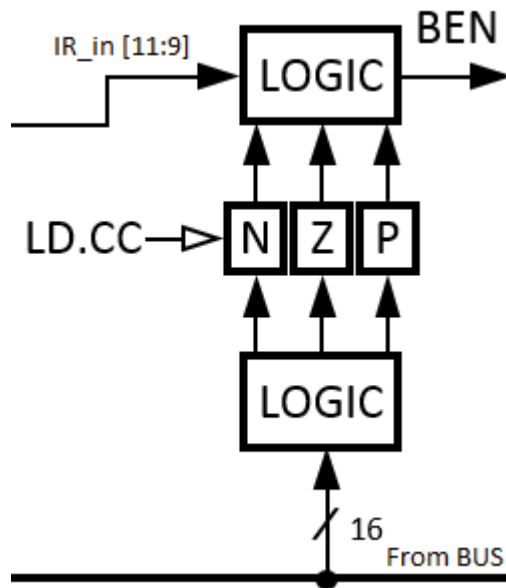
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Lab 8: Branch Logic and Condition Code Register

In this lab we will combine our condition code (nzp) logic and branch enable combinational logic with some flip-flops to complete our Branch Enable unit.

We will be creating this part of the LC-3 Lite. We've already built the top and bottom blocks labeled "Logic." We'll connect them with loadable flip-flops.




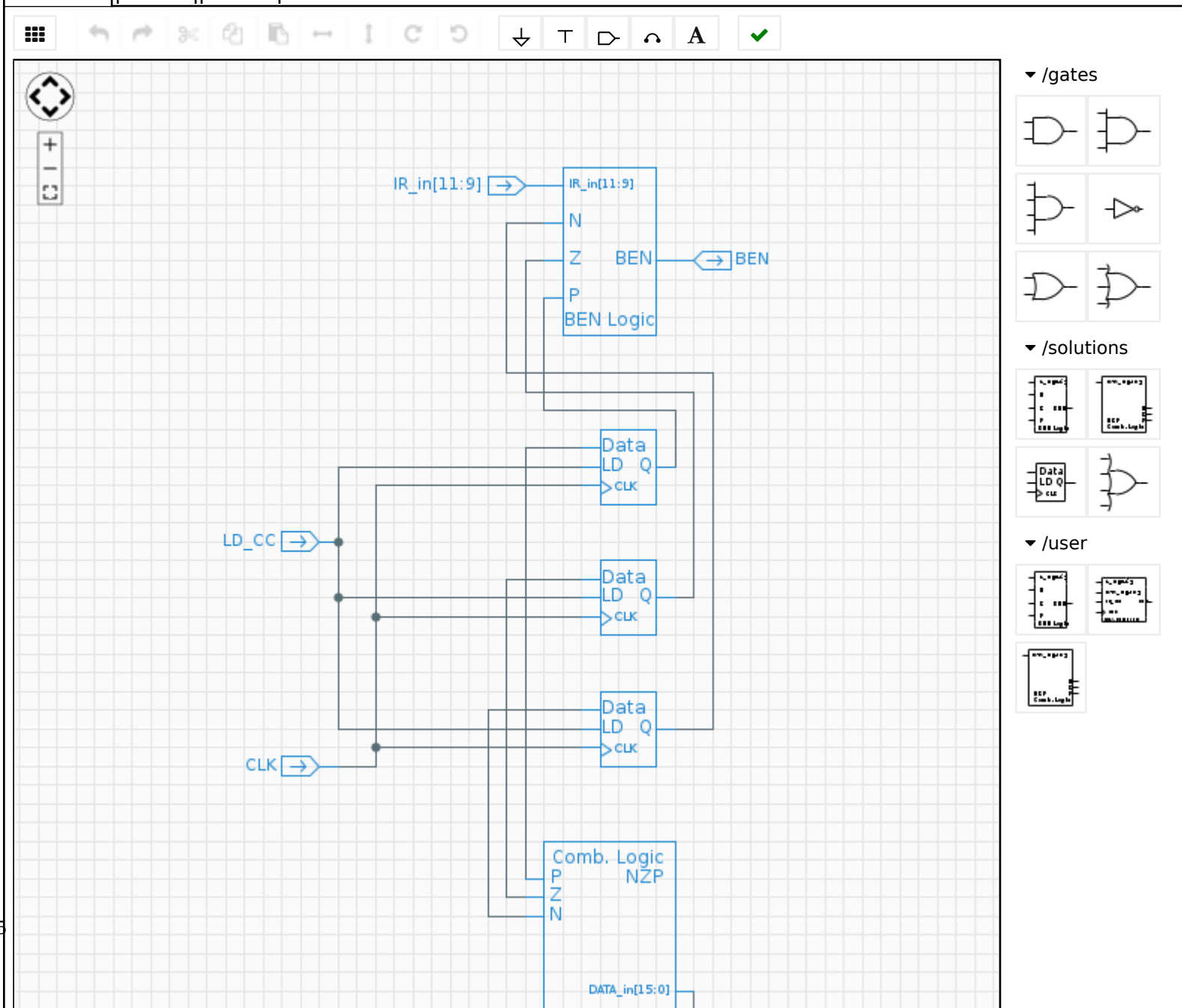
To do so, use your previously created modules `"/user/lc3benlogic"` and `"/user/lc3nzplogic"`, add 3 loadable flip-flops (`/user/loadableregister/` or `/solutions/loadableregister_s` -- one each for N, Z, and P), and hook them together with your modules.

Don't forget to load your `/user/` modules if you want to use them! Solutions from previous labs are also available.

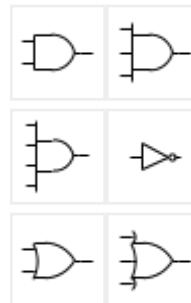
Check your work with the green check button in the toolbar of Jade, save your modules and submit by clicking the "Check" button.

Help

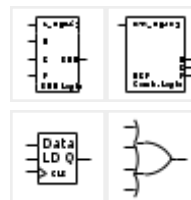
LC-3 LITE BRANCH LOGIC AND CC REGISTER (1/1 point)

Module:   SCHEMATIC ☒ ICON ☐ TEST ☐

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Lab 8: Branch Logic and Condition Code Register | Finishing the LC-3 Lite Lab ...

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