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## MUX LOGIC DIAGRAMS

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1.0x

## 1. CHECK YOUR UNDERSTANDING (1/1 point)

Consider a 4-to-1 Mux that functions as follows:

Input A is connected to the output Out when  $S_1S_0 = 00$ 

Input B is connected to the output Out when  $S_1S_0 = 01$ 

Input C is connected to the output Out when  $S_1S_0 = 10$ 

Input D is connected to the output Out when  $S_1S_0 = 11$ 

Fill in the table below for Out: (You only have ONE try for this problem)

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