

- Courseware (/courses/UTAustinX/UT.6.01x/1T2014/courseware)
- Course Info (/courses/UTAustinX/UT.6.01x/1T2014/info)
- Discussion (/courses/UTAustinX/UT.6.01x/1T2014/discussion/forum)
- Progress (/courses/UTAustinX/UT.6.01x/1T2014/progress)
- Questions (/courses/UTAustinX/UT.6.01x/1T2014/a3da417940af4ec49a9c02b3eae3460b/)
- Syllabus (/courses/UTAustinX/UT.6.01x/1T2014/a827a8b3cc204927b6efaa49580170d1/)

In this video we present the basics of serial communication. Each UART interface has a data register, a 16-element FIFO and a shift register.

VIDEO 11.2B. UART OPERATION



DR. JONATHAN VALVANO: The essence of the universal receiver transmitter is that the data is sent serially. That means one bit at a time. The idle condition is a high or a 3.3 volts. And then the data, when it wants to send, begins with a start bit which is low, which is always a level 0. And then the bits are sent 0, 1, 2, 3, 4, 5, 6,

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