

UTAustinX: UT.6.01x Embedded Systems - Shape the World

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The chapter covers the purpose of parallel ports, how to program them using memory-mapped I/O and initialization rituals. We will learn how to access I/O registers in a friendly manner. We will test the system by single-stepping in the simulator, and we will observe the running system using a logic analyzer.

## **Learning Objectives:**

Help

- Know what is a parallel port
- Know how a pin can be either input or output as specified by the direction register
- Know the steps required to initialize a parallel port
- Know how to access I/O registers in a friendly manner
- Know how to read data from an input port
- Know how to write data to an output port
- Know how to use the logic analyzer in the simulator

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