1. DR. RAMESH YERRABALLI: So we looked at the two steps.
2. One is the initialization step, which is what this routine is doing.
3. The initialization ritual, which we do one, which is a 13 step sequence.
4. You can look at all the details here.
5. We talked about it.
6. This part here is simply making the GPI pin be an alternate pin and an input.
7. So this is saying input.
8. We did the clock.
9. We did the alternate bit.
10. And then we did select, and we enabled (Disable digital) it.
11. Now, the second part is we turn on the clock for the ADC.
12. We wait for a little bit too for the clock to get going.
13. We set the sampling rate.
14. We set the priority of the sequencer.
15. We disable it and enable it between these two steps.
16. And while we are configuring it, the steps involved
17. are, what is the trigger we're using?
18. We're using software as a trigger.
19. We are saying, I'm using channel PE2, which is analog in one(AIN1).
20. And we made sure that IE0 is set to one.
21. DR. JONATHAN VALVANO: I have a question, Professor Yerraballi.
22. Why did you disable the digital input?
23. DR. RAMESH YERRABALLI: Oh, that is correct.
24. In fact, what we did for the D-enable is we turned it off.
25. DR. JONATHAN VALVANO: Yeah, but why?
26. DR. RAMESH YERRABALLI: That is because it's not digital.
27. It's analog.
28. DR. JONATHAN VALVANO: Ah, yes.
29. You're right.
30. DR. RAMESH YERRABALLI: All right.
31. So let's take a look at our second routine, which is our routine
32. where we're actually going to be read the data.
33. So this is a data read routine.
34. This is when there is a sample, and you're ready to read it.
35. And this routine has been called.
36. This routine's responsibility is to tell the ADC module
37. that you are ready to read it, which is our first step.
38. OK.
39. Let's take a look at the read routine.
40. We've already seen it, so I'm going to summarize it.
41. We start.
42. And once we get the sampling going, we'll
43. keep checking to see if it is done.
44. So you're we're going to check the status bit.
45. And if the status bit says that it's Busy,
46. then we keep going back and keep checking it.
47. And eventually, it's going to say it's done.
48. And once it's done, we're going to read the data.
49. And we're going to clear the bit.
50. So that's exactly what you see here.
51. This is a start.
52. We have a loop here, which repeatedly checks the status.
53. Then we come out of this.
54. We read the data, which is right here in the FIFO3.
55. And the last step is we cleared the bit, which is right here.
56. And we return the results.
57. So we return.
58. DR. JONATHAN VALVANO: All right.
59. Shall we see if it works?
60. DR. RAMESH YERRABALLI: Let's do it.
61. DR. JONATHAN VALVANO: All right.
62. So we have a analog signal connected up to PE2.
63. So to test it, we ill use this main program.
64. And we will ask the ADC converter to capture the input
65. and store it into this variable.
66. All right.
67. Let's go.
68. Build.
69. Download.
70. Debug.
71. Let's look at the watch window.
72. And in this watch window, we have the variable
73. that we're going to set every time we sample.
74. So let's hit the Go button.
75. In this watch window, we can see the results of the ADC converter.
76. That's a 12-bit number.
77. So Professor Yerraballi, make it go smaller.
78. DR. RAMESH YERRABALLI: So I'm sliding the slide-pot.
79. And it's moving it to the right.
80. And I moved it as right as I can.
81. DR. JONATHAN VALVANO: Oh, that's a small number.
82. DR. RAMESH YERRABALLI: And it's a small number.
83. DR. JONATHAN VALVANO: OK.
84. The ADC converter goes from zero to, what number does it go to?
85. Let's see.
86. Ooh, bigger.
87. DR. RAMESH YERRABALLI: Bigger.
88. DR. JONATHAN VALVANO: Bigger.
89. DR. RAMESH YERRABALLI: I'm moving it.
90. DR. JONATHAN VALVANO: Come on, faster.
91. No, not so fast.
92. Slow it down.
93. There we go.
94. And the largest number is 4,095.
95. There we are, 12 bit converter.
96. Now, you try it.

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