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## SKILLS SUMMARY

- Programming Languages  
C, Python,  
Java, JavaScript,  
ARM assembly,  
Verilog HDL
- Frameworks  
Django, Django Rest,  
React JS, jQuery
- Database System  
MongoDB, MySQL,  
SQLite, PostgreSQL
- IOT  
Arduino, Raspberry Pi
- Software  
AWS, Proteus,  
MATLAB, AutoCAD

## CERTIFICATIONS

- AWS Fundamentals: Going  
Cloud-Native | Coursera
- Project Management: The  
Basics for Success |  
Coursera
- Python | HackerRank

## INTERESTS

- Web Development
- Mobile Development
- Machine Learning
- IOT

## REFERENCE

Prof. Roshan G. Ragel  
Professor  
Dept. of Computer Engineering  
Faculty of Engineering  
University of Peradeniya  
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Dr. Asitha Bandaranayake  
Senior Lecturer  
Dept. of Computer Engineering  
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# KARIKARAN VETTIVEL

## COMPUTER ENGINEERING UNDERGRADUATE

### PERSONAL PROFILE

I am a dedicated, hardworking, and self-motivated individual with the willingness to explore new things in the field of the computer industry. I am seeking a position where my skills can utilize and get experiences as a trainee to become a successful professional in the industry.

### EDUCATIONAL HISTORY

**BSc. in Computer Engineering | Nov 2017 Present**

University Of Peradeniya | Current GPA : 3.34/4.00

### PROJECTS

#### Smart Vending Machine

Purchasing products from a web application and receiving products in a vending machine [GitHub Repo](#)

*Contribution : Backed development , Web API*

*Technologies : Django, HTML, AWS, JavaScript, Python, Django Rest*

#### Institute Management and Marketing Platform

A web application that is managing institute staff and students details and market their services [GitHub Repo](#)

*Technologies : Django , PostgreSQL, JavaScript, HTML, CSS, Heroku*

#### Car Rental Platform

A web application that is managing car rental store

*Contribution : Database connectivity*

*Technologies : PHP, HTML, CSS, AWS, MySQL*

#### Calendar Event Scheduling System

Calendar events scheduling stand alone software [GitHub Repo](#)

*Technologies : Python*

#### ARM Processor Implementation

An ARM architecture based processor was implemented with ALU, register file, control unit, data memory, data cache, instruction memory and instruction cache [GitHub Repo](#)

*Technologies : Verilog HDL*

#### Auction Server in Java

Implemented a sample server which can be used by clients to bid for items in a stock exchange

*Contribution : UI design and Sockets programming*

*Technologies: Threads, Synchronization primitives, Sockets, OOP*

#### Simple Computer System (ALU)

Digital Design project to build Arithmetic logic unit

*Contribution : Logic circuit design*

*Technologies : Proteus software*

### EXTRA CURRICULAR ACTIVITIES

**Department Coordinator** - IET On Campus, University of Peradeniya

**Project Chairperson** - Rotaract Club of University of Peradeniya

**Member** - ACES, University of Peradeniya