

# CSE312: Electronic Design Automation Fall 2022

# Project (2): Verilog Linting (DetErr the Linter)

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## <u>Parser</u>

```
static String[] ParseVerilogCode(String filename) throws FileNotFoundException {
    File file = new File(filename);
    String[] line = new String[100];
    int i = 0;
    Scanner scanner = new Scanner(file);
    while (scanner.hasNextLine()) {
        line[i] = scanner.nextLine();
        i++;
    }
    return line;
}
```

## **Static Checker Engine**

#### Main

Note: We have added GUI to make the experience even better! Check the GUI code after the static engine checker code.

#### Arithmetic overflow function

```
static void arithmeticOperation(String[] line, FileWriter fw) throws IOException {
    String[] variablesArray = new String[100];
    String[] numbersArray = new String[10000];
    char[] chararray1 = new char[10];
    char[] chararray2 = new char[10];
    int indexVariablesArray = 0;
    int indexNumbersArray = 0;
    String[] tempt2 = new String[line.length];
    String[] temp = new String[line.length];
         tempt2[0] += line[\underline{i}];
    temp = tempt2[0].split( regex: " ");
         \underline{\text{temp}}[\underline{i}] = \underline{\text{temp}}[\underline{i}].\text{replaceAll(} \text{regex: } "[\\[\\]()\{\},;]", \text{ replacement: } "");
         if (temp[m].contains("'b")) {
              temp[m] = temp[m].substring(3, temp[m].length());
              variablesArray[indexVariablesArray] = temp[m - 2];
              numbersArray[indexNumbersArray] = temp[m];
              indexVariablesArray++;
```

```
indexNumbersArray++;
int counterrros = 0;
fw.append("Arithmetic overflow:\n");
int indexoffirst = 0, indexofsecond = 0;
int r1 = 0, r1Max = 0, r2Max = 0, r2 = 0, carry = 0;
for (int overflow = 0; overflow < temp.length; overflow++) {</pre>
    if (temp[overflow].contains("+")) {
         for (int k = 0; k < variablesArray.length; <math>k++) {
              if (temp[overflow - 1].equals(variablesArray[k])) {
                  indexoffirst = k;
                  chararray1 = numbersArray[indexoffirst].toCharArray();
             if (temp[overflow + 1].equals(variablesArray[k])) {
                  indexofsecond = k;
                  chararray2 = numbersArray[indexofsecond].toCharArray();
         String result = null;
         for (int \underline{i} = 0; \underline{i} < \underline{chararray1}.length; \underline{i}++) {
              if (chararray1[chararray1.length - 1 - \underline{i}] == '1') {
                  \underline{r1} += Math.pow(2, \underline{i});
             \underline{r1Max} += Math.pow(2, \underline{i});
         for (int j = 0; j < chararray2.length; j++) {
```

```
for (int j = 0; j < characray2.length; j++) {
    if (characray2(characray2.length - 1 - j] == '1') {
        p2 += Math.pow(2, j);
    }
    c2Max += Math.pow(2, j);
}

if ((int j = 0; j < characray2.length - 1 - j] == '1') {
        p2 += Math.pow(2, j);
}

if ((int j = 0; Math.mox(riMax, r2Max))) {
    int of = 0;
    white (tine[or] != mult) {
        if (tine[or].contains("+") && tine[or].contains(variablesArray[indexoffirst]) &&
        tine[or].contains(variablesArray[indexoffirst]) &&
        tine[or].contains(variablesArray[indexoffirst]) &&
        tine[or].contains(variablesArray[indexoffirst]) &&
        tine[or].contains(variablesArray[indexoffirst]) &&
        tine[or].contains(variablesArray[indexoffirst]) &&
        tine[or].contains(variablesArray[indexoffirst]) &&
        tine[or].contains(variablesArray[indexoffirst]) &&
        tine[or].contains(variablesArray[indexoffirst]) &&
        tine[or].contains(variablesArray[indexoffirst]) &&
        tine[or].contains(variablesArray[indexoffirst]) &&
        tine[or].contains(variablesArray[indexoffirst]) &&
        tine[or].contains(variablesArray[indexoffirst]) &&
        tine[or].contains(variablesArray[indexoffirst]) &&
        int tine[or].
```

#### Unreachable FSM State function

```
static void UnreachableStateChecker(String[] line , FileWriter fw){
                    if (line[j].indexOf( str. " ", fromIndex k+1)< line[j].indexOf( str. "=", fromIndex k+1))
```

```
counter=0;
    fw.append("FSM States: \n");
    int flag = 0;
    for (int s = 0; states[s]!=null; s++){
        if(statesFlags[s]==1){
            fw.append("\t");
            fw.append(states[s]);
            fw.append(" initialized in line ");
            fw.append((char) statesLoc[s]);
            fw.append(" is never reached.\n");
            flag++;
    if (flag == 0)
        fw.append("\t No unreachable states.\n");
    fw.append("\n");
catch (IOException e){
    System.out.println("IO Error!");
```

#### **Un-initialized Register function**

```
static void unitiliazedRegs(String []line, FileWriter fw) throws IOException {
    String[] plusRegs = new String[100];
    String[] tempt2 = new String[line.length];
         if (temp[t].equals("=") || temp[t].equals("<=")) {</pre>
    for (int \underline{m} = 0; \underline{m} < \underline{\text{temp}}.length; \underline{m} + +) {
         if (temp[m].contains("reg")) {
             regs[indexRegs] = temp[m + 1];
    Н
         for (int j = 0; j < plusRegs.length; j++) {
                  if (regs[i].equals(plusRegs[j]) || line[i].contains(plusRegs[j])) {
```

#### Multi-Driven Bus/Register function

```
static void CheckMultipleBusses(String [] string, FileWriter fw) throws IOException{
String [][]intialw= new String[2][100];
String [][]alwaysg= new String[2][100];
int ww=0;
int w=0;
int al=0;
int all=0;
String []words=new String[10000];
String []linesplits=new String[10000];
String []errors=new String[10000];
int CountErrors=-1;
int i=0;
int j=0;
int countintial2=0;
int countalways2=0;
while(string[i]!=null) {
    string[i]=string[i].trim();
while(string[i]!=null){
    string[i]=string[i].trim();
    for(String a: linesplits){
        words[j]=a;
for(int z=0;z<words.length;z++){</pre>
    if(words[z]==null) break;
    if((words[z].length()==0) || (words[z].length()==1)){
            words[f]=words[f+1];
```

```
words[f]=words[f+1];
for(int z=0;z<words.length;z++){</pre>
    words[z]=words[z].replace( target "(", replacement "");
    words[z]=words[z].replace( target ")", replacement "");
    words[z]=words[z].replace( target "@", replacement "");
    words[z]=words[z].replace( target "{", replacement "");
    words[z]=words[z].replace( target "}", replacement "");
    words[z]=words[z].replace( target ",", replacement "");
    if(words[z]==null) break;}
    catch(Exception e){
for(int y=0;y<1000;y++){
    if((words[y]).equals("initial")){
        for(int g=y+2;g<1000;g++){
             if(words[g].equals("if")) countintial++;
             if(words[g].equals("end")) {
                 if(countintial==0) break;
                countintial--;
             Н
             if(words[g].equals("case"))break;
             if(words[g].equals("if"))continue;
             if(words[g].contains("'")) continue;
             if(words[g].contains("#")) continue;
             if(words[g-1].equals("<="))continue;
             if(words[g-1].equals("="))continue;
             if(words[g+1].equals("<="))continue;</pre>
             if(words[g].equals("end"))continue;
             if(words[g].equals("begin"))continue;
             if(words[g].equals("="))continue;
             if(words[g].equals("<="))continue;
             if(words[g].equals("else"))continue;
             if(words[g].equals("&&"))continue;
             intialw[ww][w]=words[g];
             <u>w</u>++;
```

```
w=0;
    for(int g=y+2;g<1000;g++){</pre>
        if(words[g].equals("end")) {
            if(countalways==0) break;
        if(words[g].equals("case"))break;
        if(words[g].equals("if"))continue;
        if(words[g].contains("'")) continue;
        if(words[g-1].equals("<="))continue;
        if(words[g-1].equals("="))continue;
        if(words[g+1].equals("<="))continue;</pre>
        if(words[g].equals("end"))continue;
        if(words[g].equals("="))continue;
        if(words[g].equals("<="))continue;</pre>
        if(words[g].equals("else"))continue;
        if(words[g].equals("&&"))continue;
        alwaysq[all][al]=words[g];
    <u>al</u>=0;
catch(Exception e){
```

```
}*/
for(int yy=0;yy<2;yy++){</pre>
     for(int zz=yy;zz<2;zz++){</pre>
          for(int <u>uu</u> =1;<u>uu</u><50;<u>uu</u>++){
               for(int <u>ii=vv;ii</u><50;<u>ii</u>++){
                    if(alwaysq[yy][ii]==null)continue;
                    if(<u>ii</u>==<u>uu</u> && <u>yy</u>==<u>zz</u>)continue;
                         if(zz==yy){CountErrors++;
                              firstdone=0;
                                   if(string[x].equals("initial")) break;
                                   if(string[x].contains("always")) ++countalways;
```

```
System.out.println();
countalways2=0;
for(int <u>qq</u>=0;<u>qq</u><100;<u>qq</u>++){
              errors[CountErrors]+=gg+1;
```

```
for(int <u>ii=vv;ii</u><40;<u>ii</u>++){
     if(<u>ii</u>==<u>uu</u> && <u>yy</u>==<u>zz</u>)continue;
               firstdone=0;
                    if(string[x].equals("always"))break;
                    for(int gg=0;gg<10000;gg++){
                        if(countintial2==zz+1 || countintial2==yy+1){
```

```
if(string[gg].contains(intialw[zz][uu])) {
                                         errors[<u>CountErrors</u>]+=gg+1;
                                         firstdone++;
                                         if(firstdone==2)break;
                                         if(firstdone==1){
                                             errors[CountErrors]+=" and ";
                catch(Exception e){
CountErrors=0;
    fw.append("Multi-driven buses/registers:\n");
       fw.append("\t" + errors[CountErrors++] + " \n");
    fw.append("\n");
```

#### Non-Full/Parallel Case function

```
static void CheckParallelFullCase(String [] lines, FileWriter fw)throws IOException{
   String [] linesplit = new String[100000];
   String [] Errors = new String[100000];
       for(String a:linesplit){
           caseindex[k]=i;
       words[caseindex[i]+5].equals("full_case") ||
```

```
j=caseindex[i];
    maxout=c;
String [] initializations = new String[100000];
j=caseindex[i];
    initializations[c] = words[j-1];
    maxin=c;
    if(j<0)break;
int flag=0;
for(int m=0;m<maxout;m++){</pre>
    flag=0;
    for(int b=0;b<maxin;b++){</pre>
```

```
if(outputs[m].equals(initializations[b])){
              flag=1;
words[caseindex[i]+5].equals("parallel_case")||
String [] cases = new String[100000];
         cases[c] = words[j-1];
for(int <u>o</u>=0;<u>o</u><<u>c</u>;<u>o</u>++){
```

# **GUI CODE (includes function that outputs in text file)**

```
public class NewJFrame1 extends javax.swing.JFrame {
   String RepDir:
   public NewJFrame1() { initComponents(); }
   public void setTextBox2(String s) { jTextField2.setText(s); }
   /unchecked/
   private void jButton2ActionPerformed(java.awt.event.ActionEvent evt) {//GEN-FIRST:event_jButton2ActionPerformed
```

```
String [] line = new String[100000];
    Logger.getLogger(NewJFrame1.class.getName()).log(Level.SEVERE, | msg: null, ex);
while (scanner.hasNextLine()) {
    fw = new FileWriter(RepDir);
} catch (IOException ex) {
    Logger.getLogger(NewJFrame1.class.getName()).log(Level.SEVERE, msg: null, ex);
        arithmeticOperation(line, fw);
    Logger.getLogger(NewJFrame1.class.getName()).log(Level.SEVERE, msg: null, ex);
    UnreachableStateChecker(line, fw);
} catch (IOException ex) {
    Logger.getLogger(NewJFrame1.class.getName()).log(Level.SEVERE, msg: null, ex);
        CheckMultipleBusses(line, fw);
        CheckParallelFullCase(line, fw);
        Logger.getLogger(NewJFrame1.class.getName()).log(Level.SEVERE, msg: null, ex);
```

```
fw.close();
       Logger.getLogger(NewJFrame1.class.getName()).log(Level.SEVERE, msg: null, ex);
   Done d = new Done( f this);
private void jCheckBox5ActionPerformed(java.awt.event.ActionEvent evt) {//GEN-FIRST:event_jCheckBox5ActionPerformed
    // TODO add your handling code here:
private void jButton1ActionPerformed(java.awt.event.ActionEvent evt) {//GEN-FIRST:event_jButton1ActionPerformed
private void jButton3ActionPerformed(java.awt.event.ActionEvent evt) {//GEN-FIRST:event_jButton3ActionPerformed
private void jCheckBox4ActionPerformed(java.awt.event.ActionEvent evt) {//GEN-FIRST:event_jCheckBox4ActionPerformed
    // TODO add your handling code here:
```

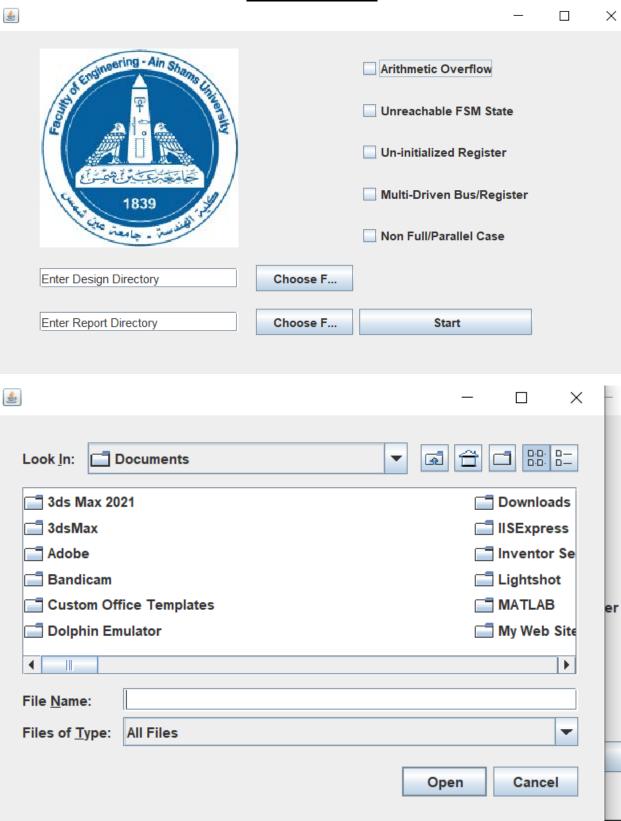
```
public static void main(String args[]) {
  java.awt.EventQueue.invokeLater(new Runnable() {
        public void run() { new NewJFrame1().setVisible(true); }
private javax.swing.JButton jButton1;
private javax.swing.JButton jButton2;
private javax.swing.JButton jButton3;
private javax.swing.JCheckBox jCheckBox4;
private javax.swing.JLabel jLabel1;
private javax.swing.JTextField jTextField1;
private javax.swing.JTextField jTextField2;
```

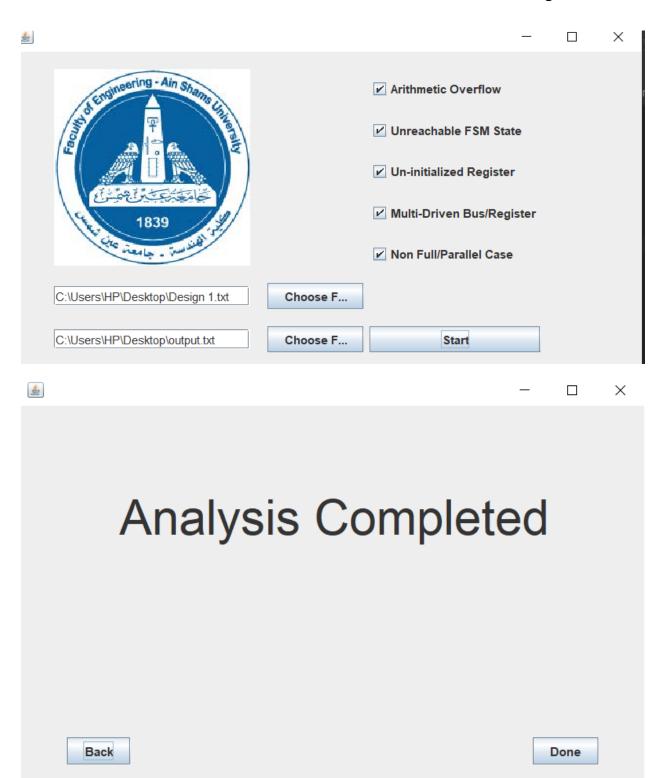
```
String Directory;
public static void main(String args[]) {
```

```
public NewJFrame11(String dir) {
public NewJFrame11(NewJFrame1 f1) {
public static void main(String args[]) {
H
```

```
public class Done extends javax.swing.JFrame {
   public Done(NewJFrame1 f) {
   private void jButton1ActionPerformed(java.awt.event.ActionEvent evt) {//GEN-FIRST:event_jButton1ActionPerformed
       // TODO add your handling code here:
   public static void main(String args[]) {
   private javax.swing.JButton jButton1;
   private javax.swing.JButton jButton2;
    private javax.swing.JLabel jLabel1;
```

# **GUI SCREENS**





## **Designs used**

#### Design 1 (code and output)

```
module Up Dn Counter (
 2
        input
              wire [4:0]
                                IN,
 3
                                Load, Up, Down,
                wire
        input
 4
        input wire
                                clk,
 5
        output reg
                      [4:0]
                                Counter,
 6
        output wire
                                High, Low
 7
        input in1,
 8
        input [3:0] X = 4'b1111,
 9
        input [3:0] Y = 4'b1111,
10
        output [3:0] Z,
11
       output reg out,
12
        output reg out3,
13
        output reg out4
14
       );
15
16
       z = x + y;
17
      initial
18
       begin
19
       Load = 1'b0;
20
        Up = 1'b1;
21
       end
22
      initial
23
        begin
24
        Load = 1'b1;
25
        Up = 1'b0;
26
      end
27
28
        always @ (posedge clk)
29
         begin
30
           if (Load)
31
             begin
32
              Counter <= IN ;
33
             end
34
           else if (Down && !Low)
35
             begin
36
              Counter <= Counter - 5'b1;
37
             end
```

```
COUNTED A COUNTED
              end
  38
            else if (Up && !High)
  39
             begin
               Counter <= Counter + 5'b1;
 40
 41
              end
           case (in1)// synopsis parallel_case
  42
 43
           1'b1 : out <= 1,
 44
                 out3 <= 0,
  45
                out4 <= 1;
           1'b1 : out <= 0,
 47
                out3 <= 1,
 48
                 out4 <= 1;
 49
         endcase
 50
           end
 51
  52
  53
      // Down flag
 54
        assign Low = (Counter == 5'b0);
 55
  56 // Up flag
  57
        assign High = (Counter == 5'b11111);
  58
 59
      endmodule
Arithmetic overflow:
        Arithmetic overflow at line 16 between X and Y
FSM States:
         No unreachable states.
Uninitialized registers:
        No uninitialized registers found.
Multi-driven buses/registers:
        Load MULTIDRIVEN in lines 19 and 24
        Up MULTIDRIVEN in lines 20 and 25
Not Full/Parallel Cases:
        not Full case at line 42
```

#### Design 2 (code and output)

module serializer (

```
input clk,
       input load,
       input inl,
      input [7:0] in value,
      output reg o_bit,
      output reg out,
      output reg out3,
 8
 9
10
      output reg out5
    );
11
12
      reg [7:0] internal reg ;
13
      always @ (posedge clk)
14
      begin
15
      if (load)
16
     begin
17
      internal_reg <= in_value ;
18
      end
19
      else
20
      begin
     o bit = internal_reg ;
21
22
      end
23
      end
24 always @ (posedge clk)
25 begin
26 load = 1'b0;
27 o_bit = 1'b1;
28 case (in1)
29 l'b1 : out
       1'b1 : out <= 1,
30
                out3 <= 0,
31
32
       out4 <= 1
1'b1 : out <= 0,
                out4 <= 1;
          out3 <= 1,
33
34
35
                out4 <= 1;
       default : out <= 1;
36
                  out3 <= 1,
37
38
                  out4 <= 1;
       endcase
39
      end
   end
endmodule
40
THE LUIT FORMAL VIEW THEIP
Arithmetic overflow:
        No arithmetic overflow found
FSM States:
         No unreachable states.
Uninitialized registers:
        out5 is not initialized at line 10
Multi-driven buses/registers:
        load MULTIDRIVEN in lines 15 and 26
        o bit MULTIDRIVEN in lines 21 and 27
Not Full/Parallel Cases:
        not parallel case at line 28
```

#### Design 3 (code and output)

```
//////// Moore FSM //////////
3
    4
5
   module LOCKER Moore (
6
   input wire button_0 , button_1 ,
7
   input wire
                   rst,
8
    input wire
                   clk,
   output reg
9
                    unlock
10
    );
11
12
13
14
   localparam [2:0]
                      IDLE = 3'b000,
15
                      S1 = 3'b001,
16
                      S11 = 3'b011,
17
                      5011 = 3'b010,
18
                      S1011 = 3'b110,
19
                      UNLOCK = 3'blll ;
20
21
   reg
         [2:0]
                      current state,
22
                      next_state ;
23
24 // state transition
25
   always @(posedge clk or negedge rst)
26
    begin
27
     if(!rst)
28
      begin
29
       current state <= IDLE ;
30
       end
31
      else
32
      begin
33
       current state <= next state ;
34
      end
35
     end
36
   // next state logic
37
   always @(*)
38
39
    begin
     case (current_state)
40
      IDLE : begin
41
               if(button 0)
42
```

```
42
                    if(button_0)
                     next_state = IDLE ;
43
44
                    else if (button 1)
45
                     next state = S1;
46
                    else
47
                     next_state = IDLE ;
48
49
        S1 : begin
50
                    if(button 0)
51
                    next state = IDLE ;
                    else if (button 1)
52
53
                     next state = S11;
54
                    else
55
                     next_state = S1 ;
56
57
        Sll : begin
58
                    if(button_0)
59
                     next state = S011 ;
60
                    else if (button 1)
61
                     next state = IDLE ;
62
                    else
63
                     next_state = S11 ;
64
                  end
65
        S011 : begin
66
                    if(button 0)
67
                    next state = IDLE ;
68
                    else if (button_1)
69
                     next_state = S1011 ;
70
                    else
71
                     next_state = S011 ;
72
                   end
73
        S1011 : begin
74
                   if(button 0)
75
                    next state = IDLE;
                   else if (button 1)
76
                     next_state = IDLE ;
77
78
79
                     next_state = S1011 ;
80
                  end
81
        UNLOCK : begin
```

```
UNLOCK : begin
 82
                 next_state = IDLE ;
 83
                  end
 84
        default : next state = IDLE ;
 85
 86
        endcase
     end
 87
 88
 89
     // next_state logic
always @(*)
 90
 91
     begin
 92
 93
        case (current_state)
 94
        IDLE : begin
 95
                   unlock = 1'b0;
 96
                  end
 97
        S1 : begin
98
99
100
                   unlock = 1'b0;
                  end
        Sll : begin
                   unlock = 1'b0;
102
                  end
103
        S011 : begin
104
                   unlock = 1'b0;
105
                  end
105
       S1011 : begin
107
                   unlock = 1'b0;
108
                  end
109
110
        S1011 : begin
                   unlock = 1'bl;
111
                  end
111 end
112 default : begin
113 unlo
114 end
115 endcase
                   unlock = 1'b0;
116
       end
117
118
119 endmodule
```

```
Arithmetic overflow:

No arithmetic overflow found

FSM States:

UNLOCK initialized in line [] is never reached.

Uninitialized registers:

No uninitialized registers found.

Multi-driven buses/registers:

No multi-driven busses/registers.

Not Full/Parallel Cases:

not parallel case at line 93
```