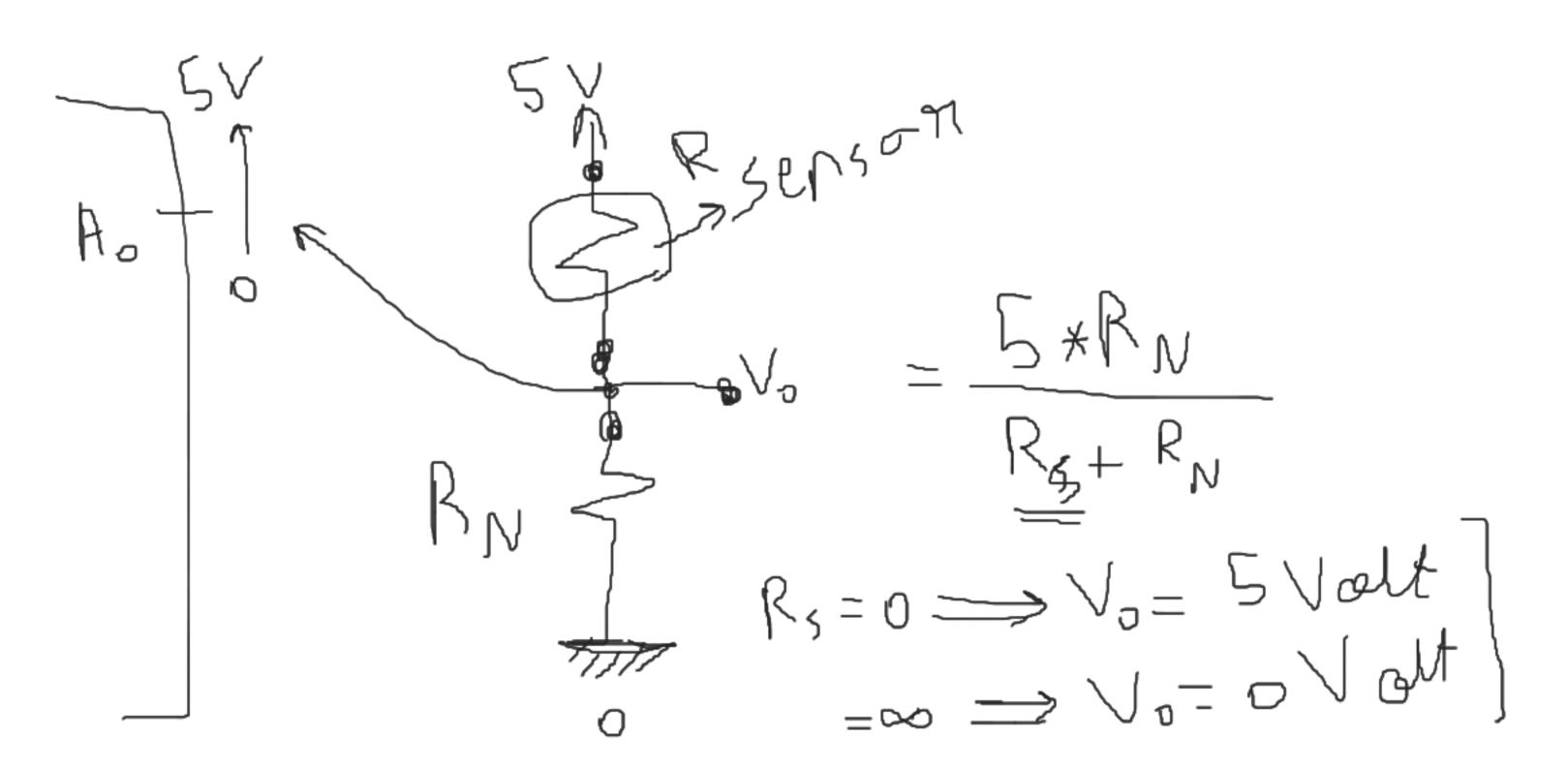
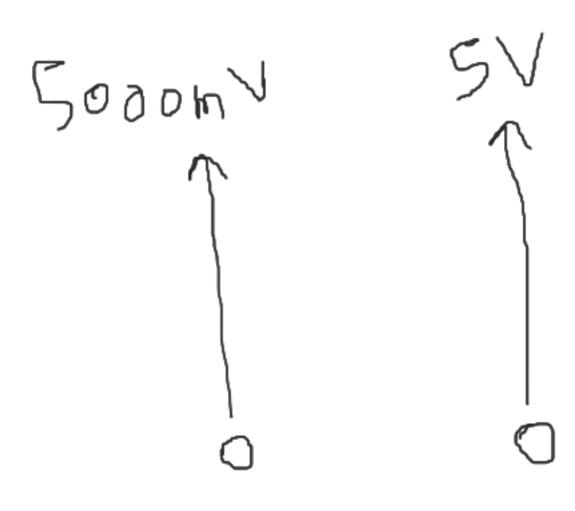


8 Bits



1_ DR: Pot: \Box \sim_0



MTC ext grem high Lam